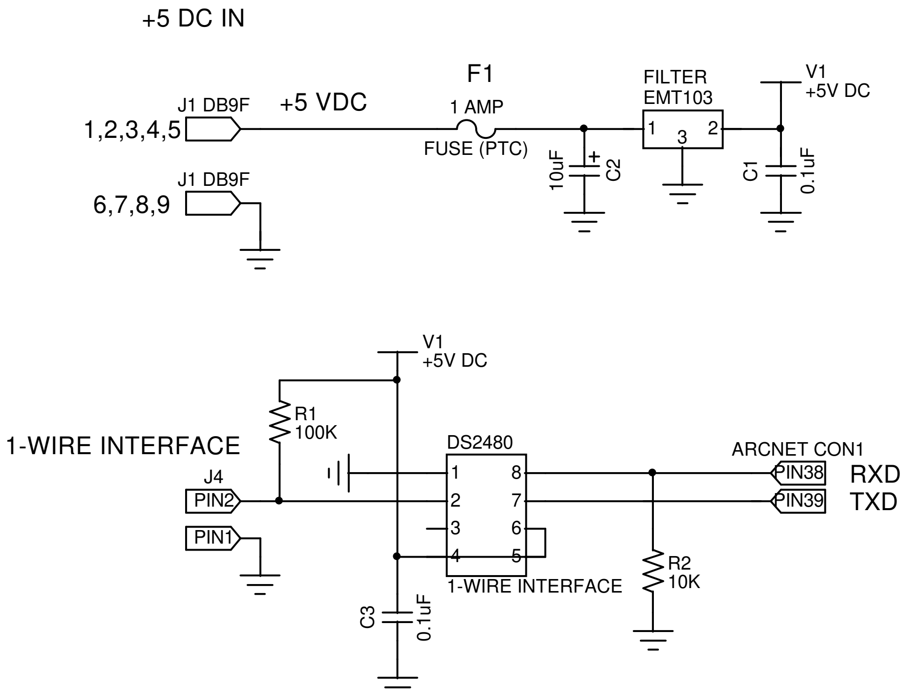


POWER CONNECTIONS AND DALLAS CHIP INTERFACE

| | | |
|---|---|---|
| Size: A | No: FEE ARCNET BOARD #1 | Rev: 36 |
| Sheet: 1 OF 4 | | Revised: 06-Nov-2000 |
| Drawn by: PLM | | Created: 03-Jun-1999 |
| File: C:\TsWin32\Circuits\farcnet1.sch | | PHENIX 105-0312-402 |