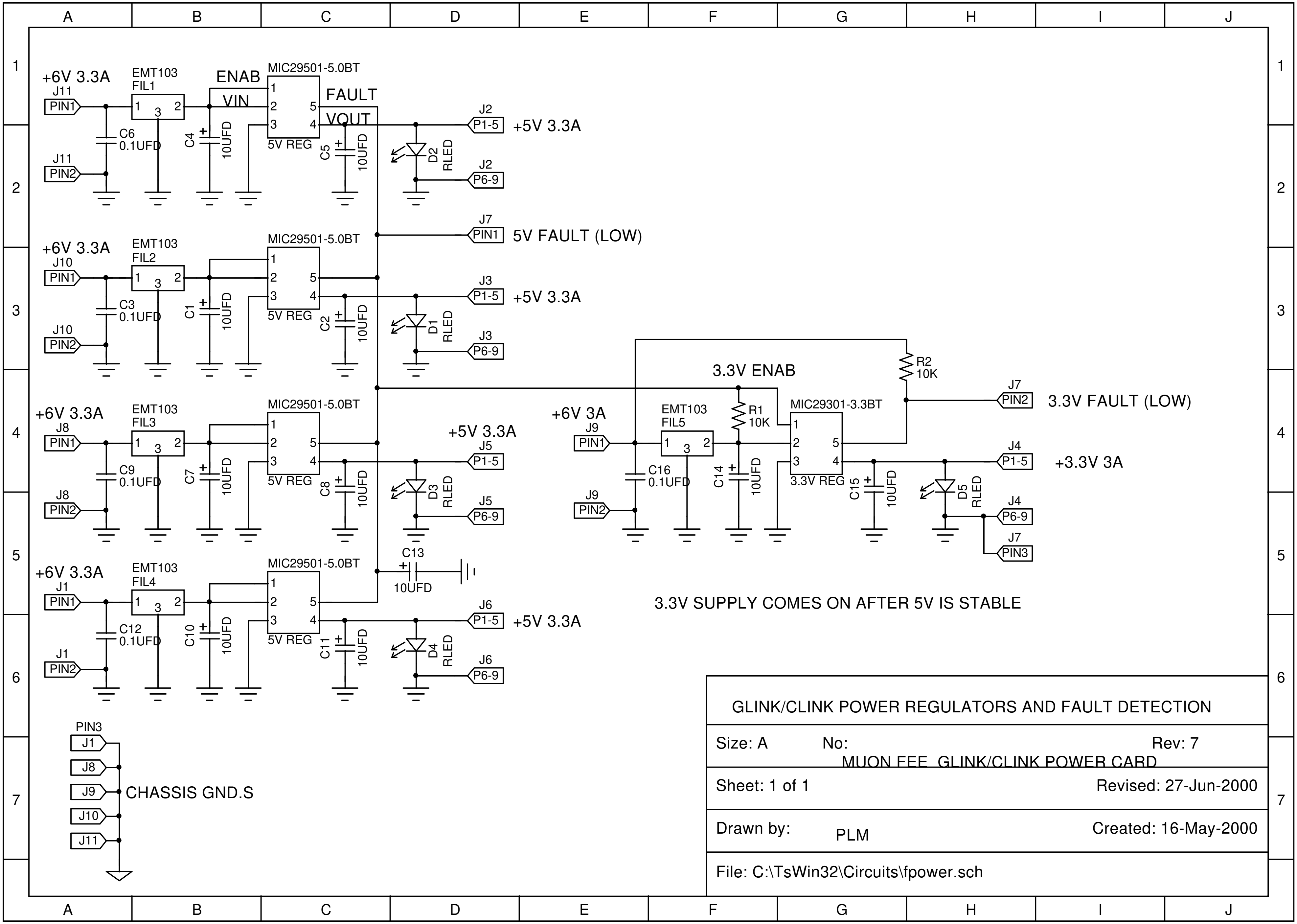
+6V 3.3A
EMT103 FIL1
ENAB
VIN
MIC29501-5.0BT
FAULT
VOUT
5V REG
C6 0.1UFD
C4 10UFD
C5 10UFD
D2 RLED
J2 P1-5
+5V 3.3A
J2 P6-9
J11 PIN1
J11 PIN2
J7 PIN1
5V FAULT (LOW)
EMT103 FIL2
J10 PIN1
J10 PIN2
C3 0.1UFD
C1 10UFD
C2 10UFD
D1 RLED
J3 P1-5
J3 P6-9
EMT103 FIL3
J8 PIN1
J8 PIN2
C9 0.1UFD
C7 10UFD
C8 10UFD
D3 RLED
J5 P1-5
J5 P6-9
EMT103 FIL4
J1 PIN1
J1 PIN2
C12 0.1UFD
C10 10UFD
C11 10UFD
C13 10UFD
D4 RLED
J6 P1-5
J6 P6-9
3.3V ENAB
R2 10K
R1 10K
J7 PIN2
3.3V FAULT (LOW)
+6V 3A
J9 PIN1
J9 PIN2
EMT103 FIL5
MIC29301-3.3BT
3.3V REG
C16 0.1UFD
C14 10UFD
C15 10UFD
D5 RLED
J4 P1-5
+3.3V 3A
J4 P6-9
J7 PIN3
3.3V SUPPLY COMES ON AFTER 5V IS STABLE
PIN3
J1
J8
J9
J10
J11
CHASSIS GND.S
GLINK/CLINK POWER REGULATORS AND FAULT DETECTION
Size: A
No:
Rev: 7
MUON FEE GLINK/CLINK POWER CARD
Sheet: 1 of 1
Revised: 27-Jun-2000
Drawn by:
PLM
Created: 16-May-2000
File: C:\TsWin32\Circuits\fpower.sch