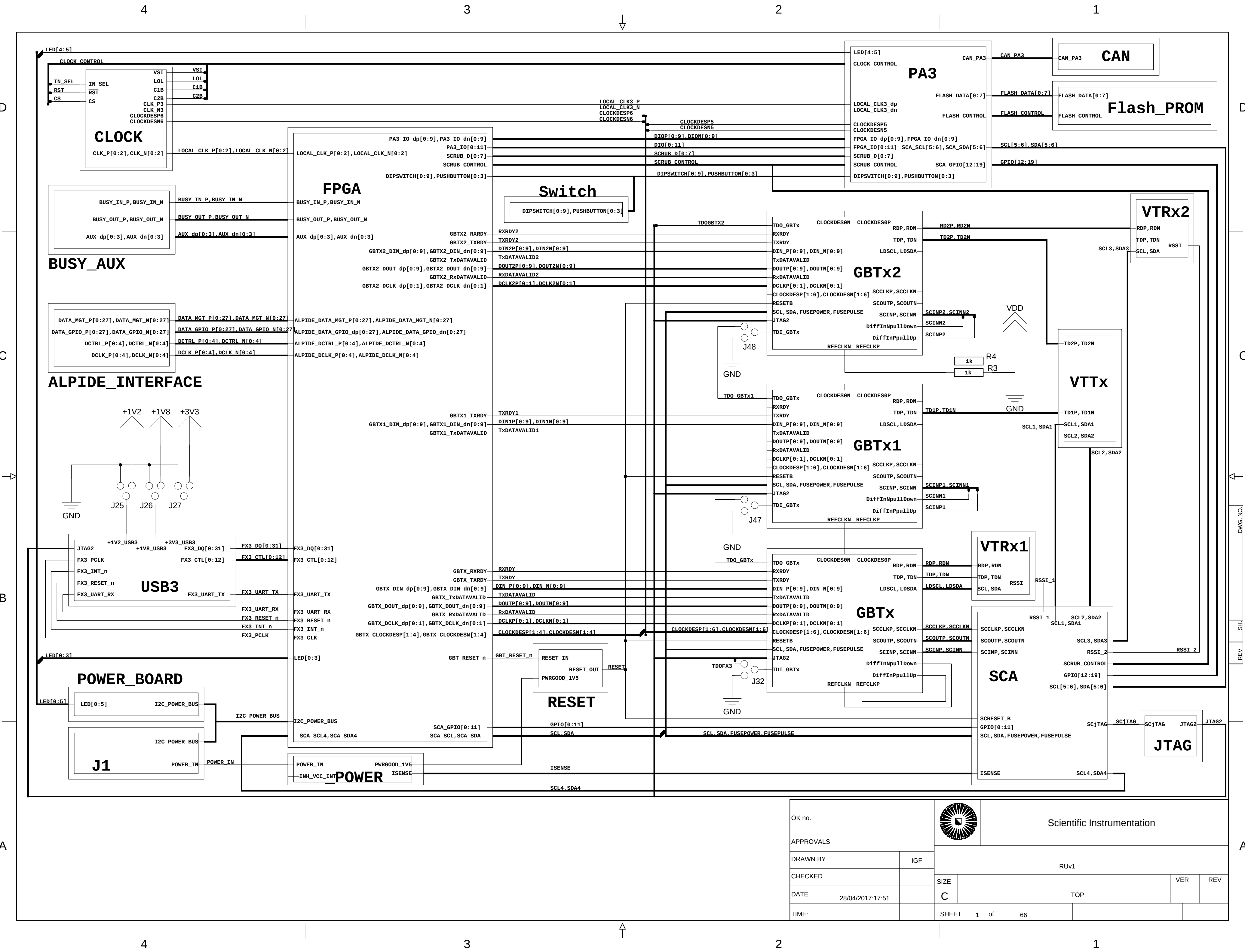
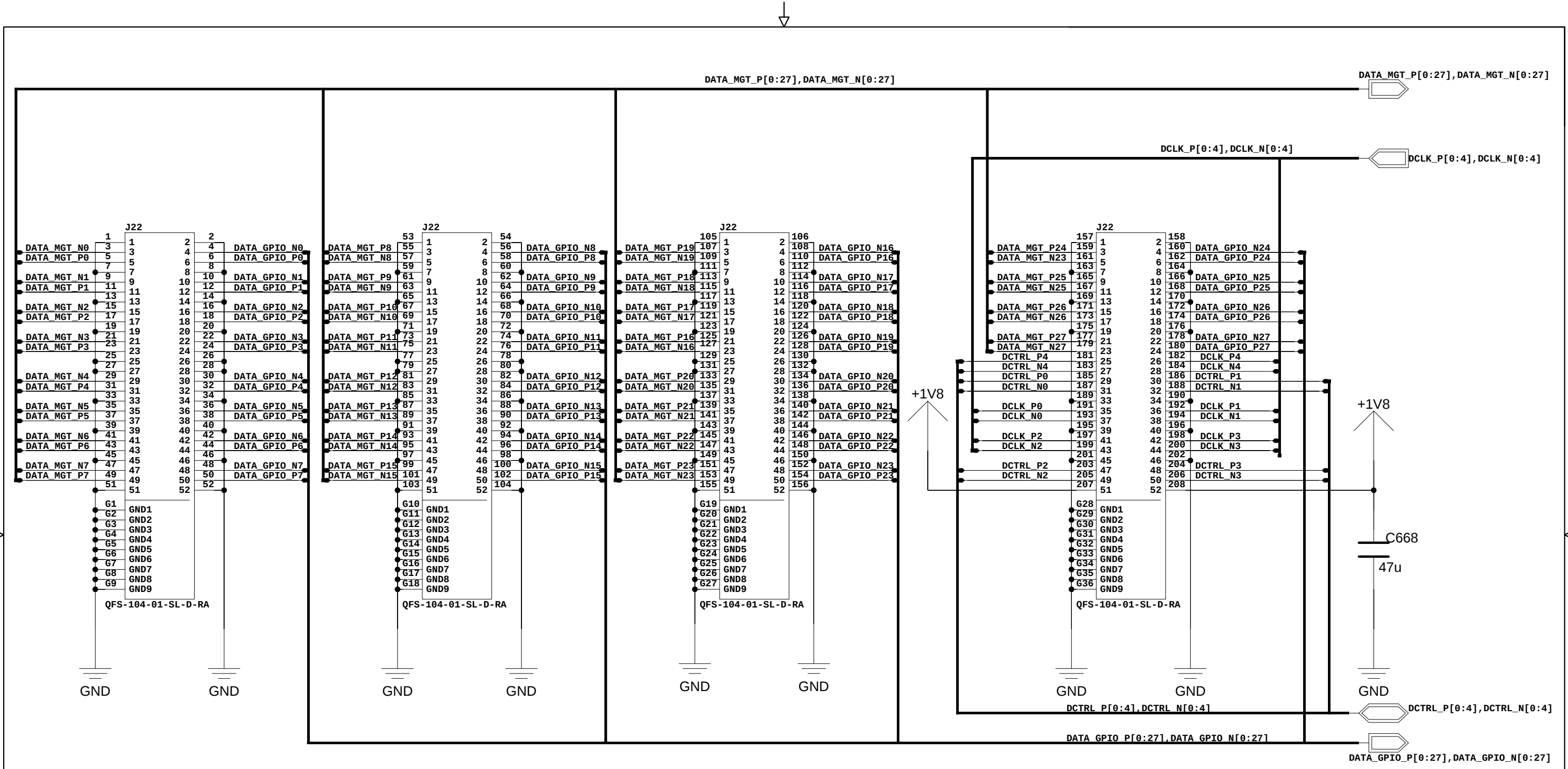
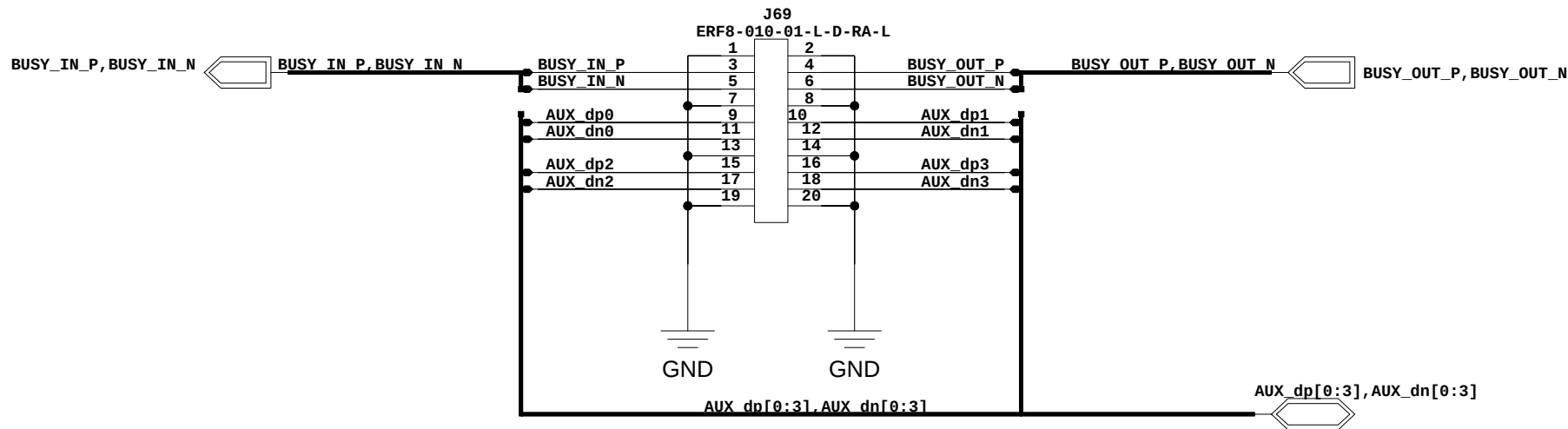




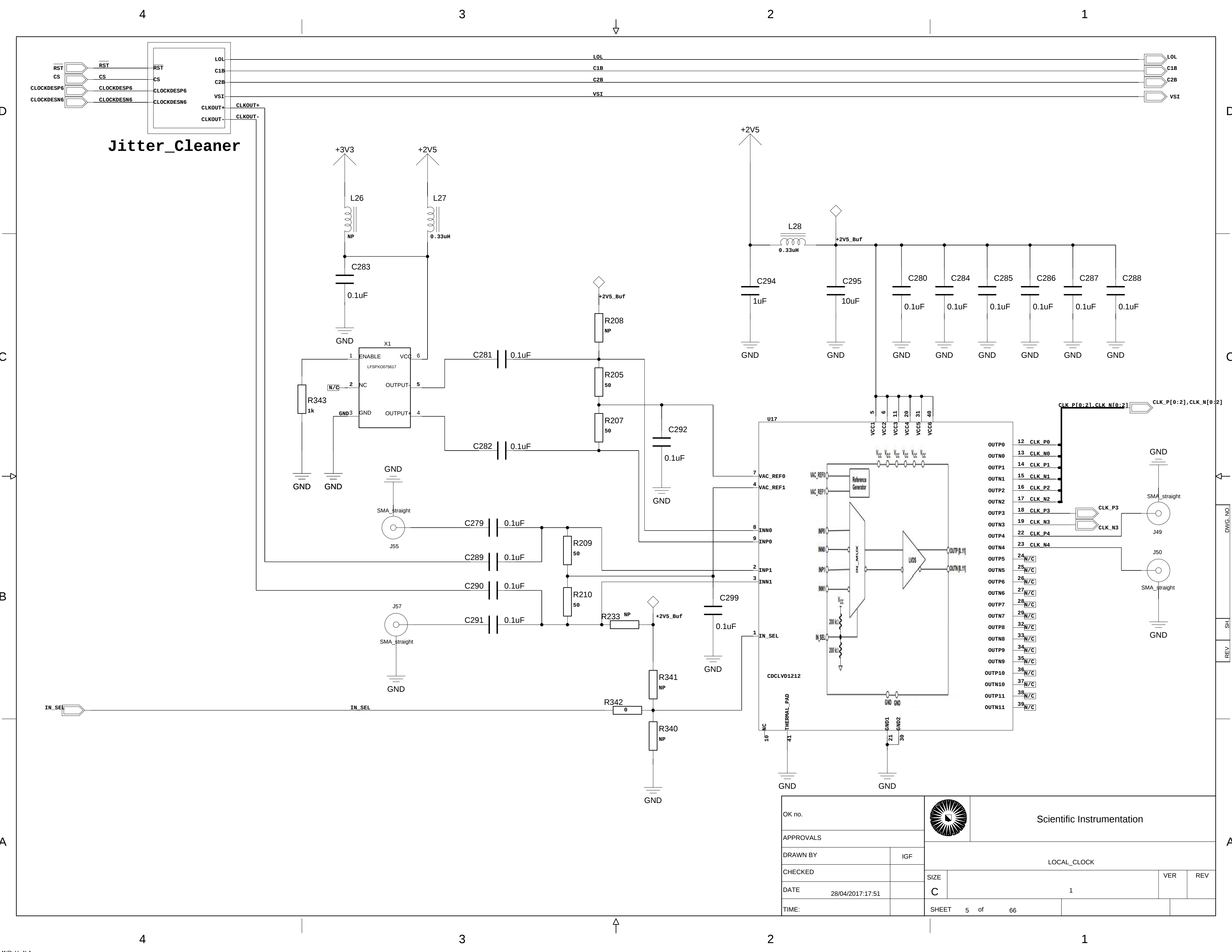
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APPROVALS						
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CHECKED						
DATE	28/04/2017:17:51	SIZE	TOP		VER	REV
		C				
TIME:		SHEET 1 of 66				



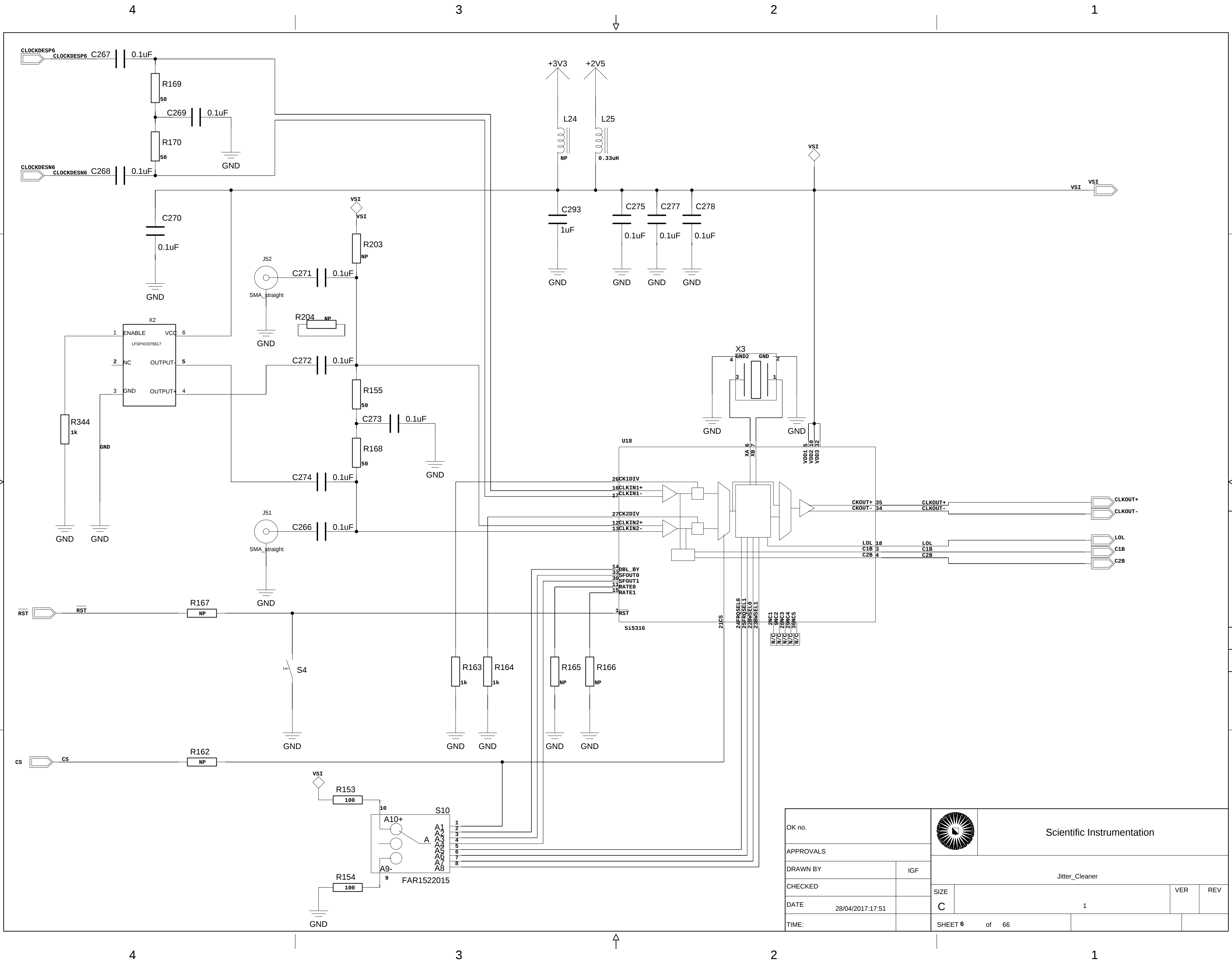
OK no.			Scientific Instrumentation				
APPROVALS							
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DATE 28/04/2017:17:51			SIZE B	ALPIDE_TRANSISTIONBOARD_INTERFACE		VER	REV
TIME:			SHEET 2 of 66				



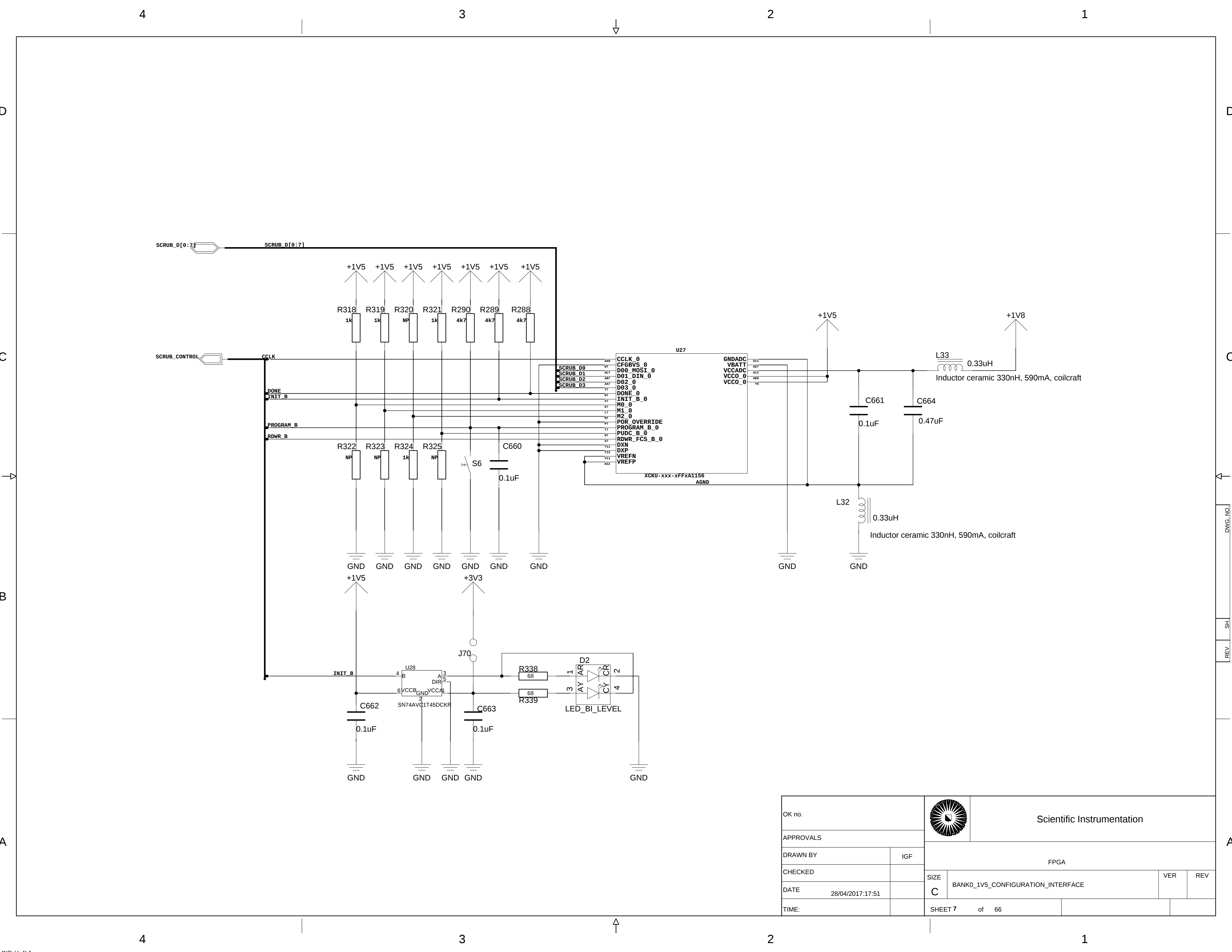
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TIME:		SHEET 3 of 66					



OK no.			Scientific Instrumentation		
APPROVALS					
DRAWN BY	IGF	LOCAL_CLOCK			
CHECKED		SIZE		VER	REV
DATE	28/04/2017:17:51	C	1		
TIME:		SHEET 5 of 66			



OK no.			Scientific Instrumentation					
APPROVALS								
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CHECKED								
DATE		28/04/2017:17:51		SIZE	1		VER	REV
TIME:				C				
		SHEET 6 of 66						



OK no.			Scientific Instrumentation					
APPROVALS								
DRAWN BY		IGF	FPGA					
CHECKED			BANK0_1V5_CONFIGURATION_INTERFACE			VER	REV	
DATE								
28/04/2017:17:51			C					
TIME:			SHEET 7 of 66					

D

C

B

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D

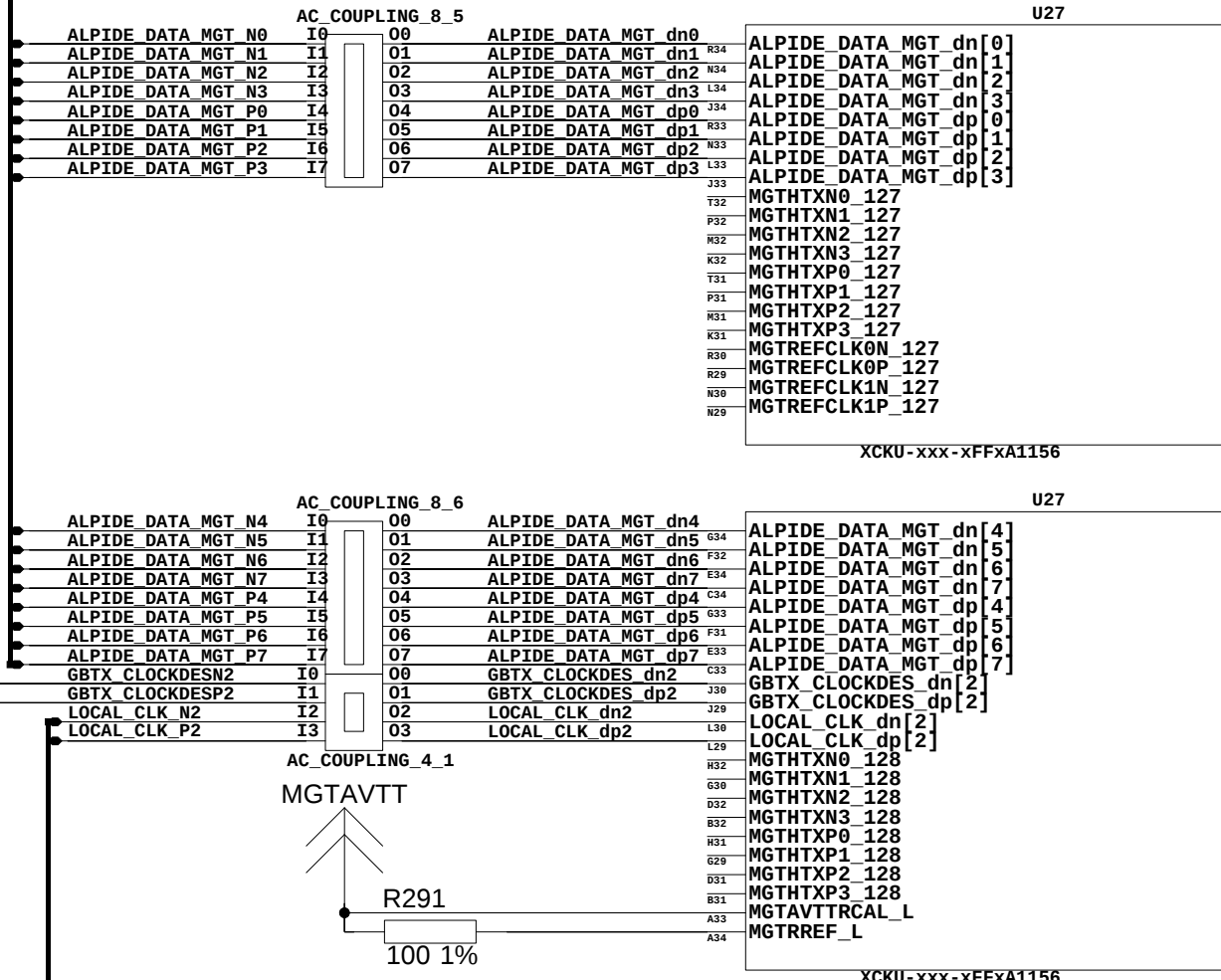
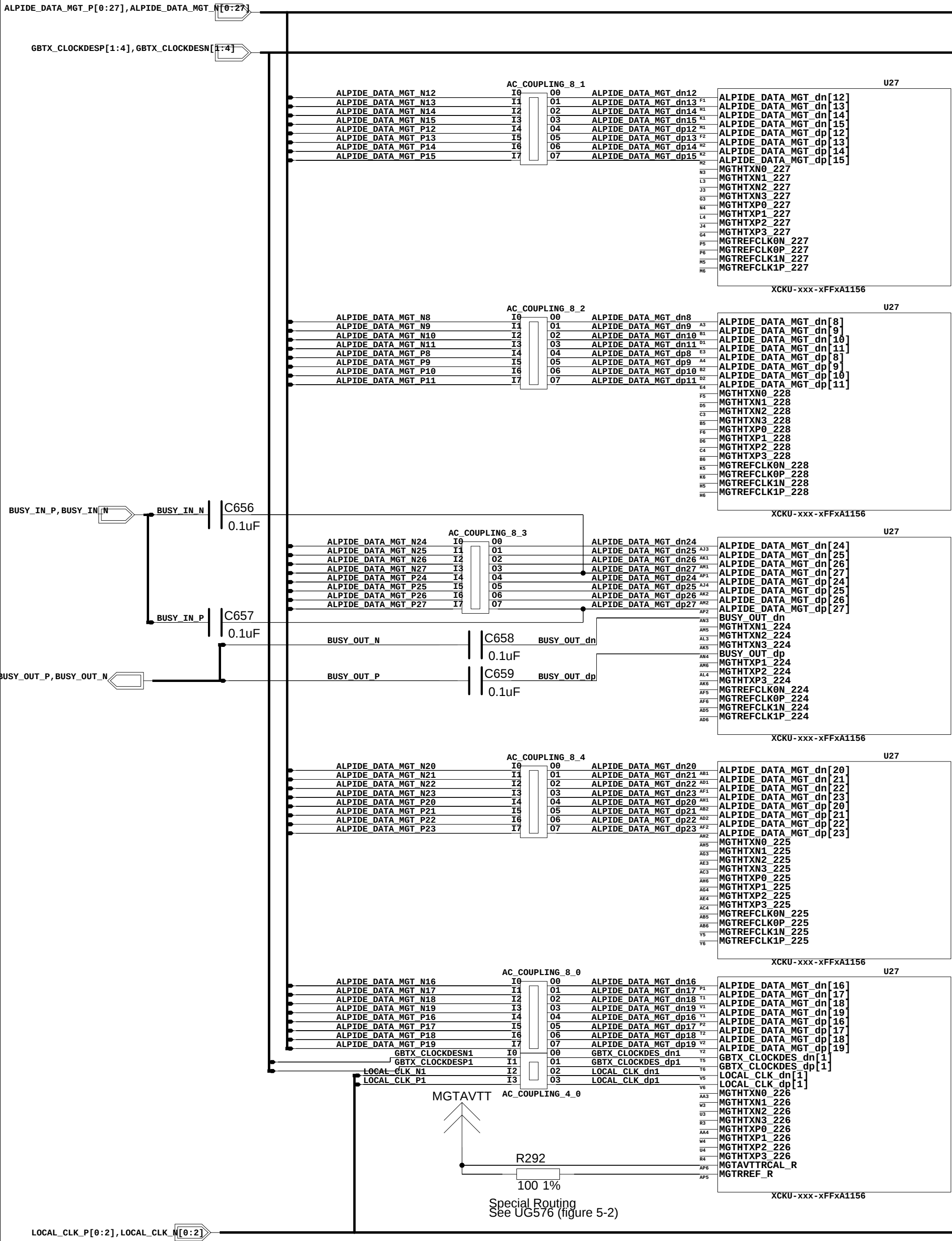
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DWG. NO.

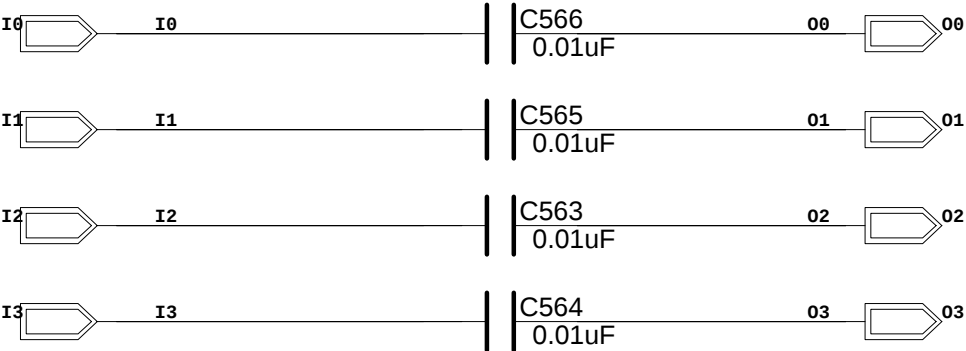
SH.

REV

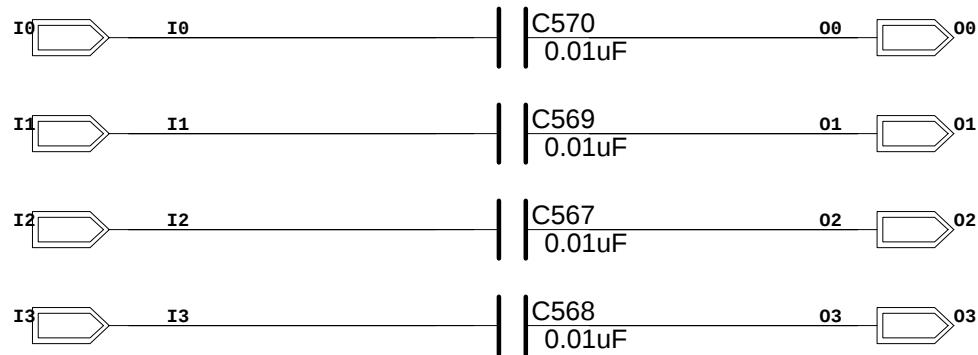
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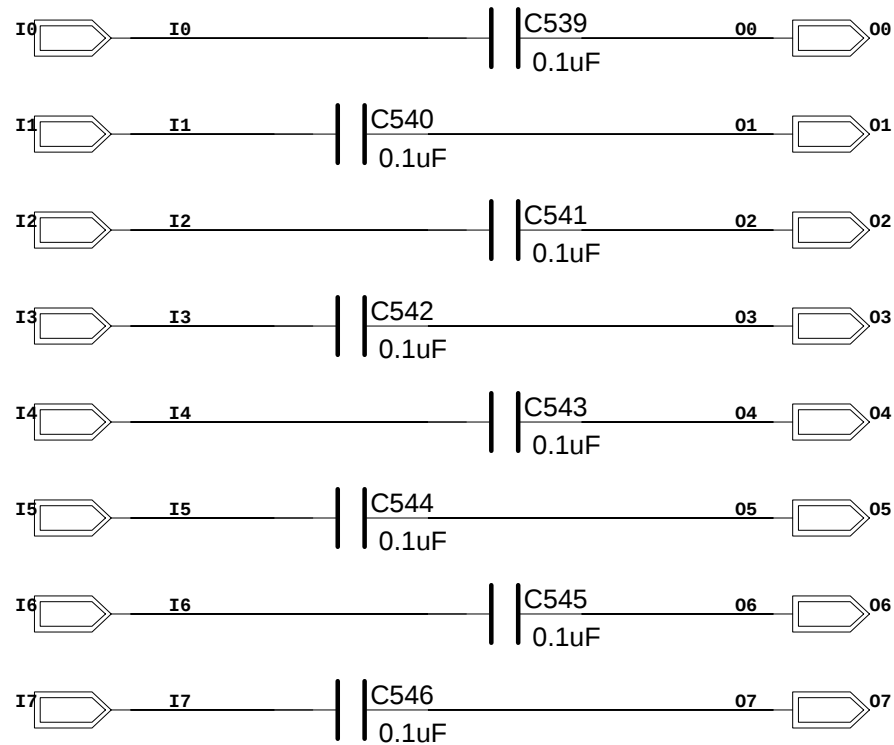
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DATE	28/04/2017:17:51	C			REV
TIME:		SHEET 8	of 66		



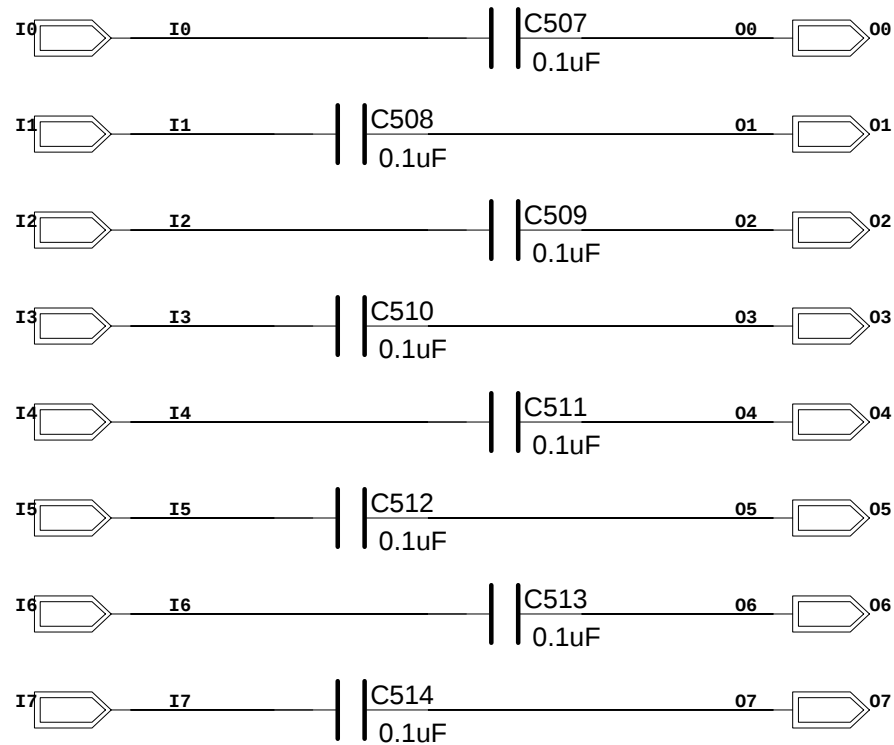
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APPROVALS							
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CHECKED							
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TIME:			SHEET 9 of 66				



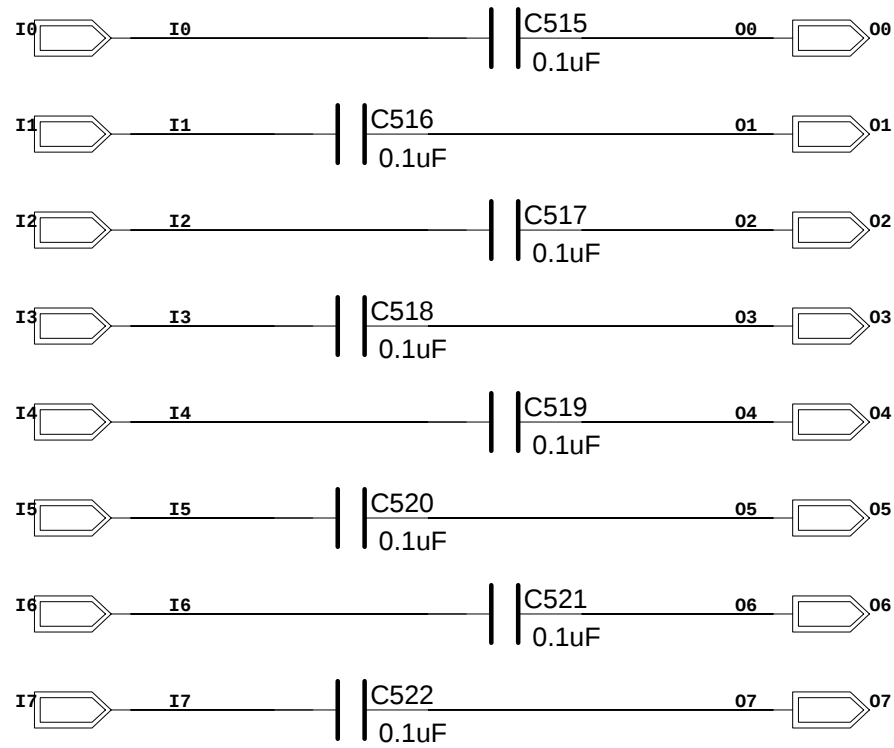
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TIME:			SHEET 10 of 66				



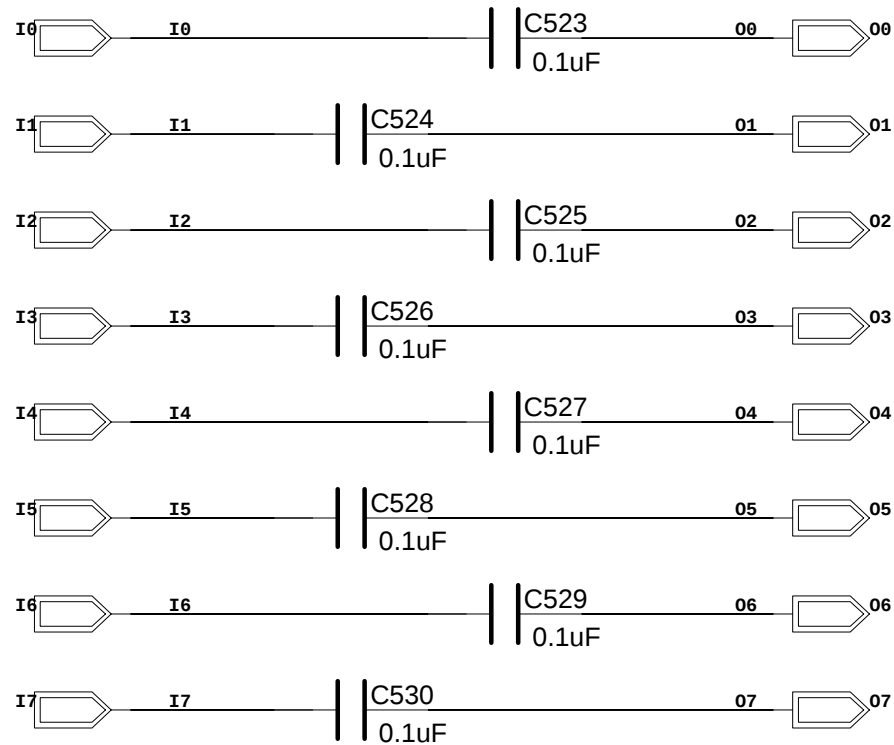
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APPROVALS						
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CHECKED						
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TIME:		SHEET 11 of 66				



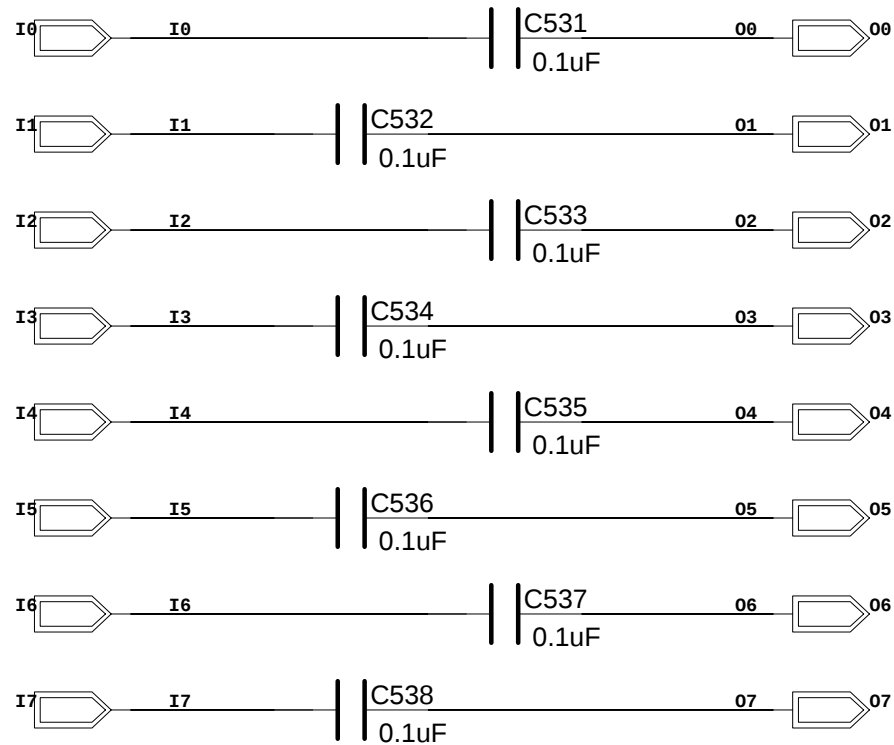
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DATE		28/04/2017:17:51	A				
TIME:			SHEET 12 of 66				



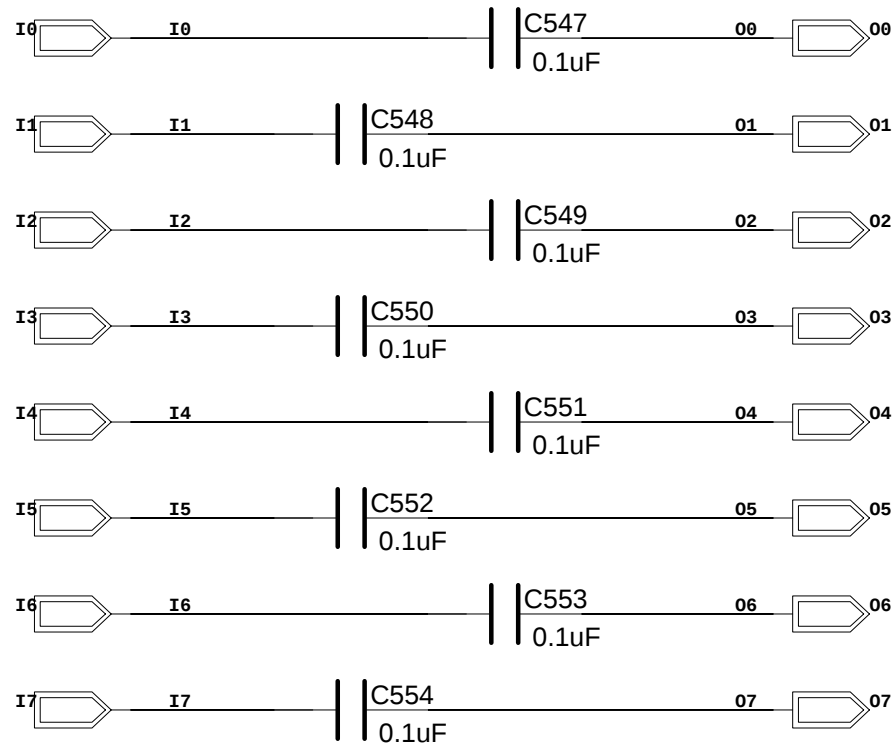
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DATE		28/04/2017:17:51	A				
TIME:			SHEET 13 of 66				



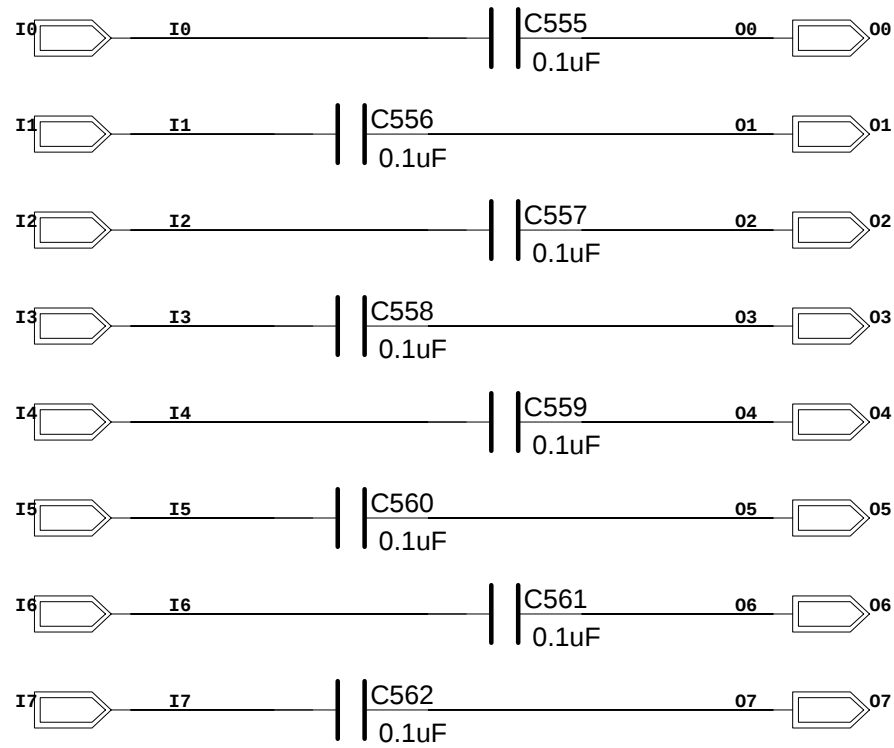
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APPROVALS						
DRAWN BY	IGF					
CHECKED						
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TIME:		A				
		SHEET 14 of 66				



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED						
DATE	28/04/2017:17:51	SIZE	AC_DECOUPLING_8_4		VER	REV
TIME:		A				
		SHEET 15 of 66				



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED						
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TIME:		A				
		SHEET 16 of 66				



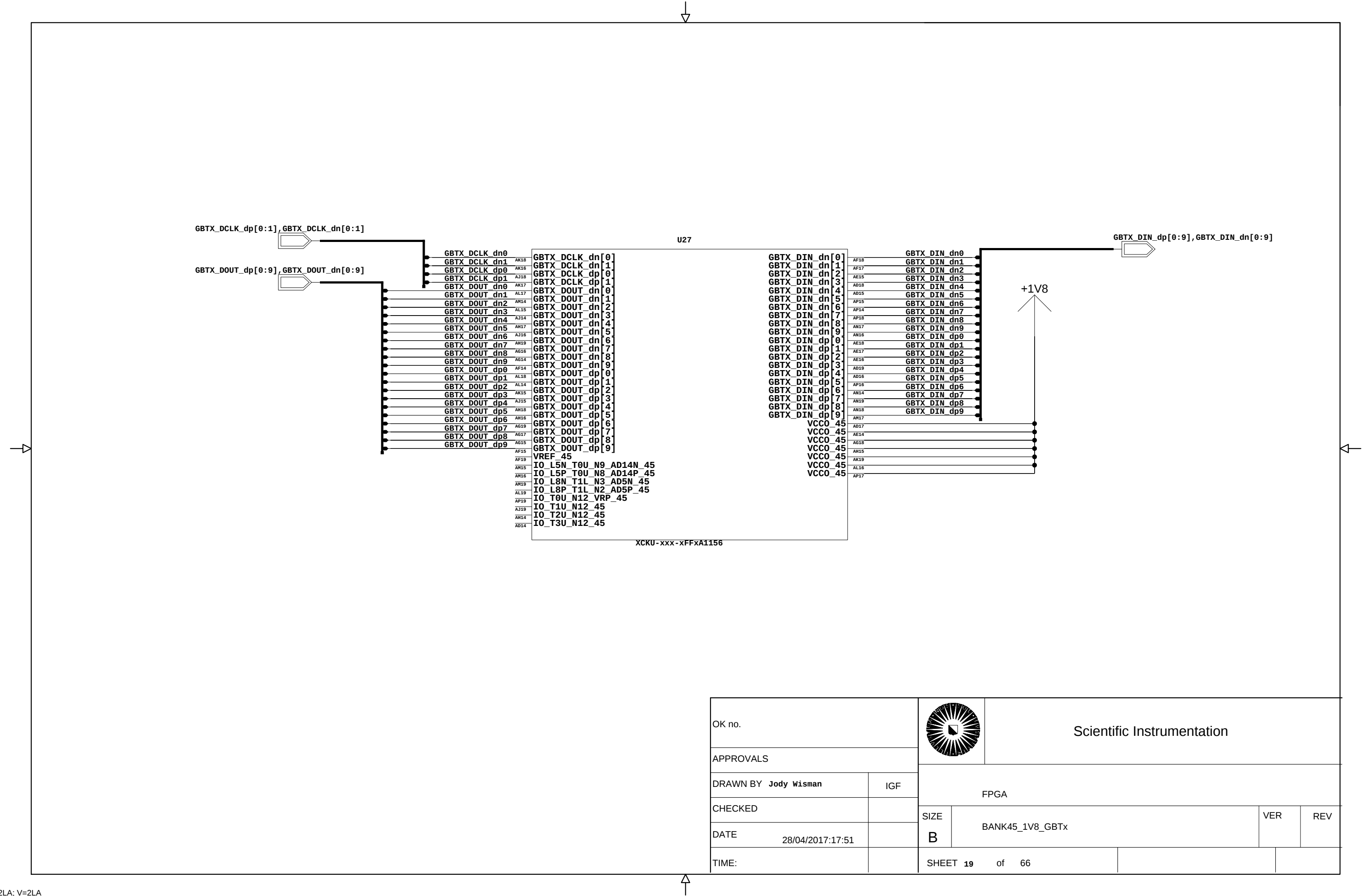
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DATE	28/04/2017:17:51	A					
TIME:		SHEET 17 of 66					

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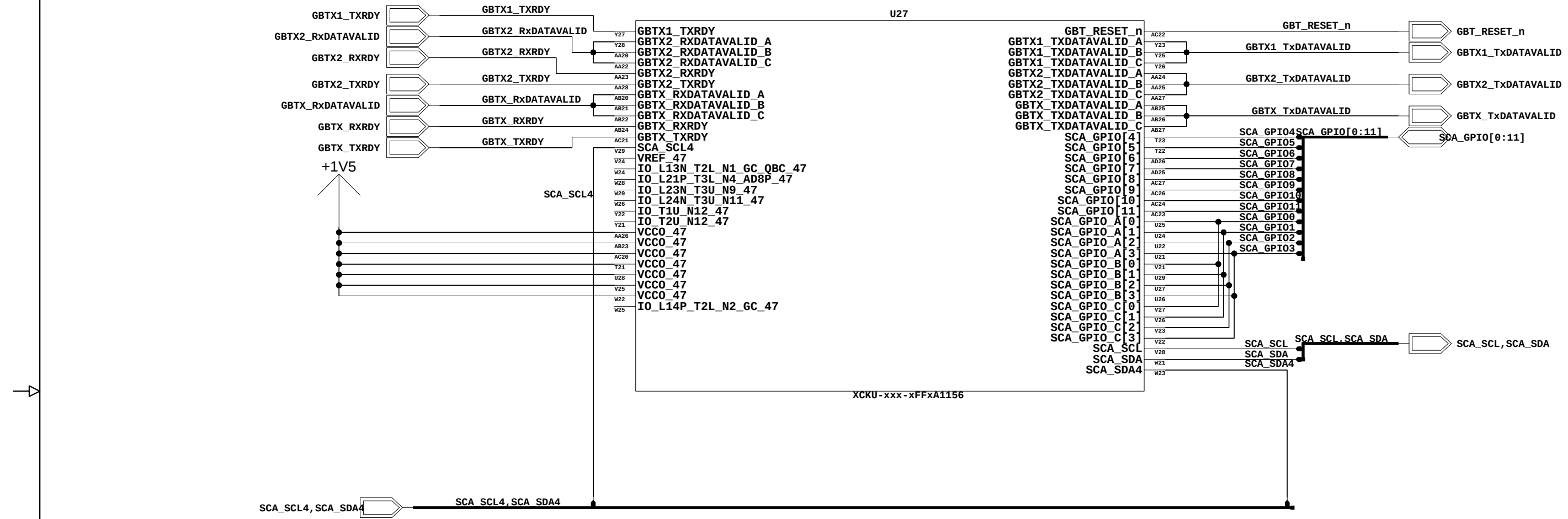
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AE21	IO_L1N_T0L_N1_DBC_44	VCC0_44	AH25
AD21	IO_L1P_T0L_N0_DBC_44	VCC0_44	AJ22
AG20	IO_L2N_T0L_N3_44	VCC0_44	AM23
AF20	IO_L2P_T0L_N2_44	VCC0_44	AN20
AE20	IO_L3N_T0L_N5_AD15N_44	IO_L11P_T1U_N8_GC_44	AJ23
AD20	IO_L3P_T0L_N4_AD15P_44	IO_L12N_T1U_N11_GC_44	AH23
AH21	IO_L4N_T0U_N7_DBC_AD7N_44	IO_L12P_T1U_N10_GC_44	AH22
AG21	IO_L4P_T0U_N6_DBC_AD7P_44	IO_L13P_T2L_N0_GC_QBC_44	AJ21
AE23	IO_L5N_T0U_N9_AD14N_44		
AE22	IO_L5P_T0U_N8_AD14P_44		
AG22	IO_L6N_T0U_N11_AD6N_44		
AF22	IO_L6P_T0U_N10_AD6P_44		
AE26	IO_L7N_T1L_N1_QBC_AD13N_44		
AE25	IO_L7P_T1L_N0_QBC_AD13P_44		
AF24	IO_L8N_T1L_N3_AD5N_44		
AF23	IO_L8P_T1L_N2_AD5P_44		
AG25	IO_L9N_T1L_N5_AD12N_44		
AG24	IO_L9P_T1L_N4_AD12P_44		
AJ25	IO_L10N_T1U_N7_QBC_AD4N_44		
AH24	IO_L10P_T1U_N6_QBC_AD4P_44		
AJ24	IO_L11N_T1U_N9_GC_44		
AK21	IO_L13N_T2L_N1_GC_QBC_44		
AK23	IO_L14N_T2L_N3_GC_44		
AK22	IO_L14P_T2L_N2_GC_44		
AM20	IO_L15N_T2L_N5_AD11N_44		
AL20	IO_L15P_T2L_N4_AD11P_44		
AK20	IO_L16N_T2U_N7_QBC_AD3N_44		
AJ20	IO_L16P_T2U_N6_QBC_AD3P_44		
AL23	IO_L17N_T2U_N9_AD10N_44		
AL22	IO_L17P_T2U_N8_AD10P_44		
AL25	IO_L18N_T2U_N11_AD2N_44		
AL24	IO_L18P_T2U_N10_AD2P_44		
AH21	IO_L19N_T3L_N1_DBC_AD9N_44		
AM21	IO_L19P_T3L_N0_DBC_AD9P_44		
AM22	IO_L20N_T3L_N3_AD1N_44		
AM22	IO_L20P_T3L_N2_AD1P_44		
AM24	IO_L21N_T3L_N5_AD8N_44		
AM24	IO_L21P_T3L_N4_AD8P_44		
AP21	IO_L22N_T3U_N7_DBC_AD0N_44		
AP20	IO_L22P_T3U_N6_DBC_AD0P_44		
AP25	IO_L23N_T3U_N9_44		
AP24	IO_L23P_T3U_N8_44		
AP23	IO_L24N_T3U_N11_44		
AM23	IO_L24P_T3U_N10_44		
AF25	IO_T1U_N12_44		
AK25	IO_T2U_N12_44		
AM25	IO_T3U_N12_44		

XCKU-xxx-xFFxA1156

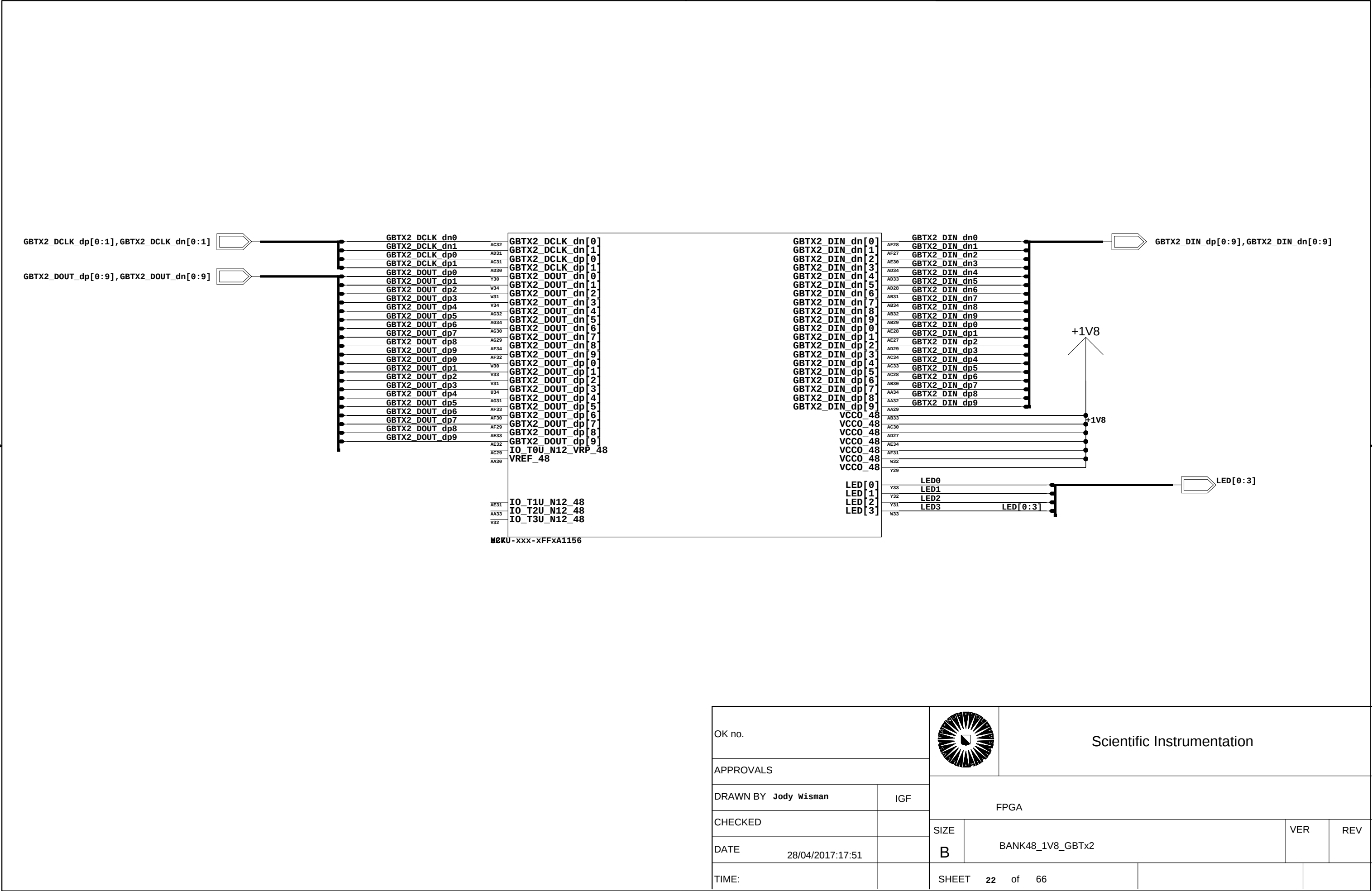
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DATE 28/04/2017:17:51		B				
TIME:		SHEET 18 of 66				



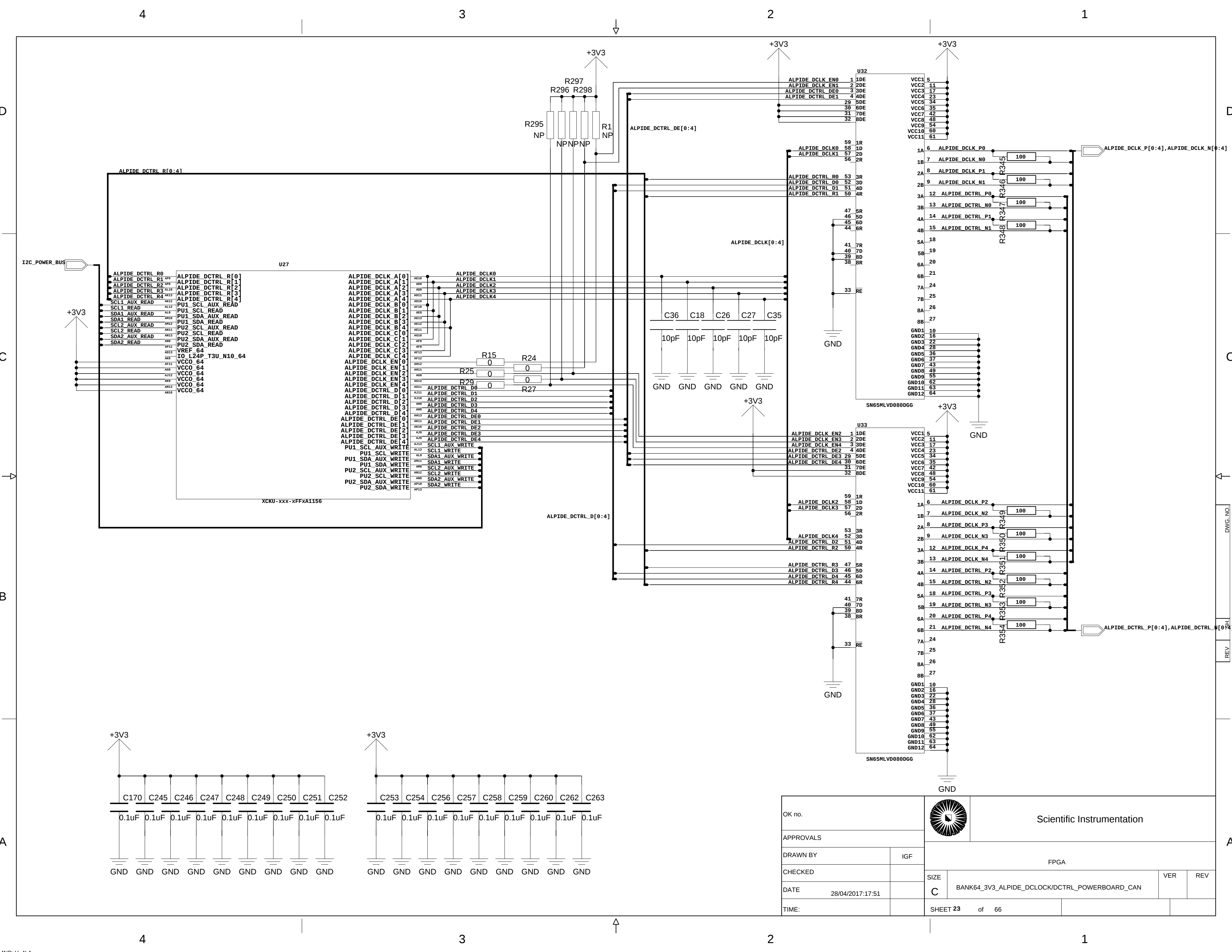
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DATE 28/04/2017:17:51		B				
TIME:		SHEET 19 of 66				



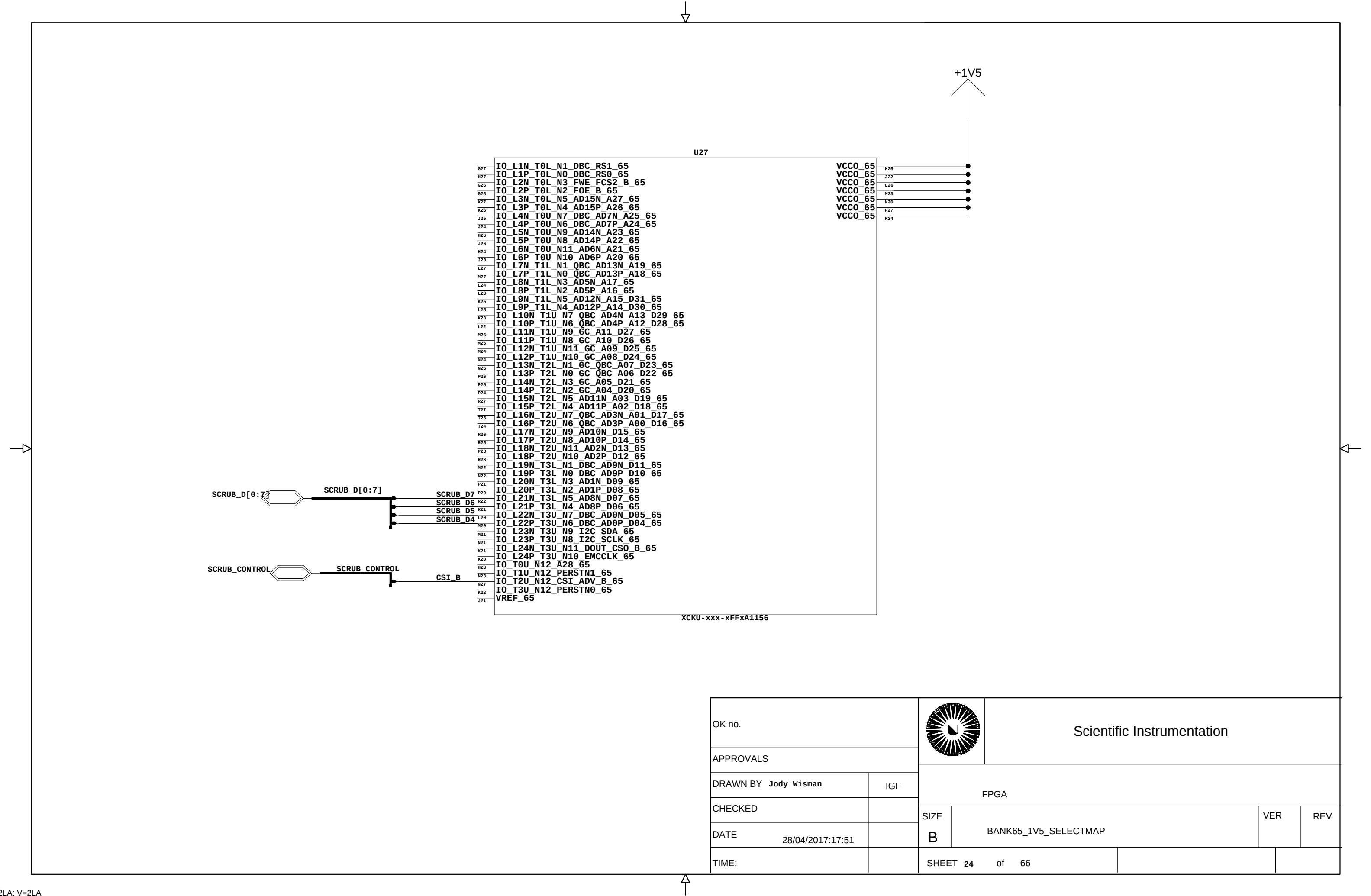
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DATE 28/04/2017:17:51		B				
TIME:		SHEET 21 of 66				



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman		IGF	FPGA			
CHECKED			SIZE B	BANK48_1V8_GBtx2	VER	REV
DATE 28/04/2017:17:51						
TIME:			SHEET 22 of 66			



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	FPGA				
CHECKED		SIZE	BANK64_3V3_ALPIDE_DCLOCK/DCTRL_POWERBOARD_CAN		VER	REV
DATE	28/04/2017:17:51	C				
TIME:		SHEET 23 of 66				



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman	IGF	FPGA				
CHECKED		SIZE			VER	REV
DATE 28/04/2017:17:51		B	BANK65_1V5_SELECTMAP			
TIME:		SHEET 24 of 66				

ALPIDE_DATA_GPIO_dp[0:27],ALPIDE_DATA_GPIO_dn[0:27]



ALPIDE_DATA_GPIO_dn0
ALPIDE_DATA_GPIO_dn1 A9
ALPIDE_DATA_GPIO_dn2 K13
ALPIDE_DATA_GPIO_dn3 G11
ALPIDE_DATA_GPIO_dn4 G12
ALPIDE_DATA_GPIO_dn5 H8
ALPIDE_DATA_GPIO_dn6 H9
ALPIDE_DATA_GPIO_dn7 H13
ALPIDE_DATA_GPIO_dn8 J10
ALPIDE_DATA_GPIO_dn9 J11
ALPIDE_DATA_GPIO_dn10 F8
ALPIDE_DATA_GPIO_dn11 K10
ALPIDE_DATA_GPIO_dn12 F11
ALPIDE_DATA_GPIO_dn13 F12
ALPIDE_DATA_GPIO_dn14 H12
ALPIDE_DATA_GPIO_dn15 F9
ALPIDE_DATA_GPIO_dn16 F13
ALPIDE_DATA_GPIO_dn17 F13
ALPIDE_DATA_GPIO_dn18 H12
ALPIDE_DATA_GPIO_dn19 J10
ALPIDE_DATA_GPIO_dn20 F11
ALPIDE_DATA_GPIO_dn21 F8
ALPIDE_DATA_GPIO_dn22 F13
ALPIDE_DATA_GPIO_dn23 F9
ALPIDE_DATA_GPIO_dp0 F10
ALPIDE_DATA_GPIO_dp1 B9
ALPIDE_DATA_GPIO_dp2 L13
ALPIDE_DATA_GPIO_dp3 H11
ALPIDE_DATA_GPIO_dp4 H12
ALPIDE_DATA_GPIO_dp5 J8
ALPIDE_DATA_GPIO_dp6 J9
ALPIDE_DATA_GPIO_dp7 J13
ALPIDE_DATA_GPIO_dp8 K10
ALPIDE_DATA_GPIO_dp9 K11
ALPIDE_DATA_GPIO_dp10 F8
ALPIDE_DATA_GPIO_dp11 F10
ALPIDE_DATA_GPIO_dp12 F11
ALPIDE_DATA_GPIO_dp13 F12
ALPIDE_DATA_GPIO_dp14 F13
ALPIDE_DATA_GPIO_dp15 F8
ALPIDE_DATA_GPIO_dp16 F9
ALPIDE_DATA_GPIO_dp17 F13
ALPIDE_DATA_GPIO_dp18 H12
ALPIDE_DATA_GPIO_dp19 F10
ALPIDE_DATA_GPIO_dp20 F11
ALPIDE_DATA_GPIO_dp21 F8
ALPIDE_DATA_GPIO_dp22 F13
ALPIDE_DATA_GPIO_dp23 F9

ALPIDE_DATA_GPIO_dn[0
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ALPIDE_DATA_GPIO_dp[23
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VREF_66
IO T1U_N12_66
IO T2U_N12_66
IO T3U_N12_66

U27

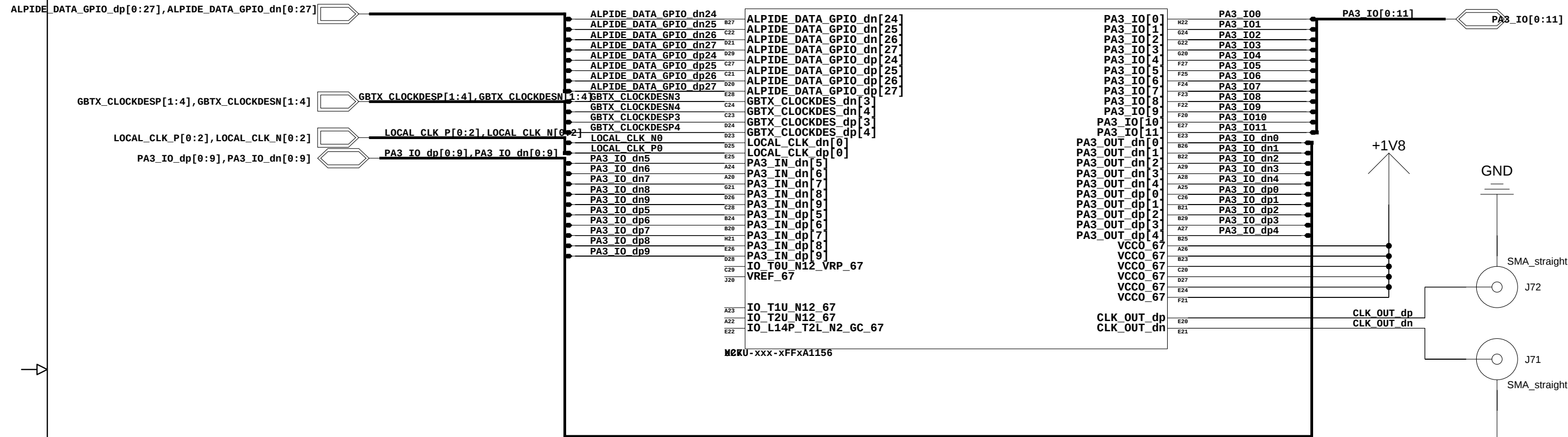
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VCC0_66
VCC0_66
VCC0_66
VCC0_66
VCC0_66

B13
C10
F11
G8
J12
K9

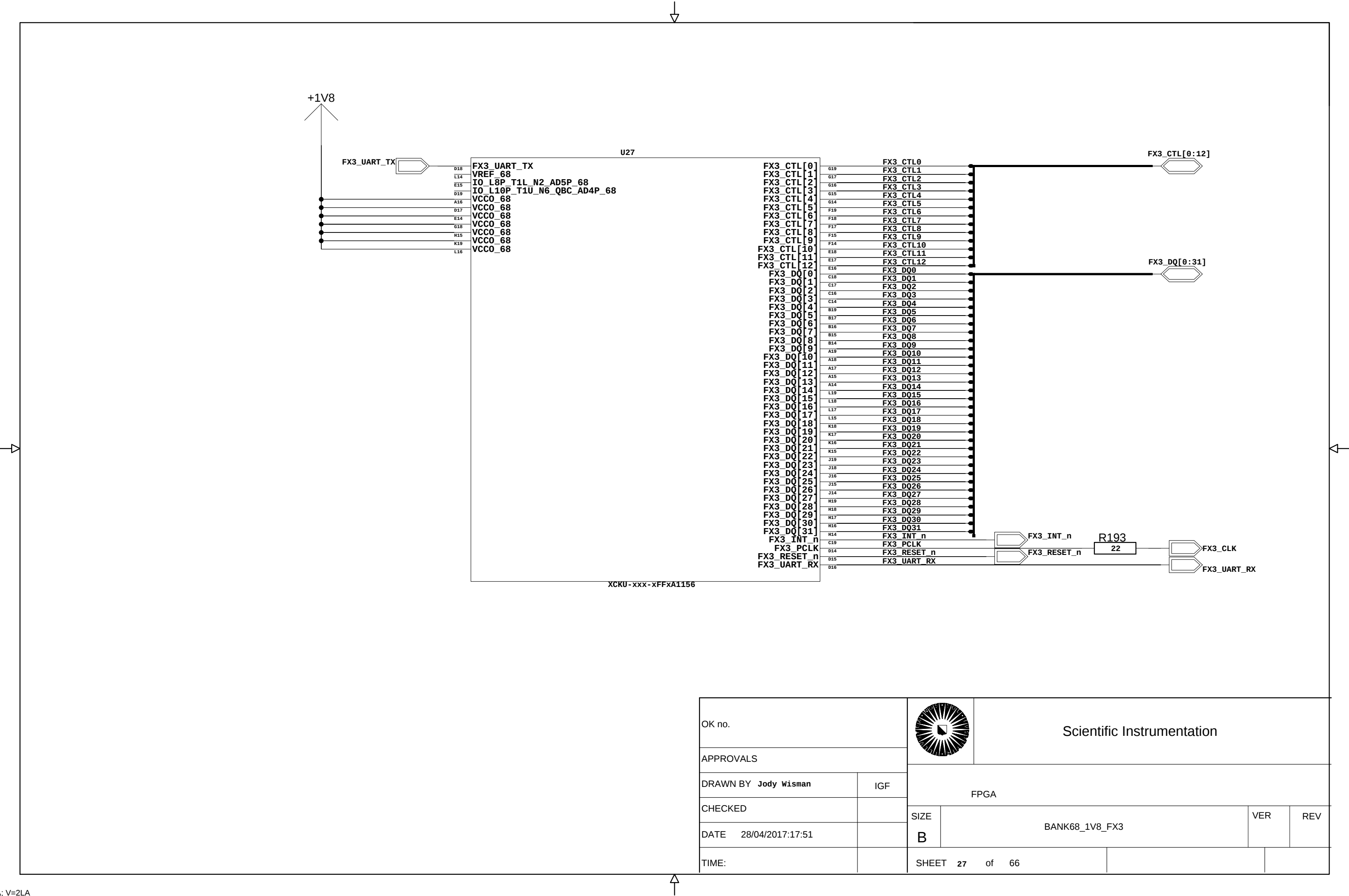
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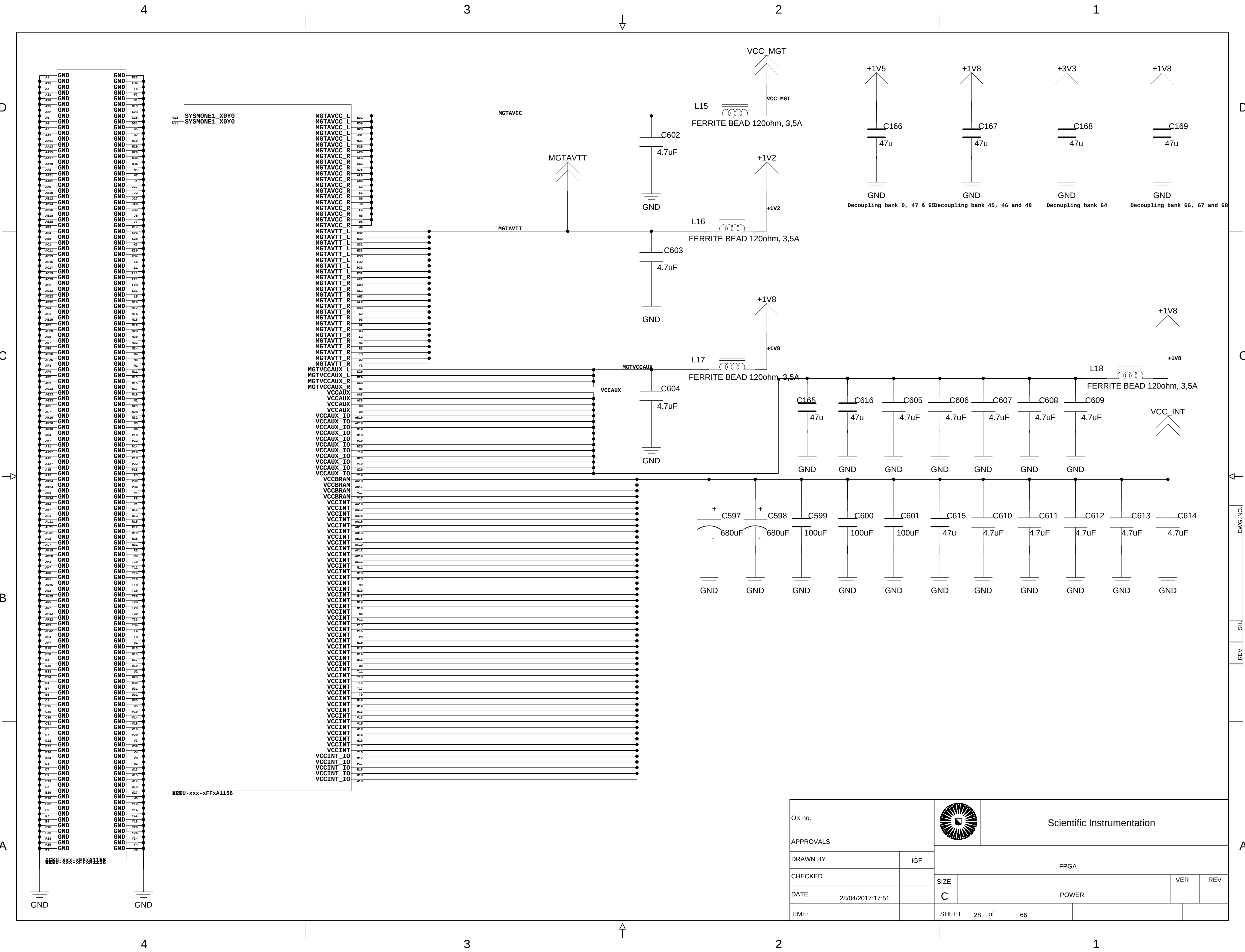
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APPROVALS						
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DATE 28/04/2017:17:51			B			
TIME:			SHEET 25 of 66			



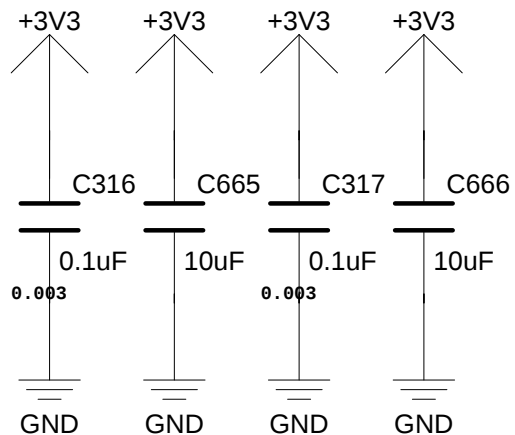
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APPROVALS						
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CHECKED		SIZE	BANK67_1V8_ALPIDE_HSDATA_PA3		VER	REV
DATE 28/04/2017:17:51		B				
TIME:		SHEET 26 of 66				



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman	IGF	FPGA				
CHECKED						
DATE 28/04/2017:17:51		SIZE	BANK68_1V8_FX3		VER	REV
		B				
TIME:		SHEET 27 of 66				



OK no.			Scientific Instrumentation			
APPROVALS			FPGA			
DRAWN BY	IGF					
CHECKED						
DATE	28/04/2017:17:51	SIZE	POWER		VER	REV
TIME:		C				
		SHEET 28 of 66				


$$H=2LA; V=2LA$$

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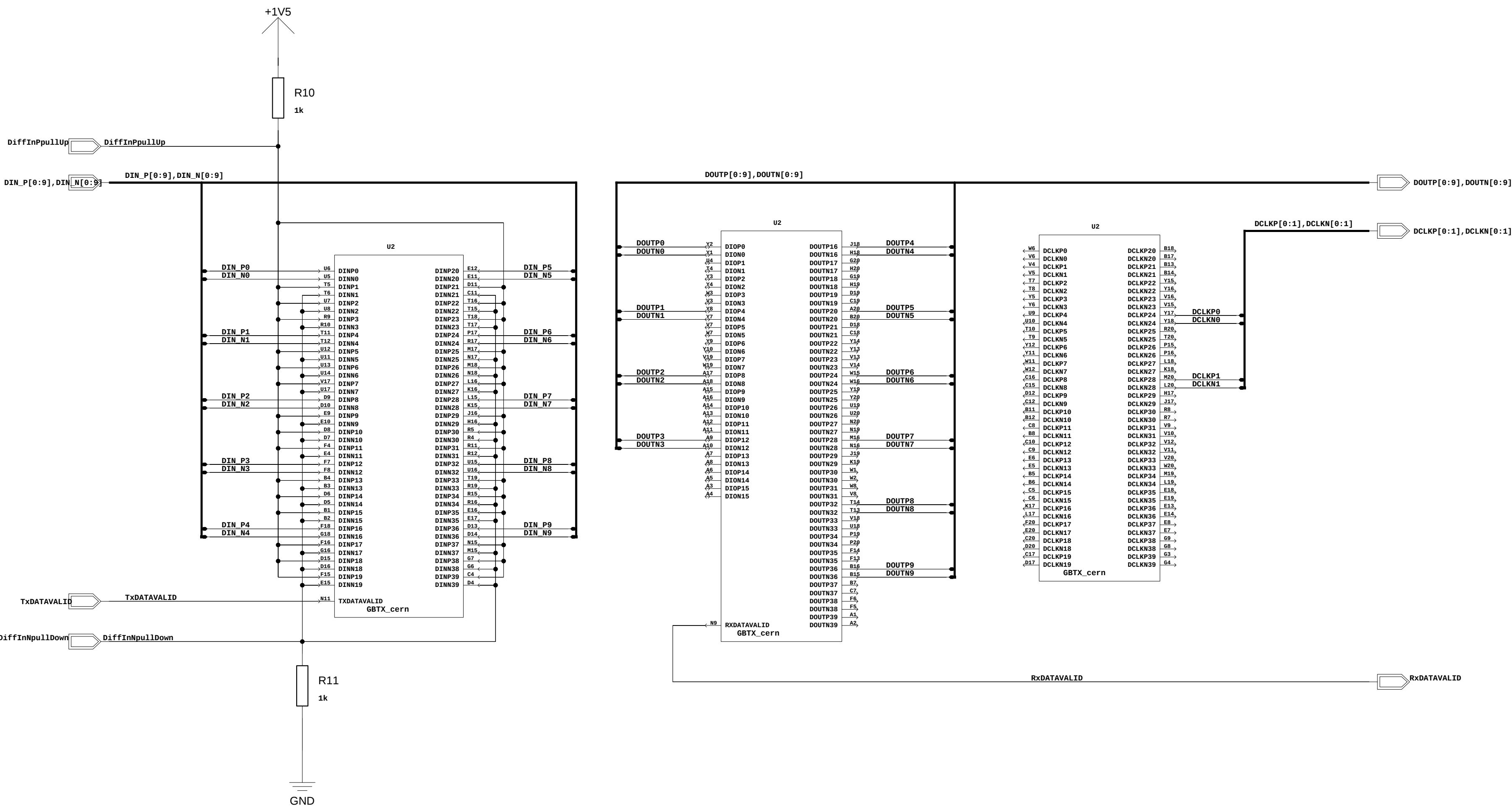
D

C

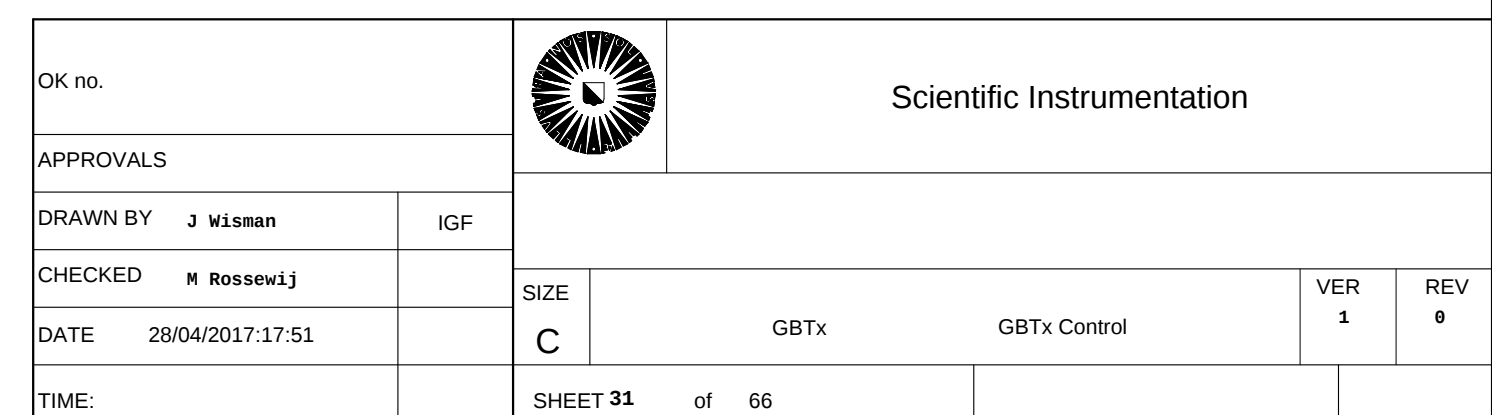
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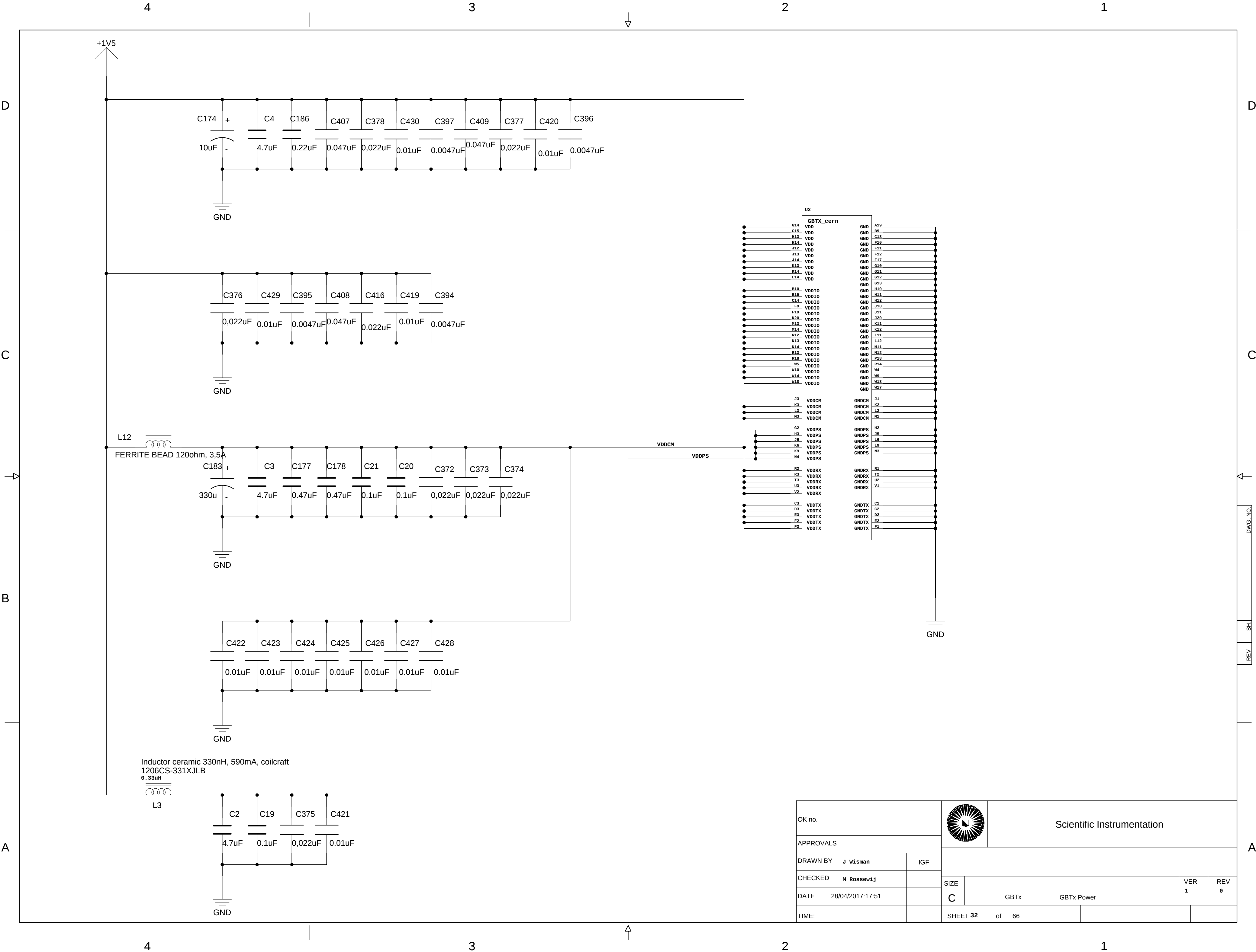
REV

A

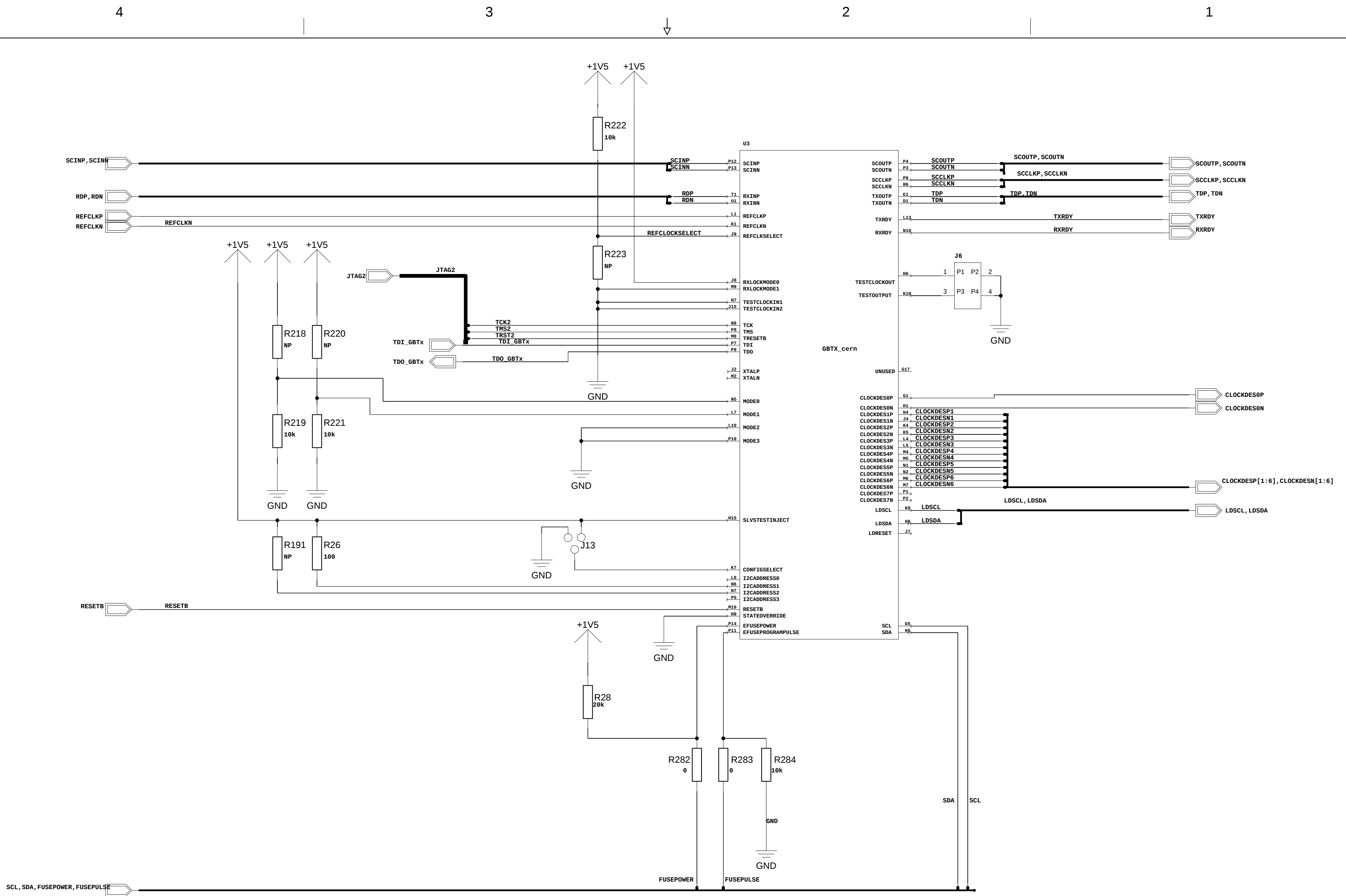


OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	J Wisman		IGF				
CHECKED	M Rossewij						
DATE	28/04/2017:17:51						
TIME:			SIZE			VER	REV
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			SHEET 30 of 66				



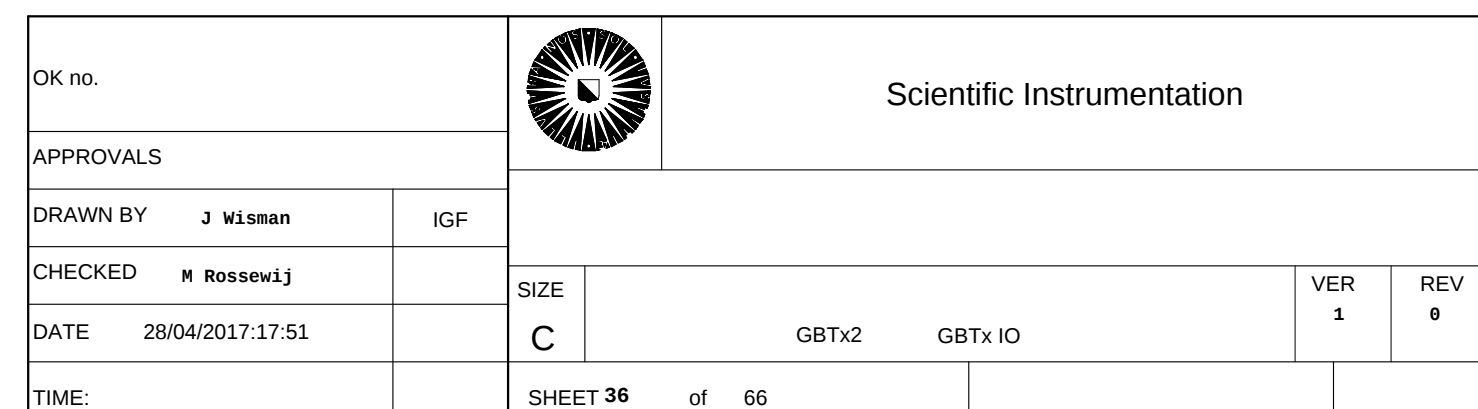


OK no.			Scientific Instrumentation				
APPROVALS							
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CHECKED M Rossewij							
DATE 28/04/2017:17:51			SIZE			VER 1	REV 0
		C	GBTx				



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	J Wisman	IGF					
CHECKED	M Rossewij		SIZE			VER	REV
DATE	28/04/2017:17:51		C	GBTx1 GBTx Control		1	0
TIME:			SHEET 34 of 66				





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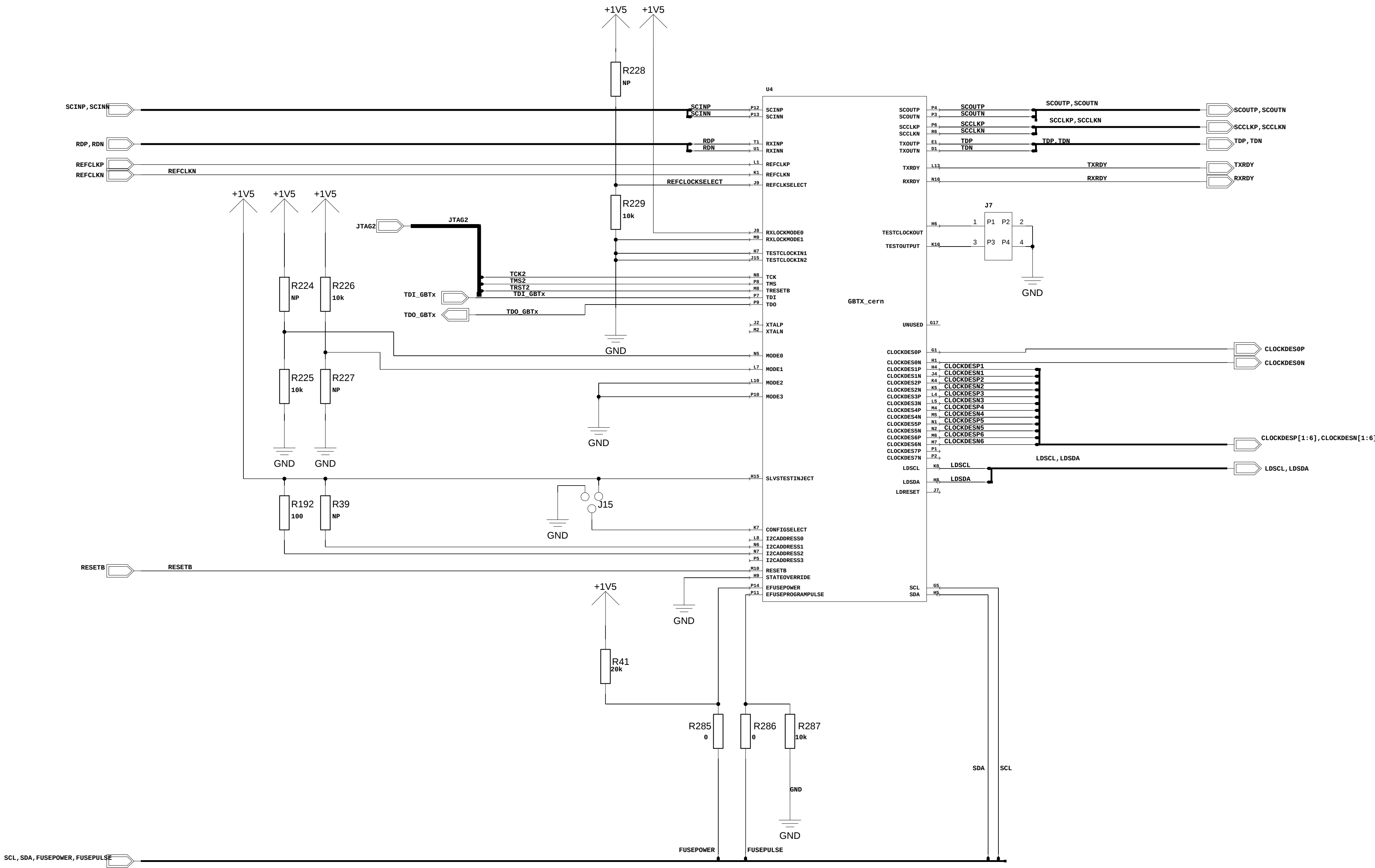
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C

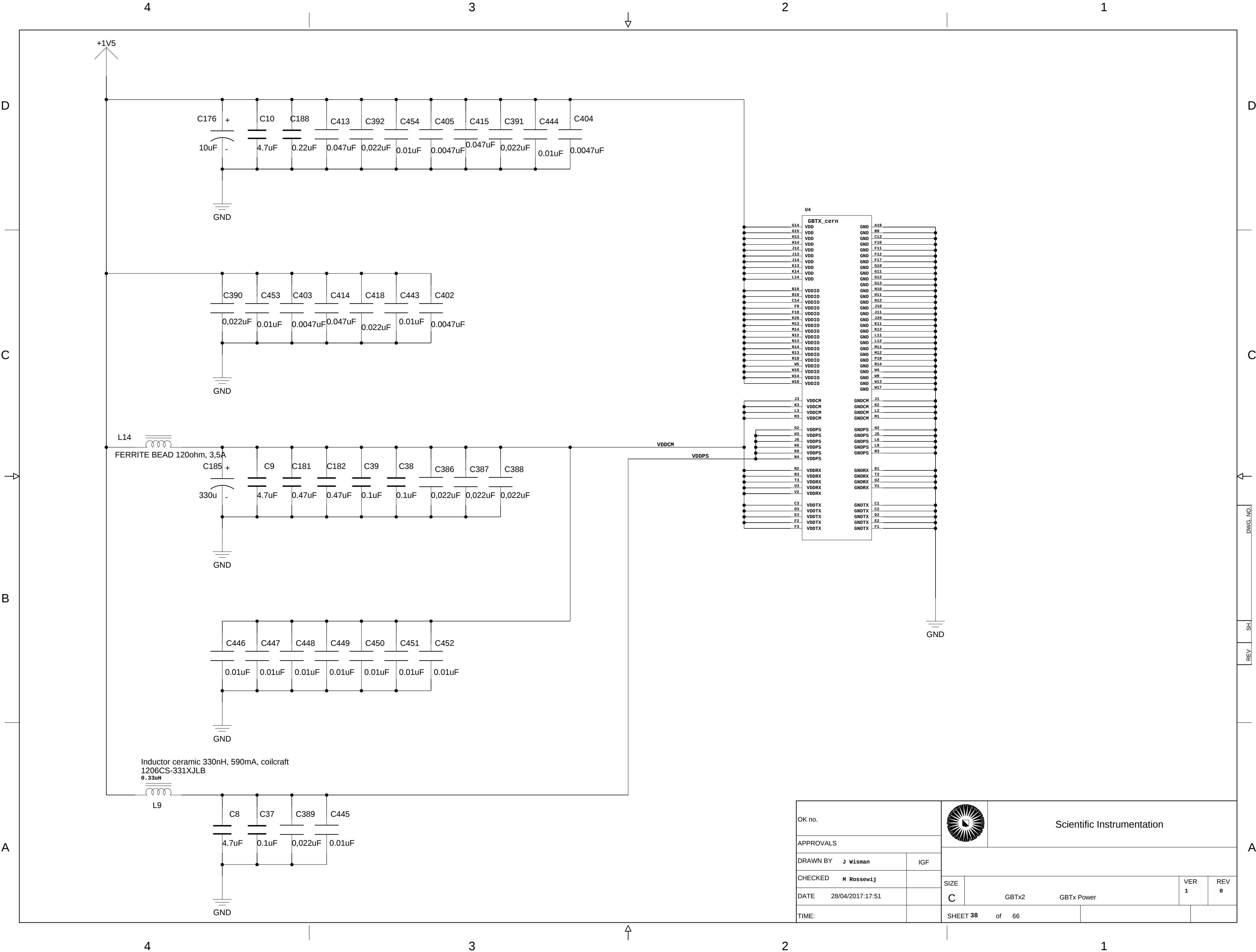
B

A

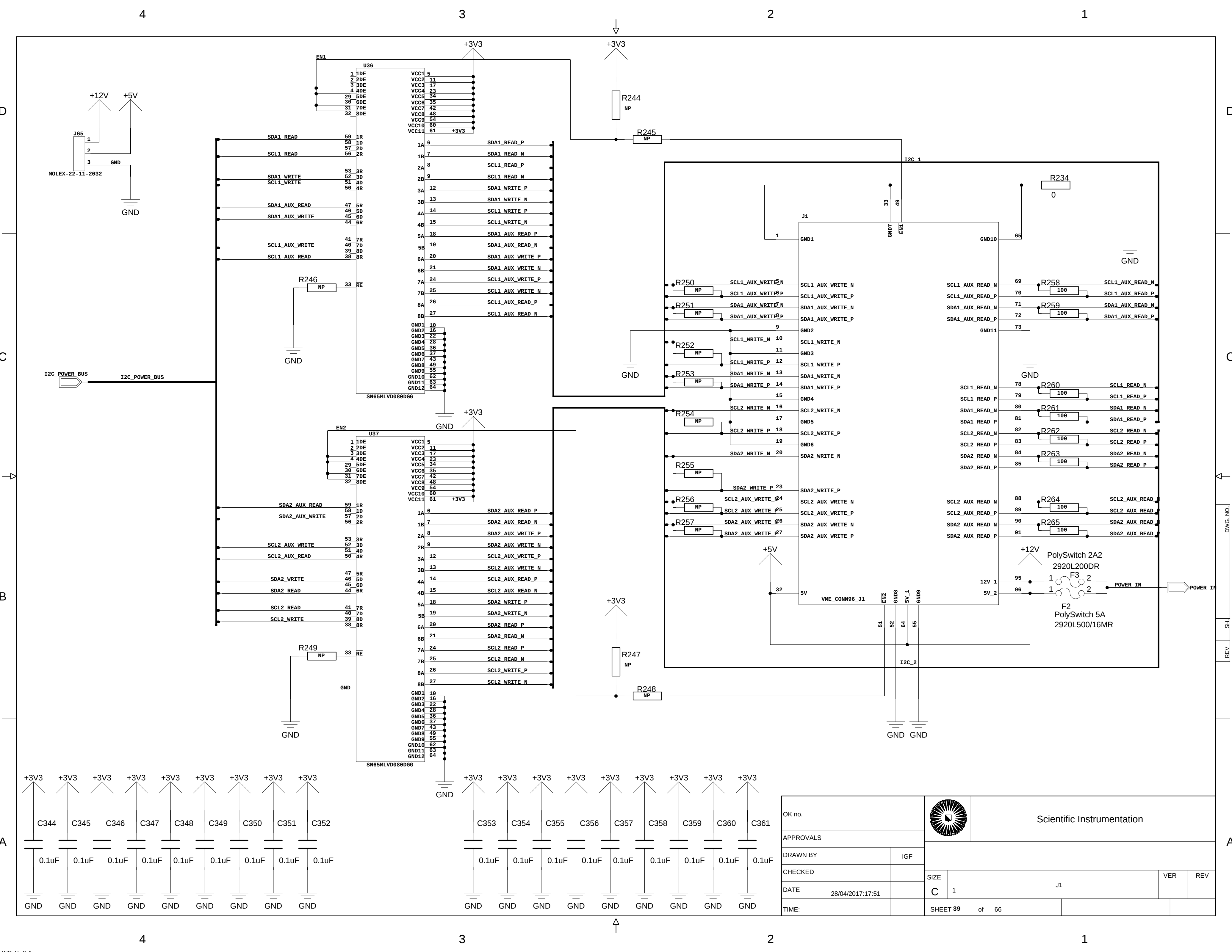
A



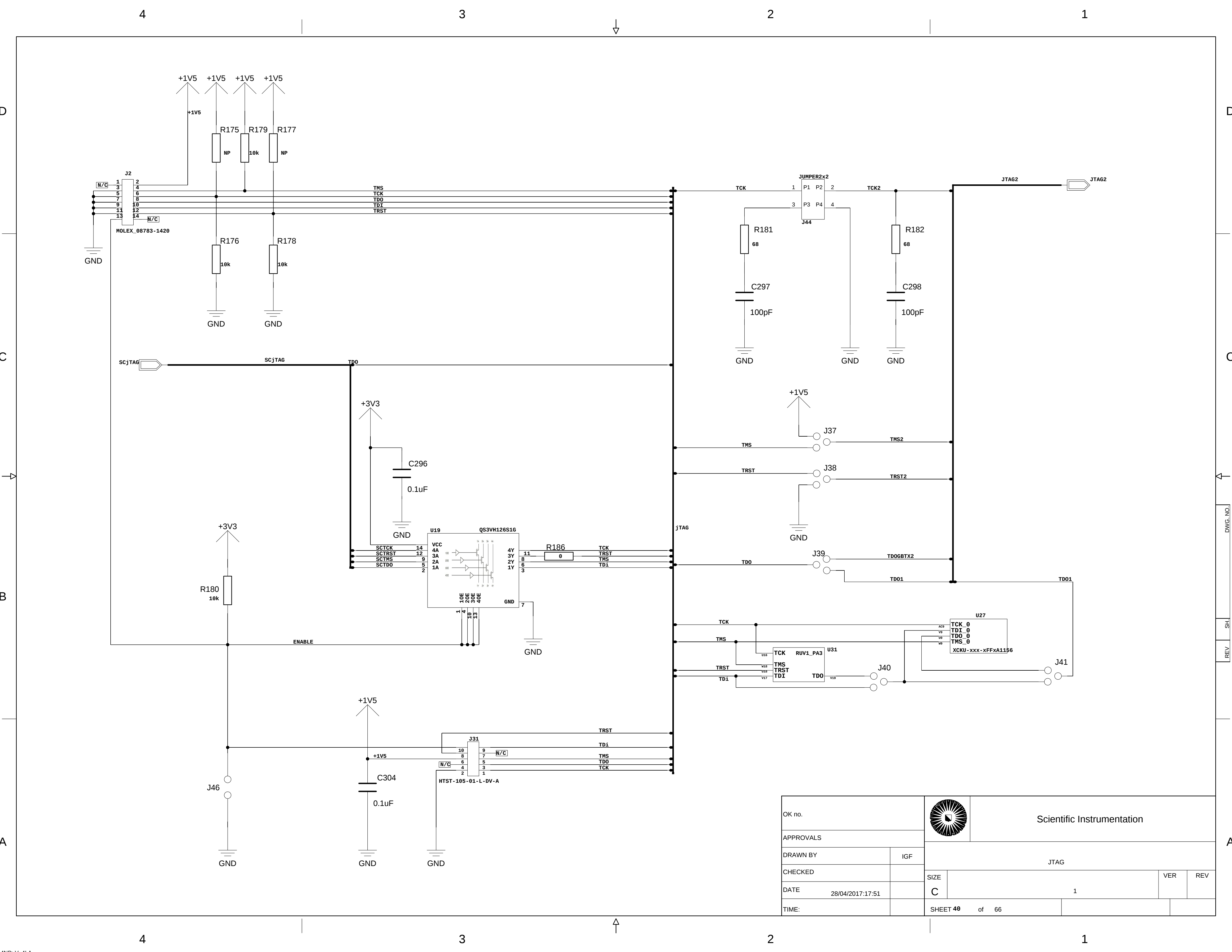
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY J Wisman		IGF					
CHECKED M Rossewij							
DATE 28/04/2017:17:51			SIZE C	GBTx2 GBTx Control		VER 1	REV 0
TIME:			SHEET 37 of 66				



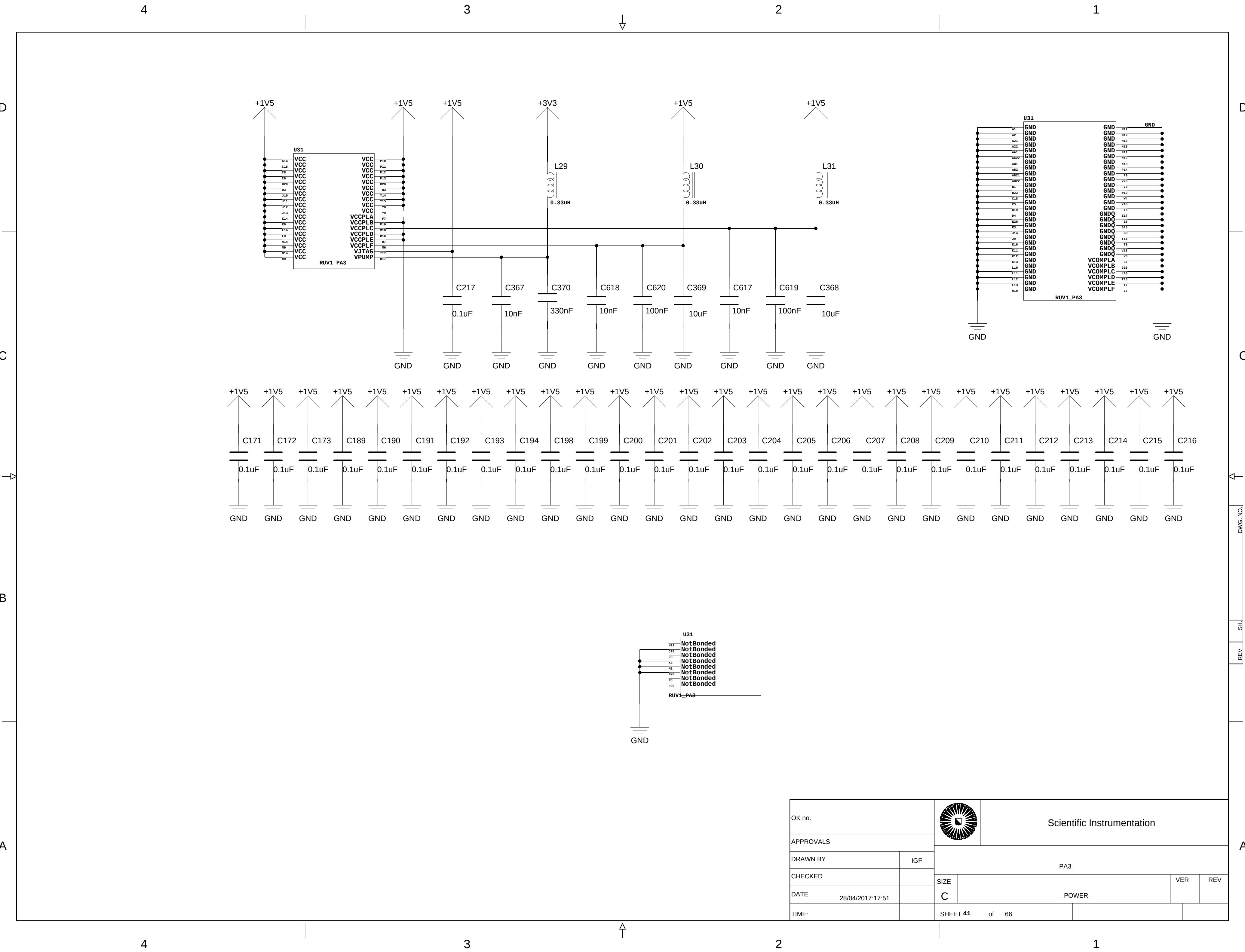
OK no.			Scientific Instrumentation			
APPROVALS						
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CHECKED M Rossewij						
DATE 28/04/2017:17:51			SIZE			VER 1
			C	GBTx2	GBTx Power	REV 0
TIME:			SHEET 38 of 66			



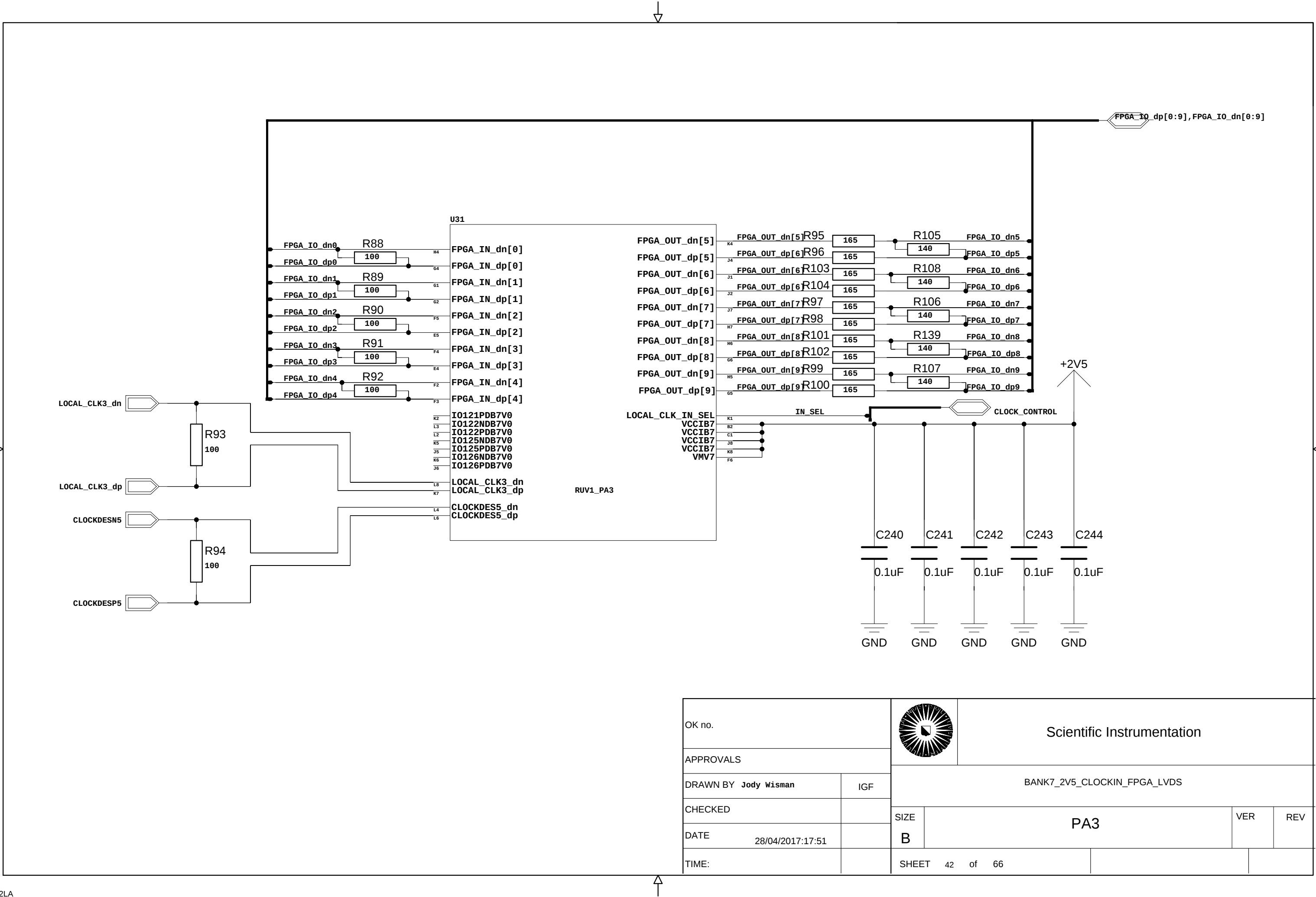
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APPROVALS							
DRAWN BY	IGF						
CHECKED		SIZE	J1			VER	REV
DATE	28/04/2017:17:51	C	1				
TIME:		SHEET 39 of 66					



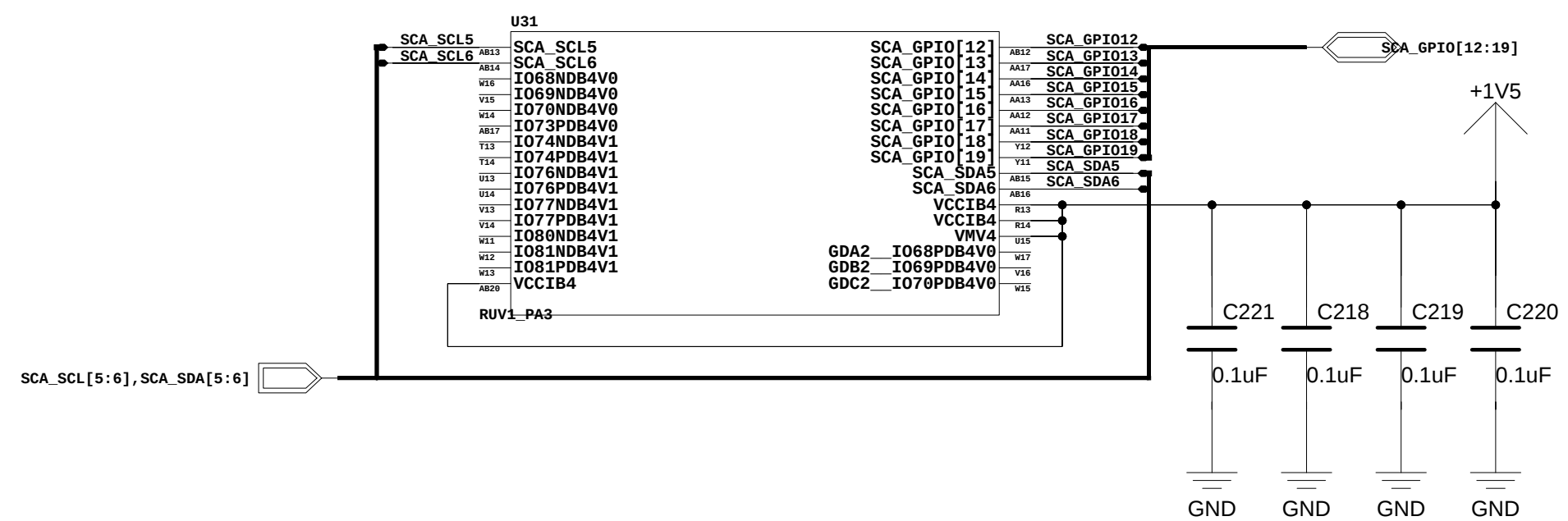
OK no.			Scientific Instrumentation		
APPROVALS					
DRAWN BY	IGF	JTAG			
CHECKED		SIZE		VER	REV
DATE	28/04/2017:17:51	C	1		
TIME:		SHEET 40 of 66			



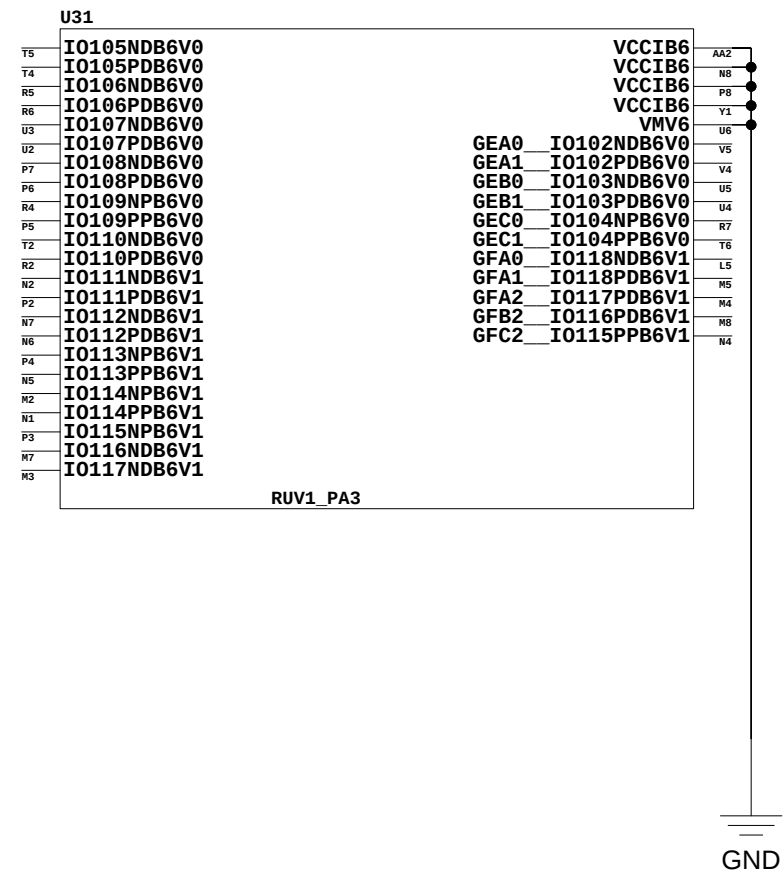
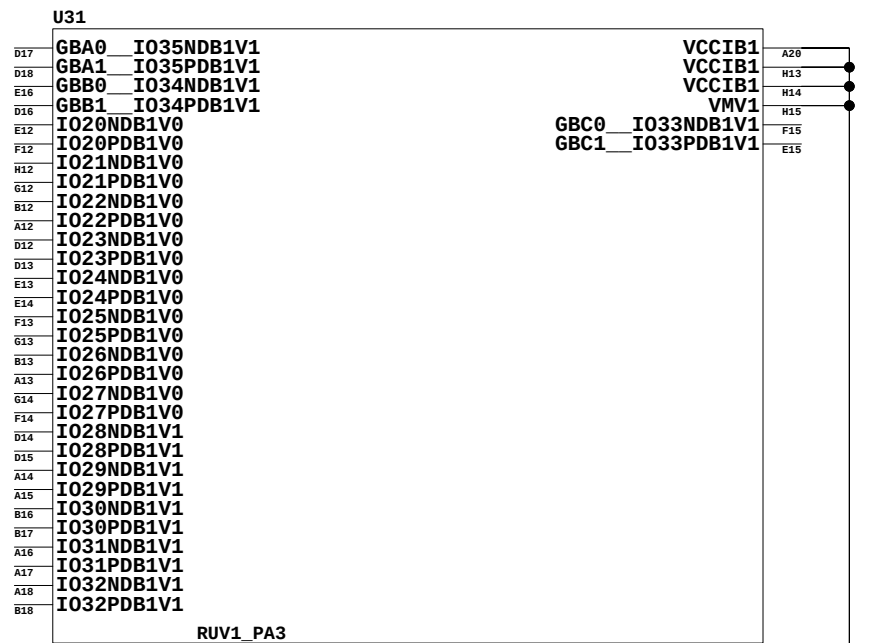
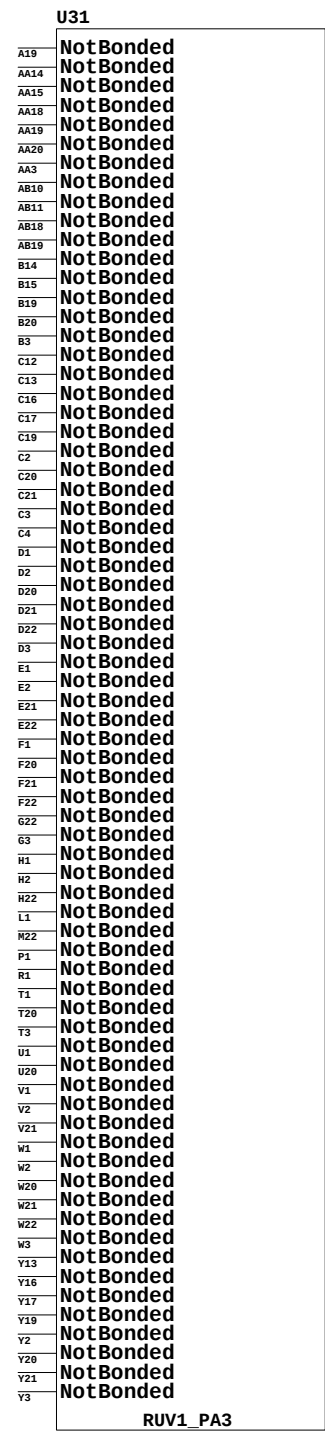
OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	PA3				
CHECKED		SIZE			VER	REV
DATE	28/04/2017:17:51	C	POWER			
TIME:		SHEET 41 of 66				



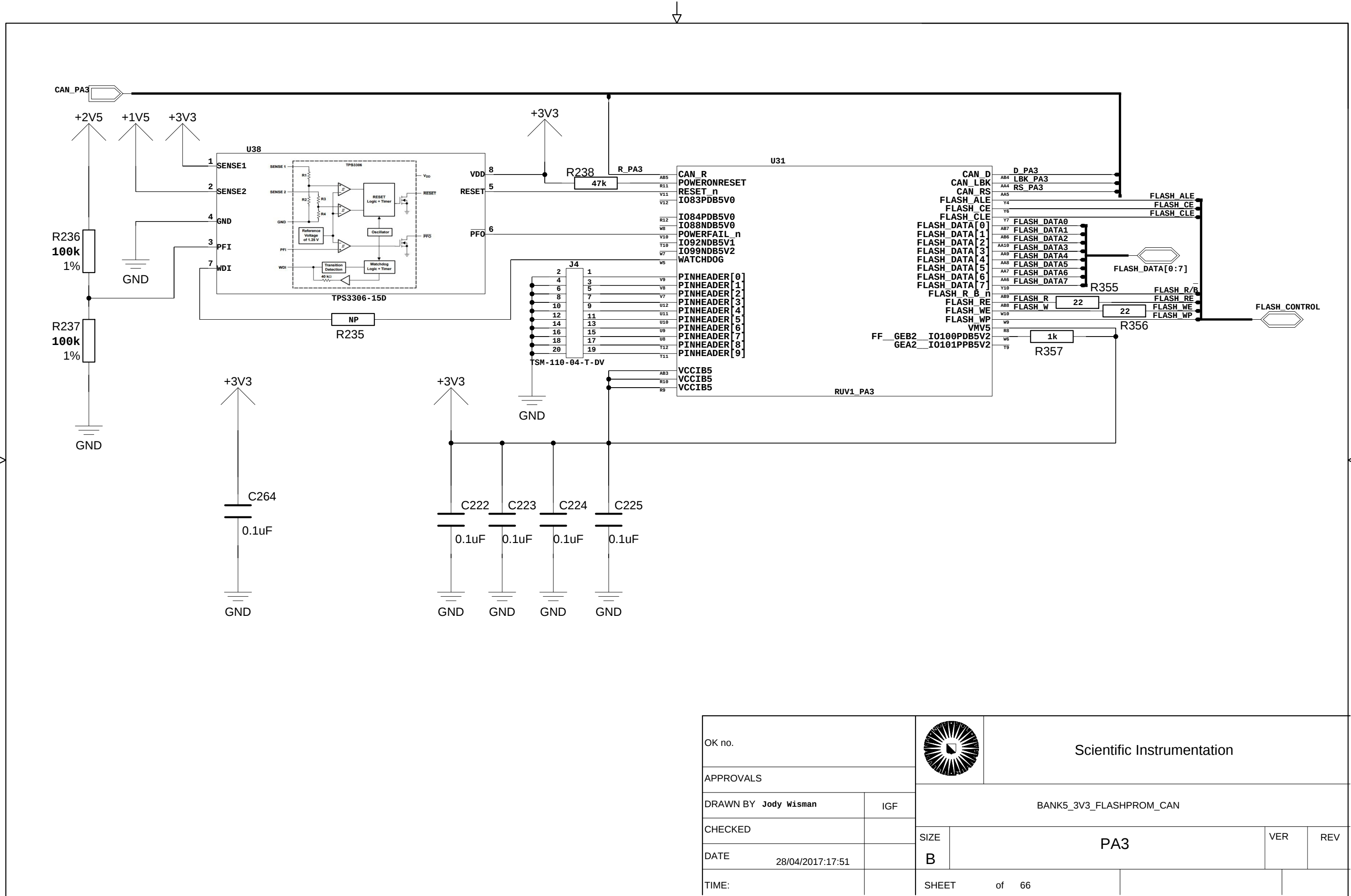
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Jody Wisman		IGF	BANK7_2V5_CLOCKIN_FPGA_LVDS				
CHECKED			SIZE	PA3		VER	REV
DATE 28/04/2017:17:51			B				
TIME:			SHEET 42 of 66				



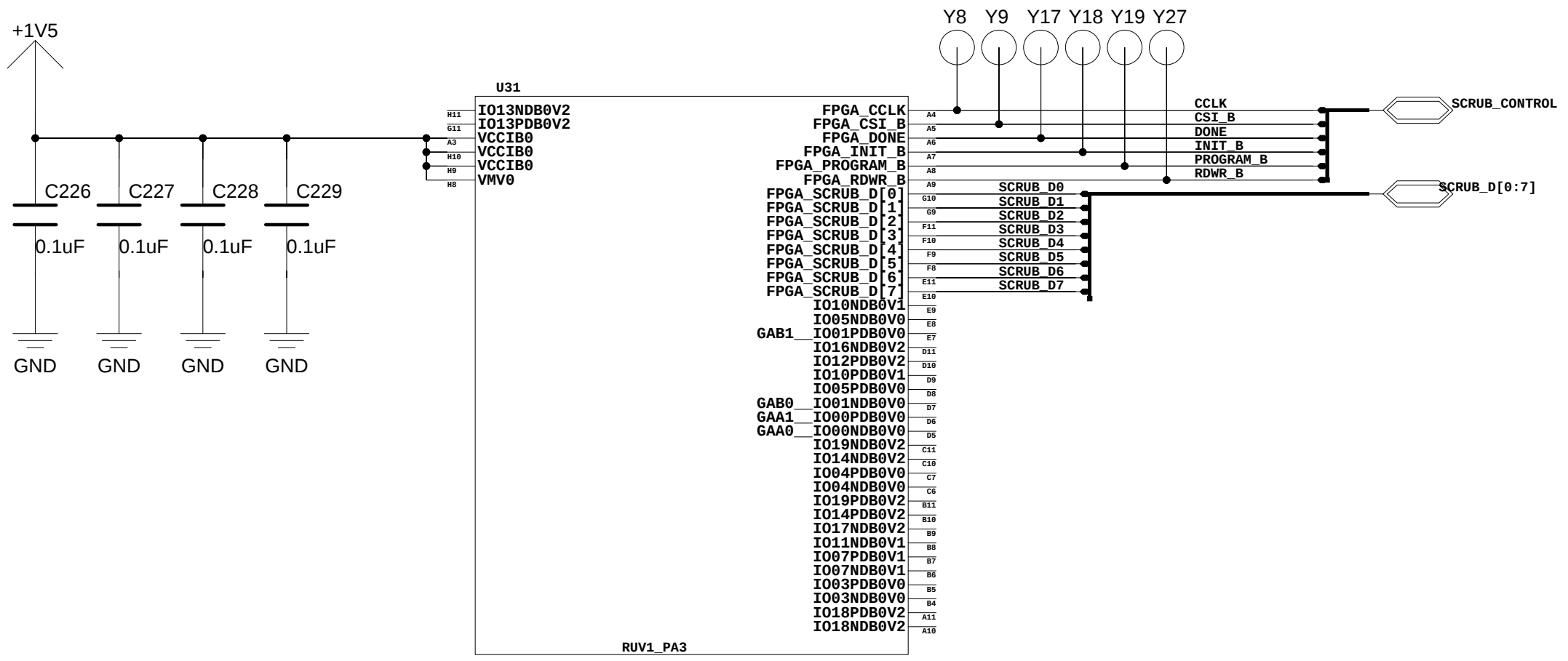
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APPROVALS							
DRAWN BY Jody Wisman		IGF	BANK4_1V5_SCA				
CHECKED			SIZE	PA3		VER	REV
DATE 28/04/2017:17:51			B				
TIME:			SHEET of 66				



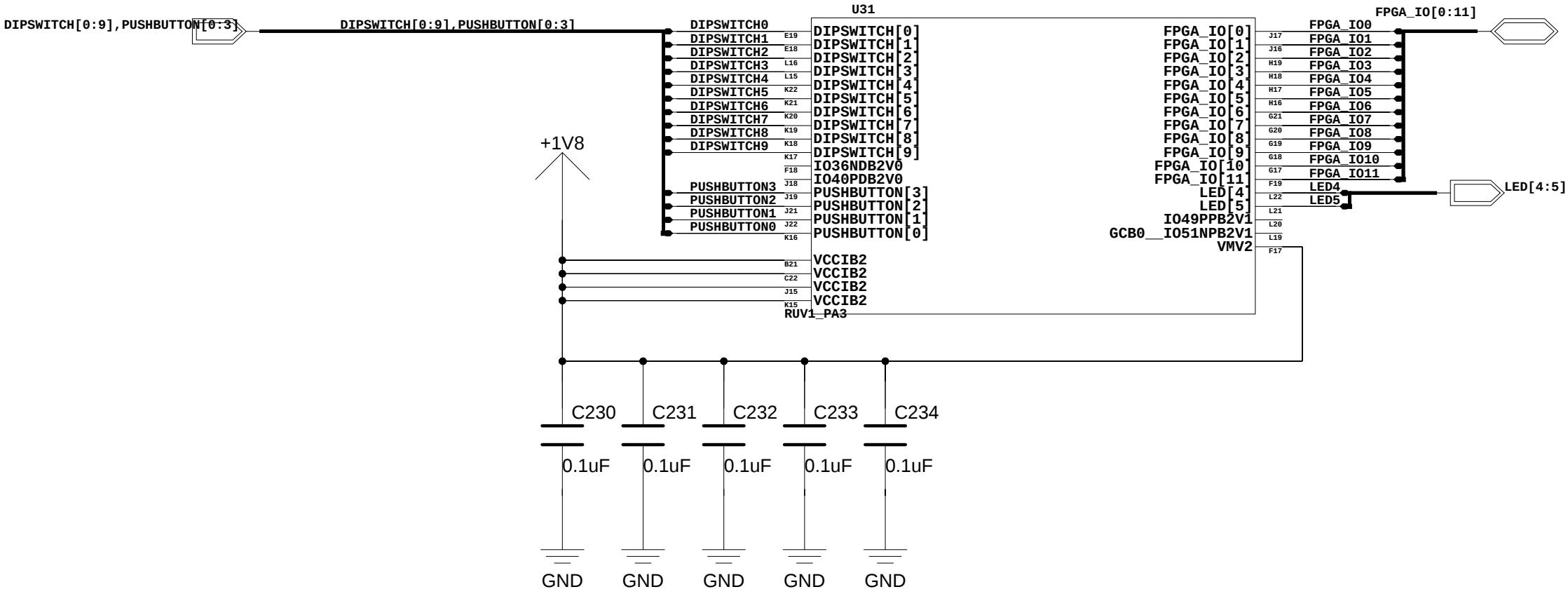
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Jody Wisman		IGF	BANK_UNUSED				
CHECKED			SIZE	PA3		VER	REV
DATE 28/04/2017:17:51			B				
TIME:			SHEET of 66				



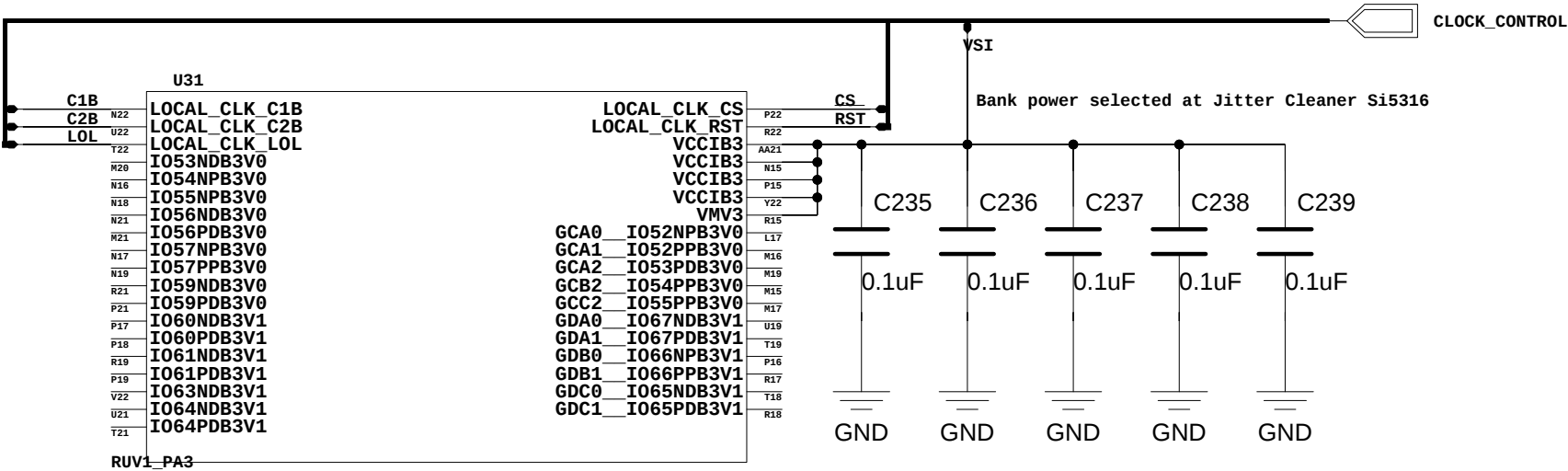
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APPROVALS						
DRAWN BY Jody Wisman		IGF	BANK5_3V3_FLASHPROM_CAN			
CHECKED			SIZE	PA3	VER	REV
DATE 28/04/2017:17:51						
TIME:			SHEET of 66			



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman		IGF	BANK0_1V5_FPGA_SCRUBBING			
CHECKED			SIZE	PA3	VER	REV
DATE 28/04/2017:17:51						
TIME:			SHEET 46 of 66			

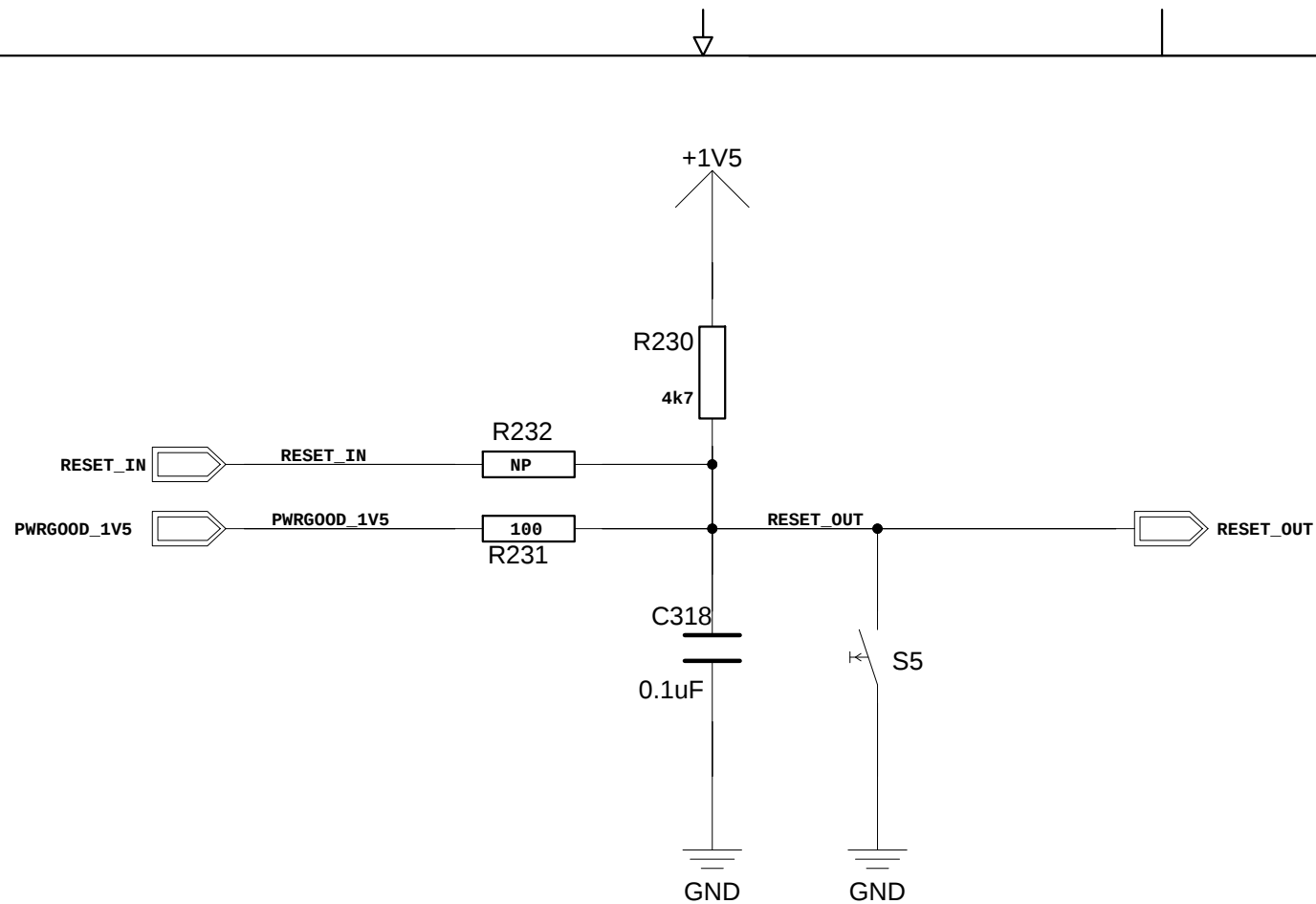


OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman		IGF	BANK2_1V8_DIPSWITCH_LED-FPGA_IO			
CHECKED			SIZE	PA3	VER	REV
DATE 28/04/2017:17:51						
TIME:			SHEET 47 of 66			



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Jody Wisman		IGF	BANK3_VSI_SI5316_CONFIG				
CHECKED			SIZE	PA3		VER	REV
DATE 28/04/2017:17:51			B				
TIME:			SHEET 48 of 66				





OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF						
CHECKED		SIZE	1 RESET			VER	REV
DATE	28/04/2017:17:51	A					
TIME:		SHEET 50 of 66					

4

3

2

1

D

D

C

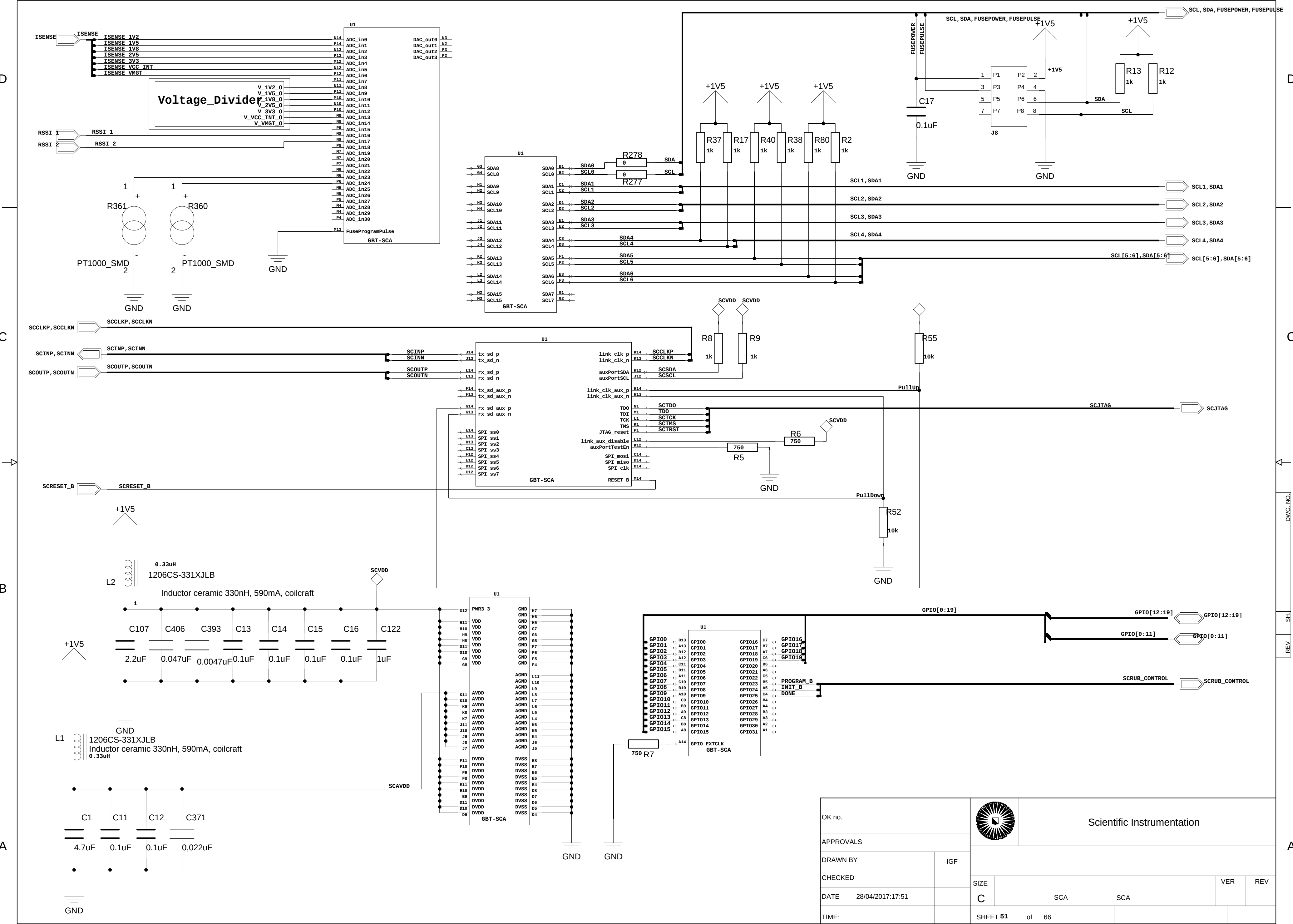
C

B

A

A

H=4ND; V=4LA



4

3

2

1

OK no.				Scientific Instrumentation	
APPROVALS					
DRAWN BY		IGF			
CHECKED					
DATE		28/04/2017:17:51			
TIME:					
		SIZE		VER	
		C		SCA	
		SHEET 51		of 66	

4

3

2

1

D

D

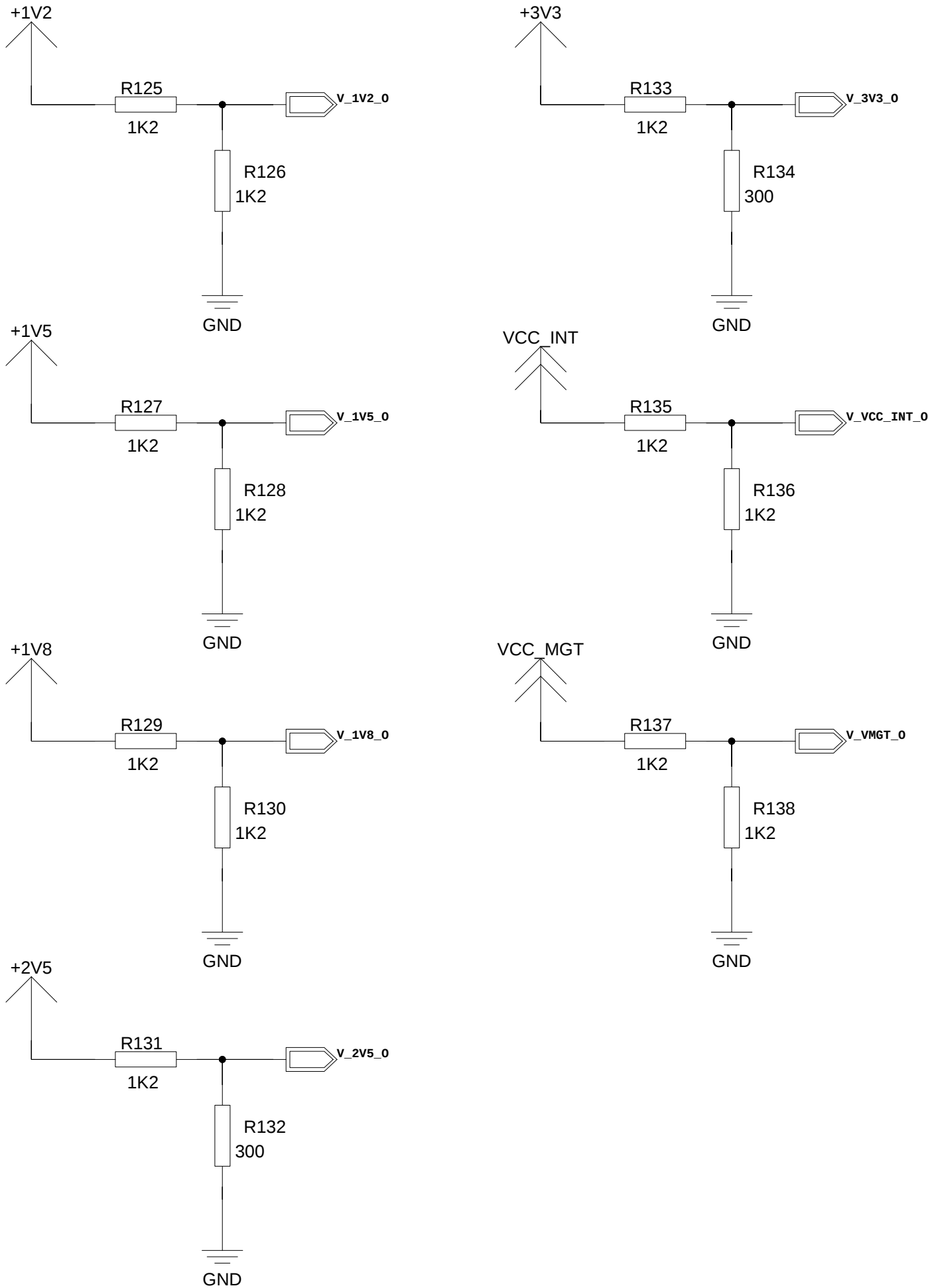
C

C

B

A

A



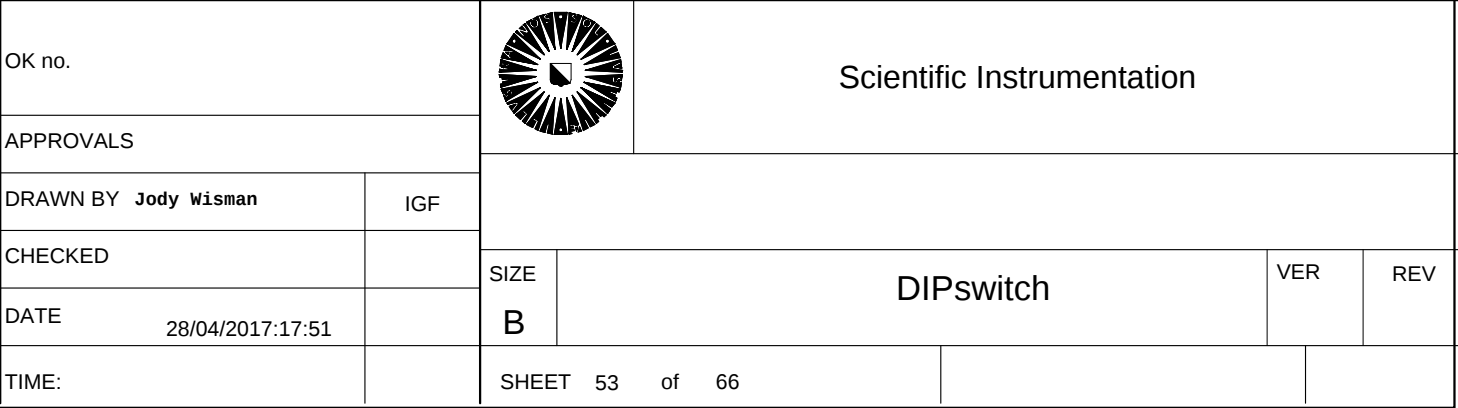
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF	Voltage_Divider					
CHECKED		SIZE				VER	REV
DATE	28/04/2017:17:51	C	1				
TIME:		SHEET 52 of 66					

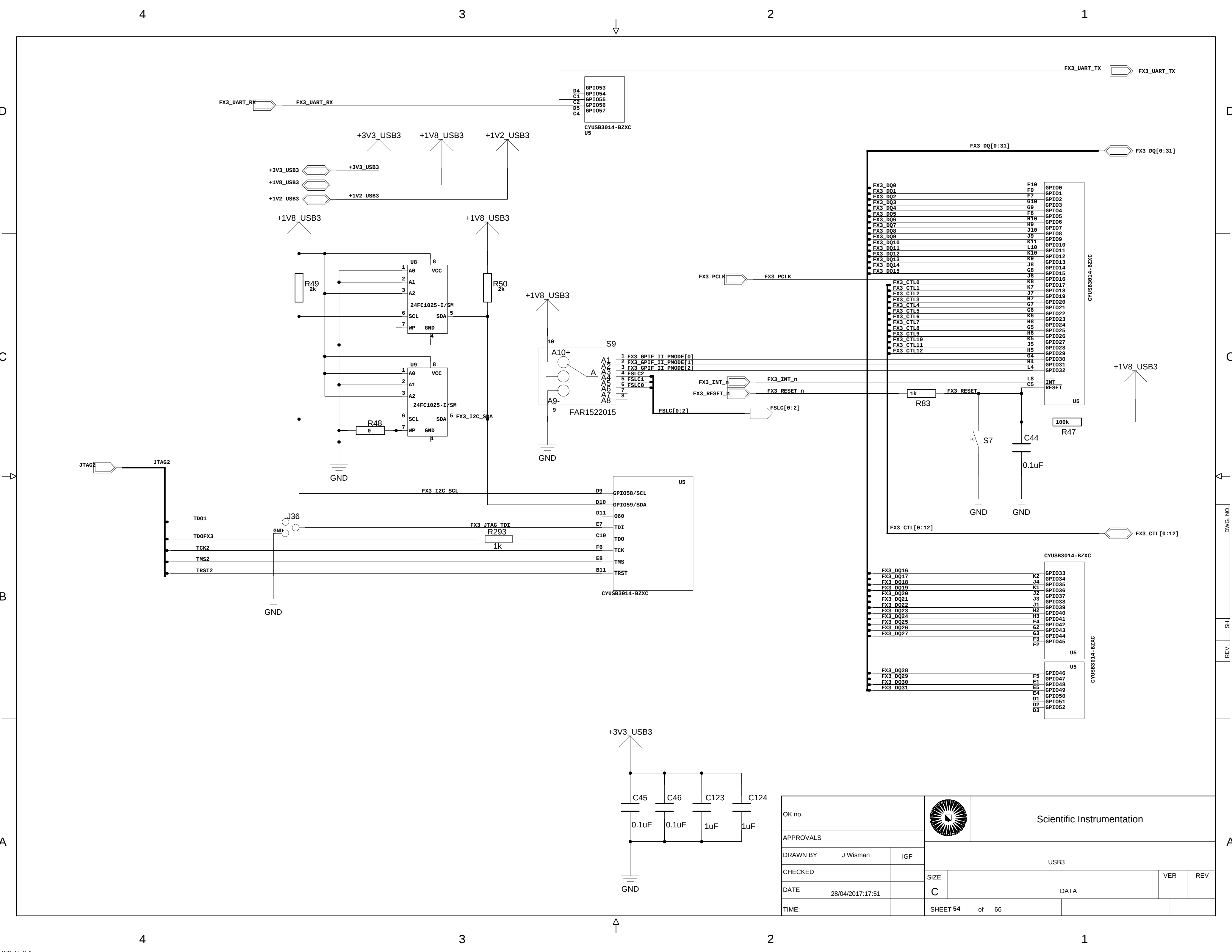
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3

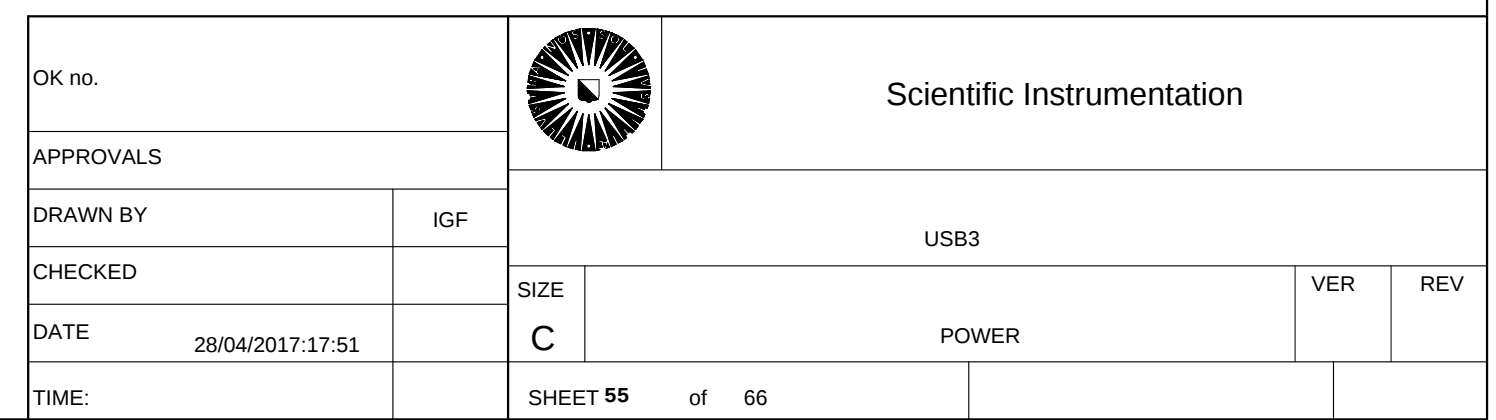
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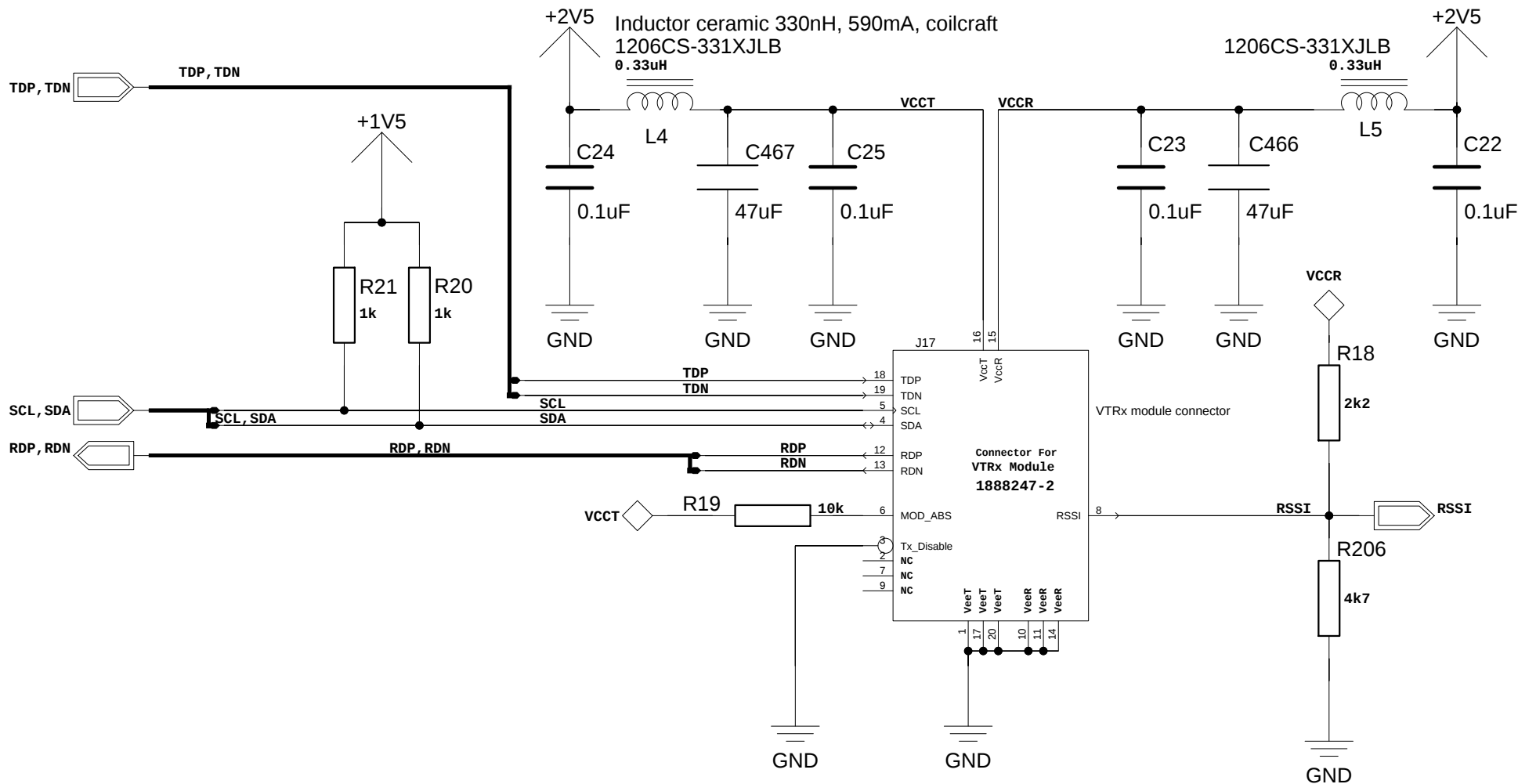
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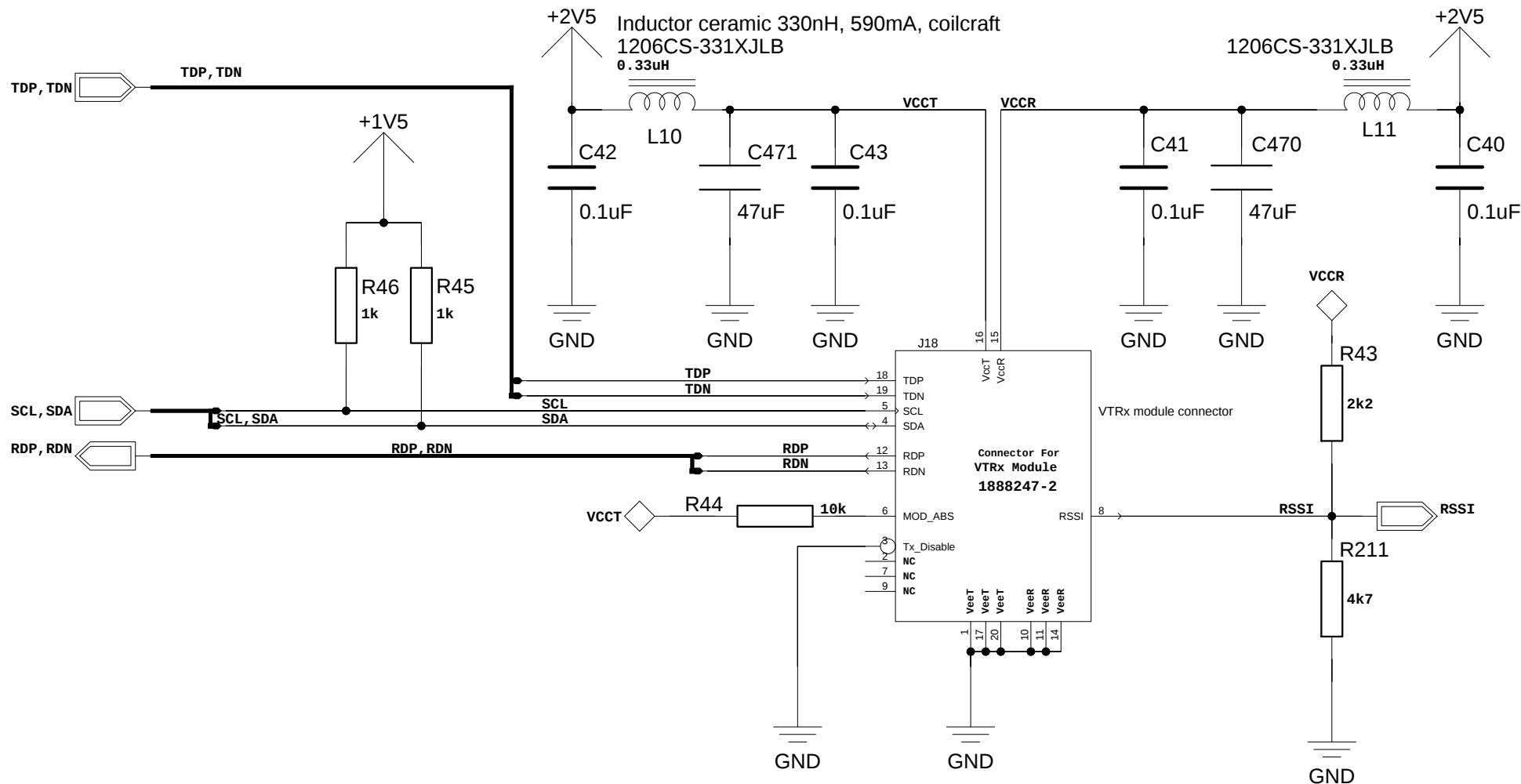


OK no.			Scientific Instrumentation			
APPROVALS			USB3			
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CHECKED						
DATE	28/04/2017:17:51	SIZE	DATA		VER	REV
TIME:		SHEET 54 of 66				

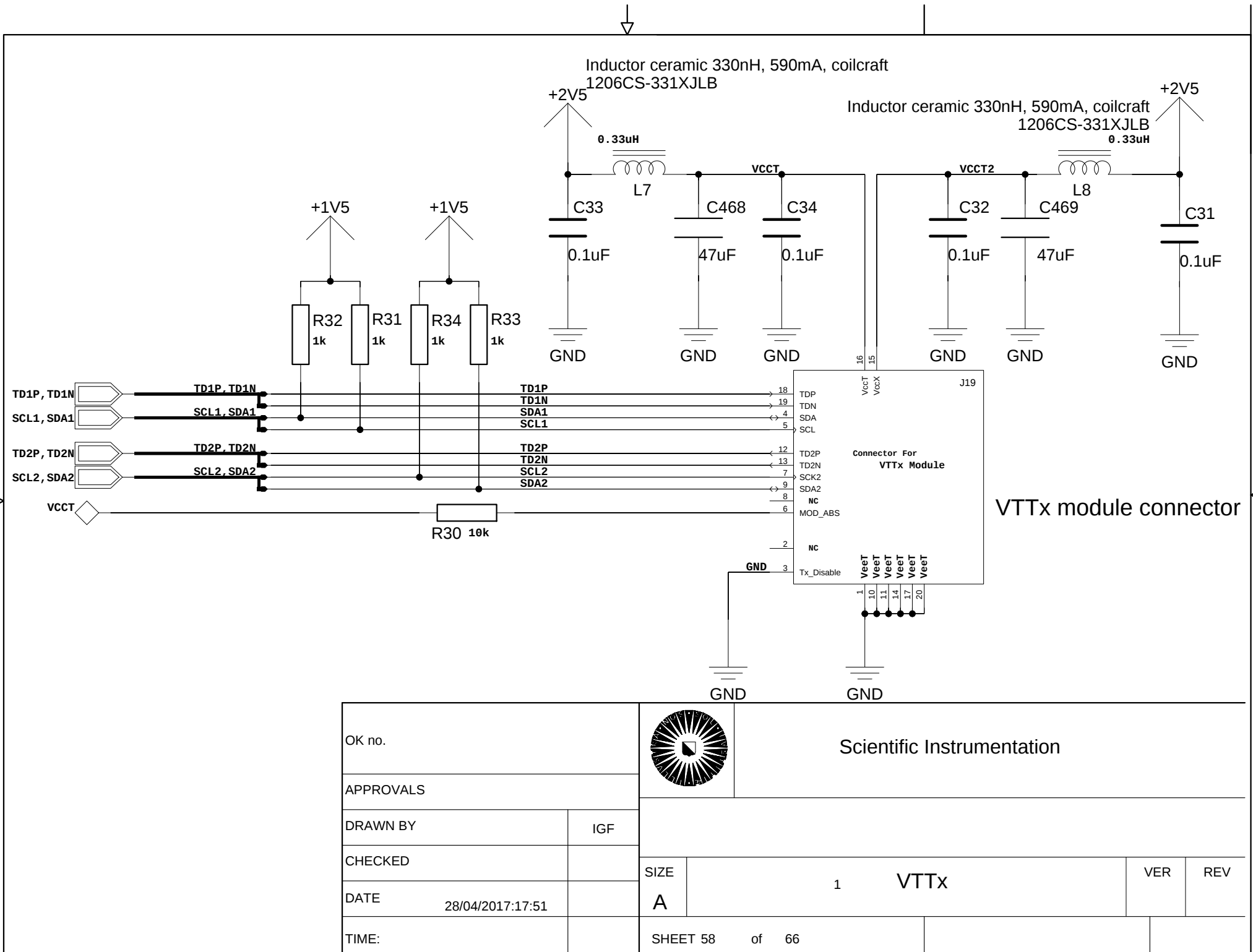




OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED		SIZE	1 VTRx1		VER	REV
DATE	28/04/2017:17:51	A				
TIME:		SHEET 56 of 66				



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF						
CHECKED		SIZE	1		VTRx2	VER	REV
DATE	28/04/2017:17:51	A					
TIME:		SHEET 57 of 66					



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED						
DATE	28/04/2017:17:51	SIZE	1 VTTx		VER	REV
		A				
TIME:		SHEET 58 of 66				

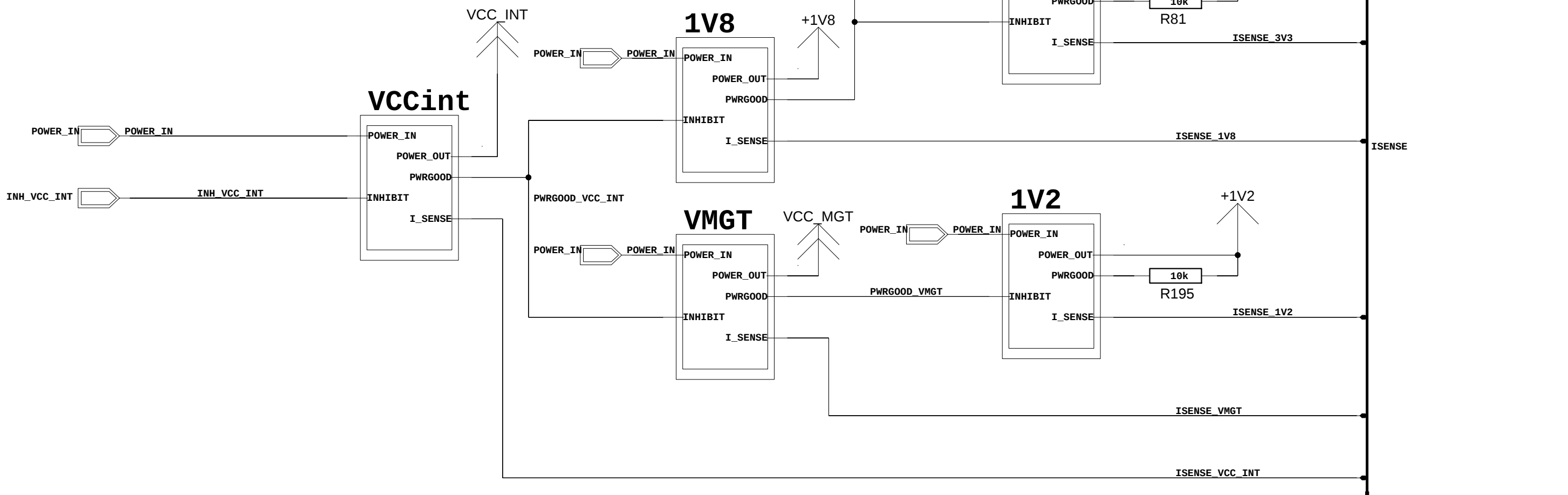
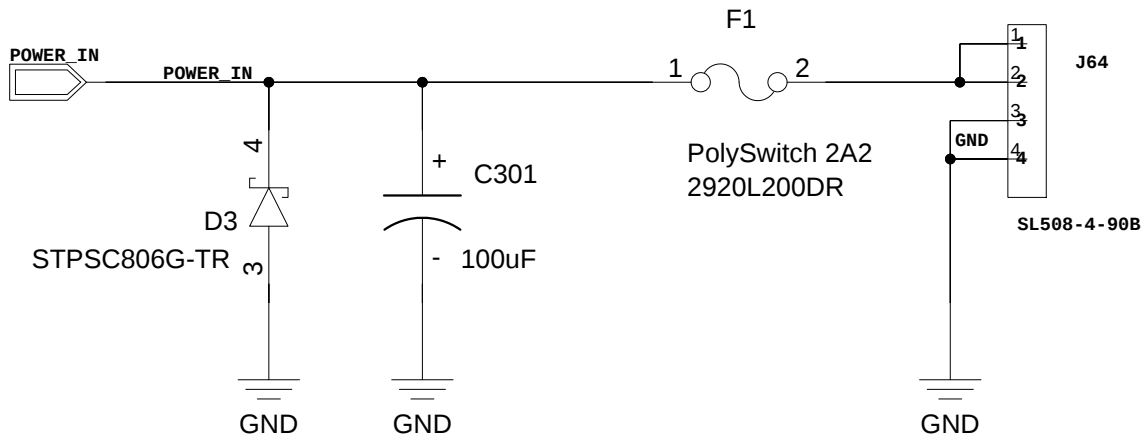
D

C

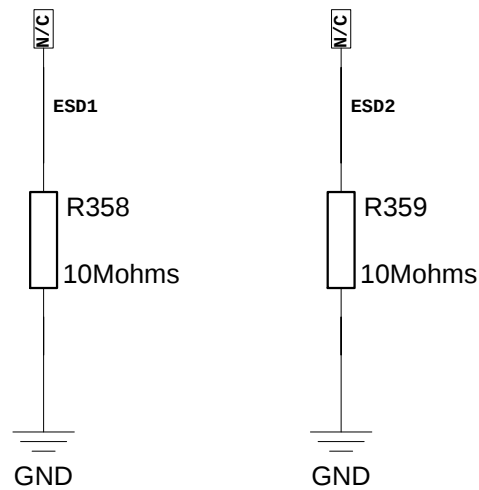
B

A

H=4ND; V=4LA



Power	(V)	R _{SET}	R _{RT}	Device	Rail	I(mA)	I _{TOTAL} (A)	R _{SENSE}	R _{DIV}
VCC_INT	0,95	2k45	1000k	XCKU060	V _{CCINT} , V _{CCINT_IO} , V _{CCBRAM}	4200	4,2	5	5 (1k2/300)
VCC_MGT	1	2k15	1000k	XCKU060	V _{MGTAVCC}	2300	2,3	10	10 (2k7/300)
+1V2	1.2	1k43	487k	XCKU060	V _{MGTAVTT}	2400	2,6	10	10 (2k7/300)
				FX3		200			
+1V5	1.5	953	487k	3*GBTx		3046	3,4	5	5 (1k2/300)
				SCA		64			
				PA3		336			
+1V8	1.8	715	169k	XCKU060	V _{CCAUX} , V _{CCAUXIO}	1581	1,6	10	10 (2k7/300)
					V _{MGTCCAUX}	19			
+2V5	2.5	453	90k9	PA3	V _{CCI_LVDS}	14	0,9	20	2 (1k2/1k2)
				V _{TTx} +2*V _{TRx}		900			
+3V3	3.3	318	90k9	7*AD626		2	24m	500	5 (1k2/300)
				2*CAN		1			
				FLASH_MEM		1			
				MLVDS_BUF		20			



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY J Wisman		IGF	POWER			
CHECKED			SIZE		VER	REV
DATE 28/04/2017:17:51		C	1			
TIME:			SHEET 59 of 66			

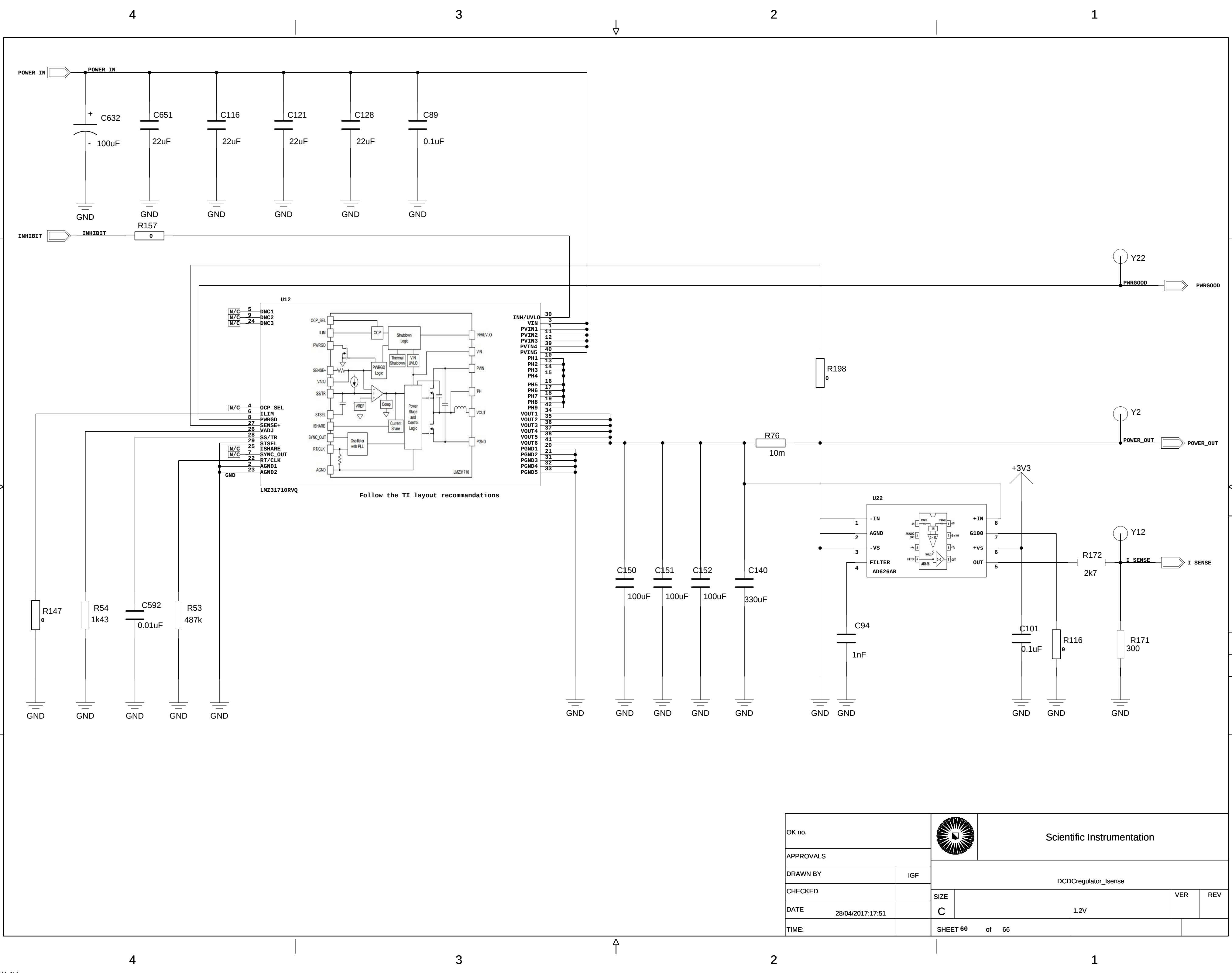
D

C

SH

REV

A



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF	DCDCregulator_1sense					
CHECKED		SIZE				VER	REV
DATE	28/04/2017:17:51	C	1.2V				
TIME:		SHEET 60 of 66					

D

C

B

A

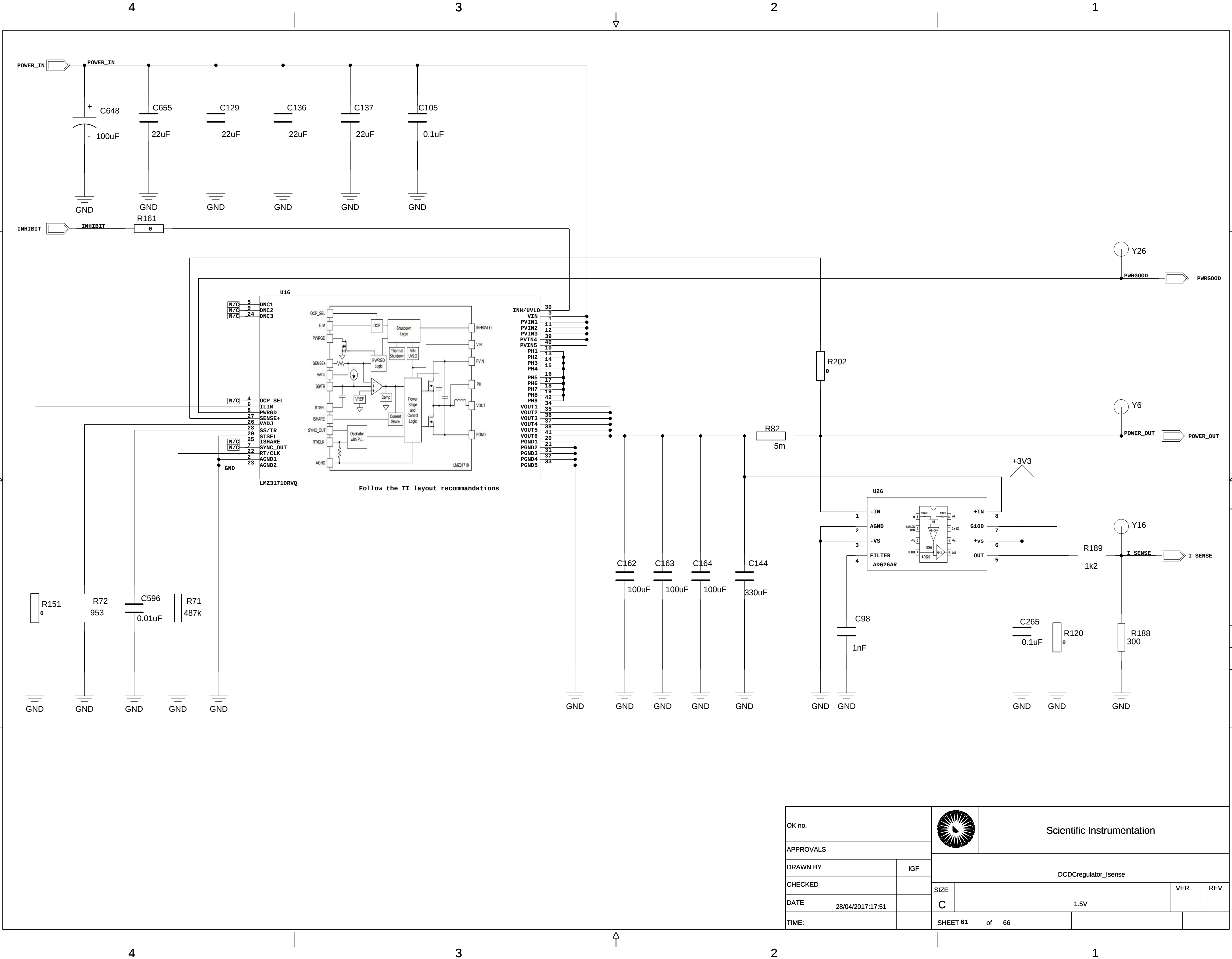
D

C

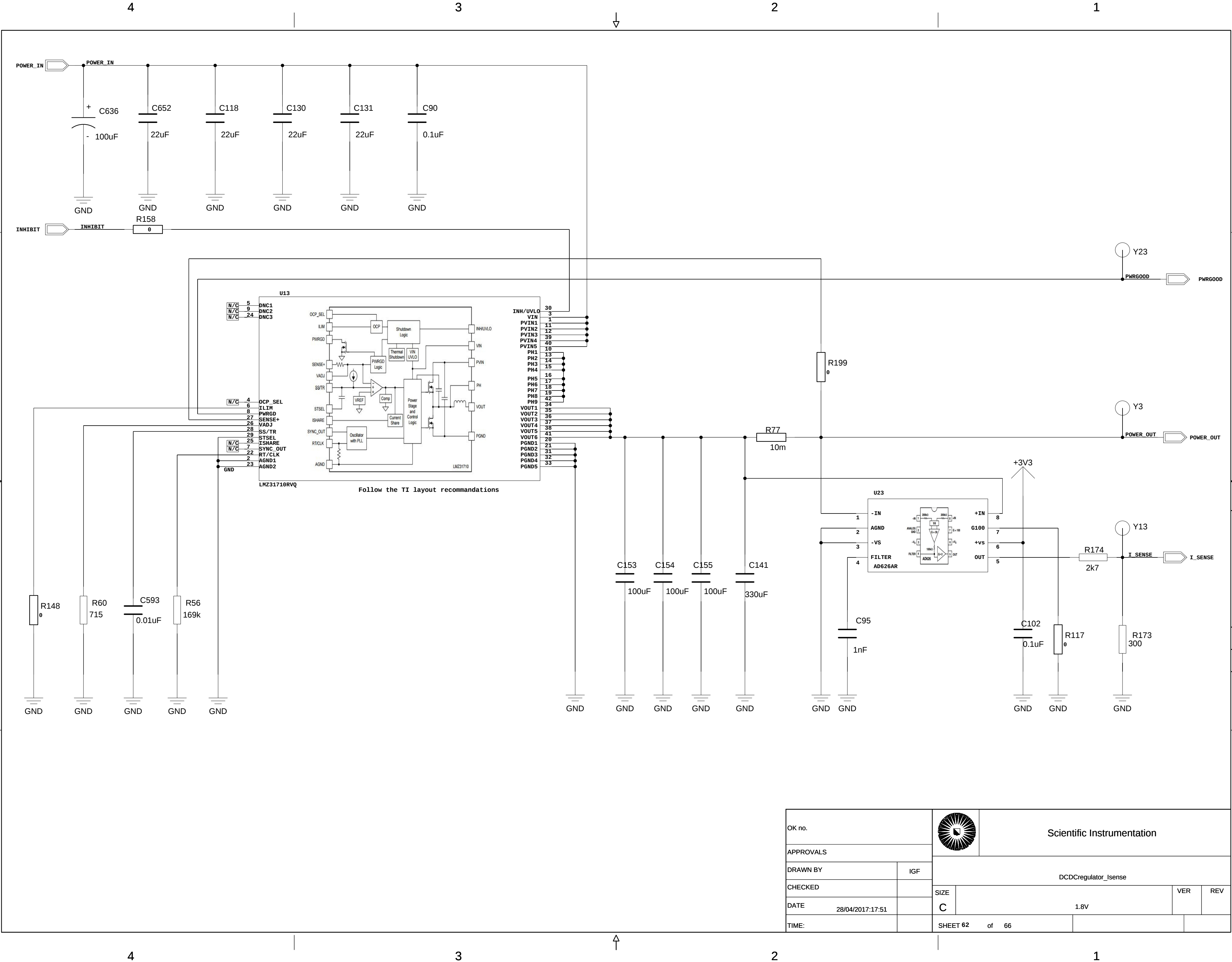
SH

REV

A



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	DCDCregulator_Isense				
CHECKED						
DATE	28/04/2017:17:51	SIZE	1.5V		VER	REV
		C				
TIME:		SHEET 61 of 66				



DWG.NO.

SH

REV

D

C

B

A

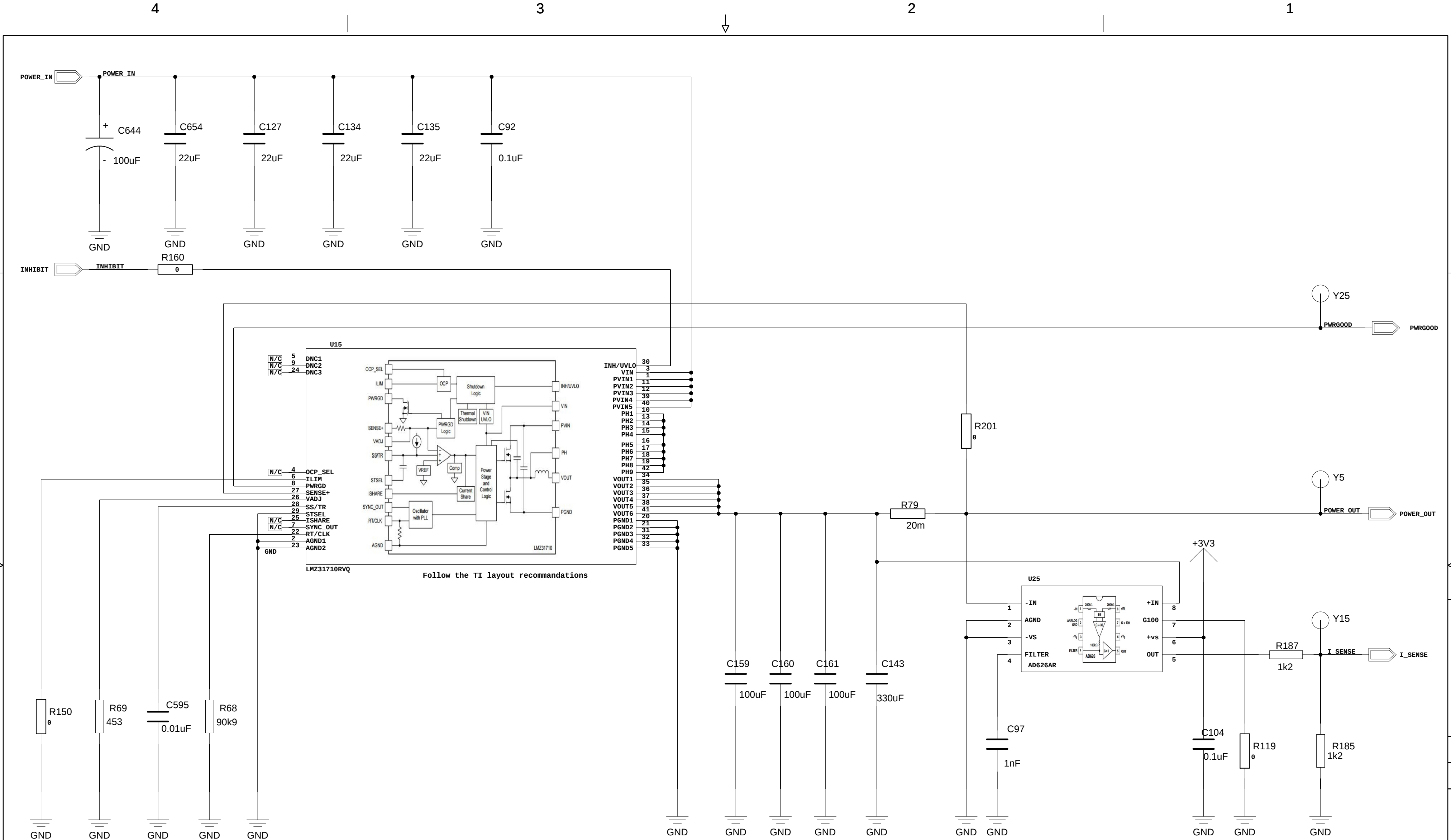
D

C

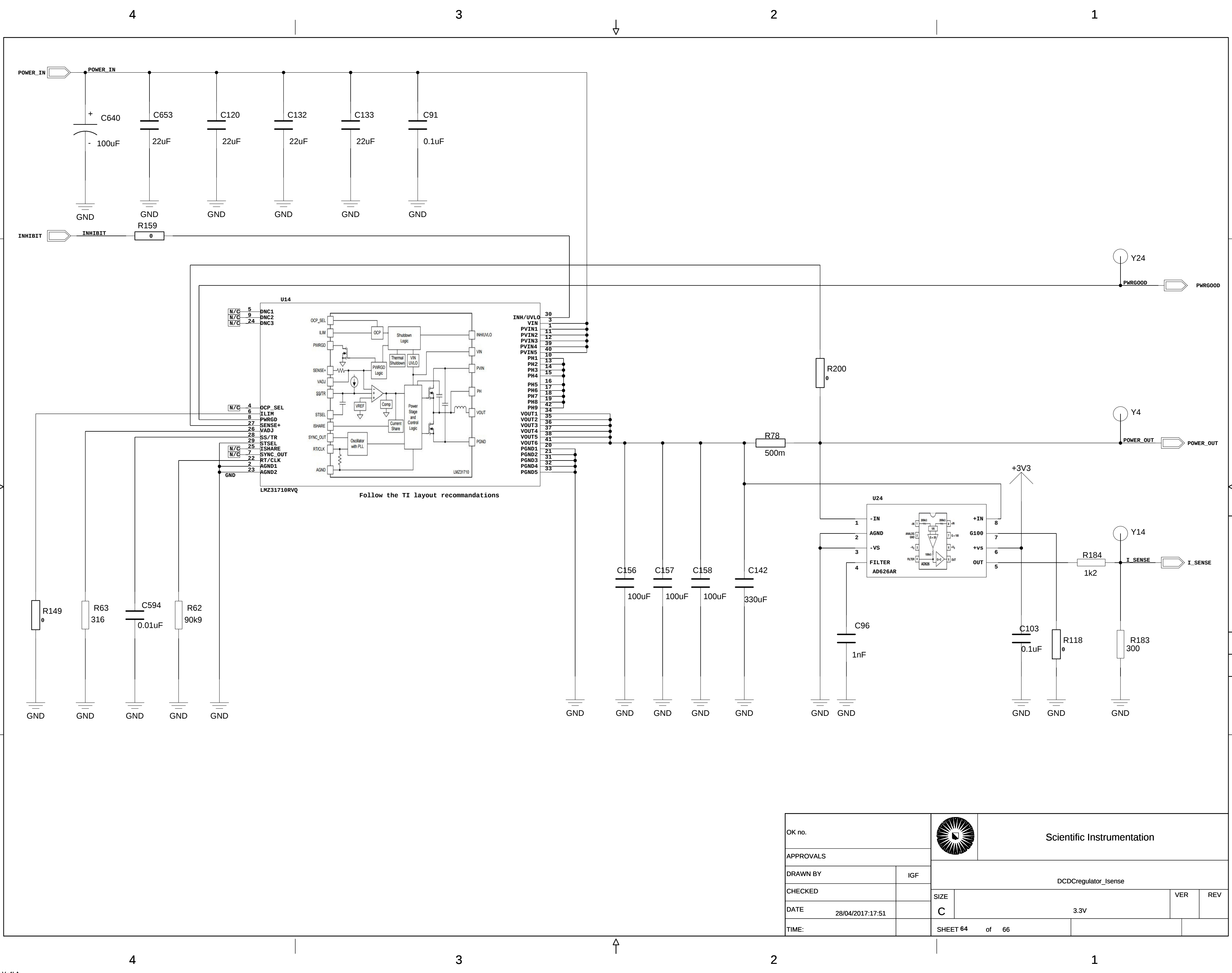
SH

REV

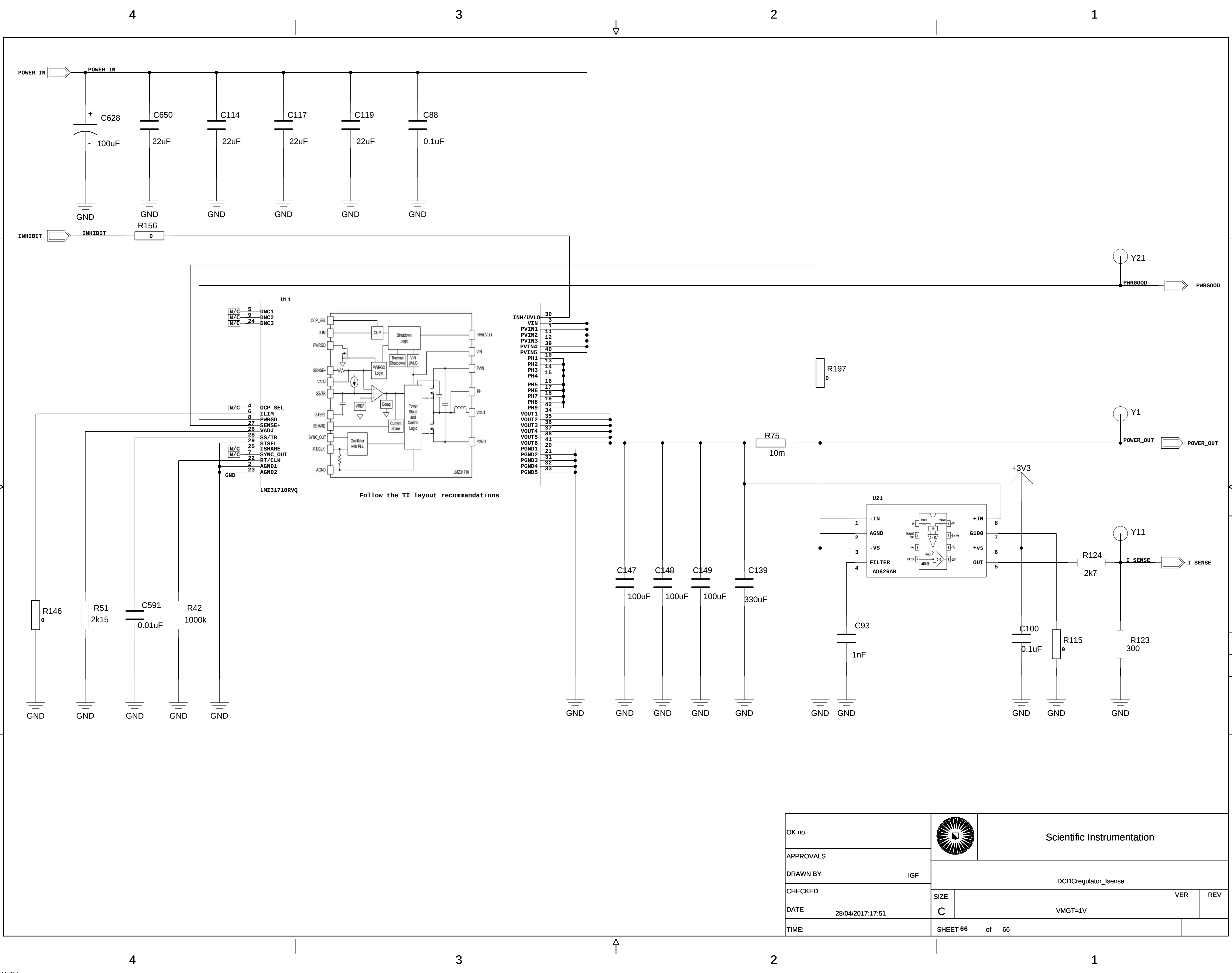
A



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF	DCDCregulator_Isense					
CHECKED		SIZE				VER	REV
DATE	28/04/2017:17:51	C	2.5V				
TIME:		SHEET 63 of 66					



OK no.		 Scientific Instrumentation			
APPROVALS					
DRAWN BY	IGF	DCDCregulator_Isense			
CHECKED		SIZE		VER	REV
DATE	28/04/2017:17:51	C	3.3V		
TIME:		SHEET 64 of 66			



OK no.		 Scientific Instrumentation			
APPROVALS		DCDCregulator_1sense			
DRAWN BY	IGF	SIZE	VMGT=1V		VER
CHECKED		C			REV
DATE	28/04/2017:17:51				
TIME:		SHEET 66 of 66			