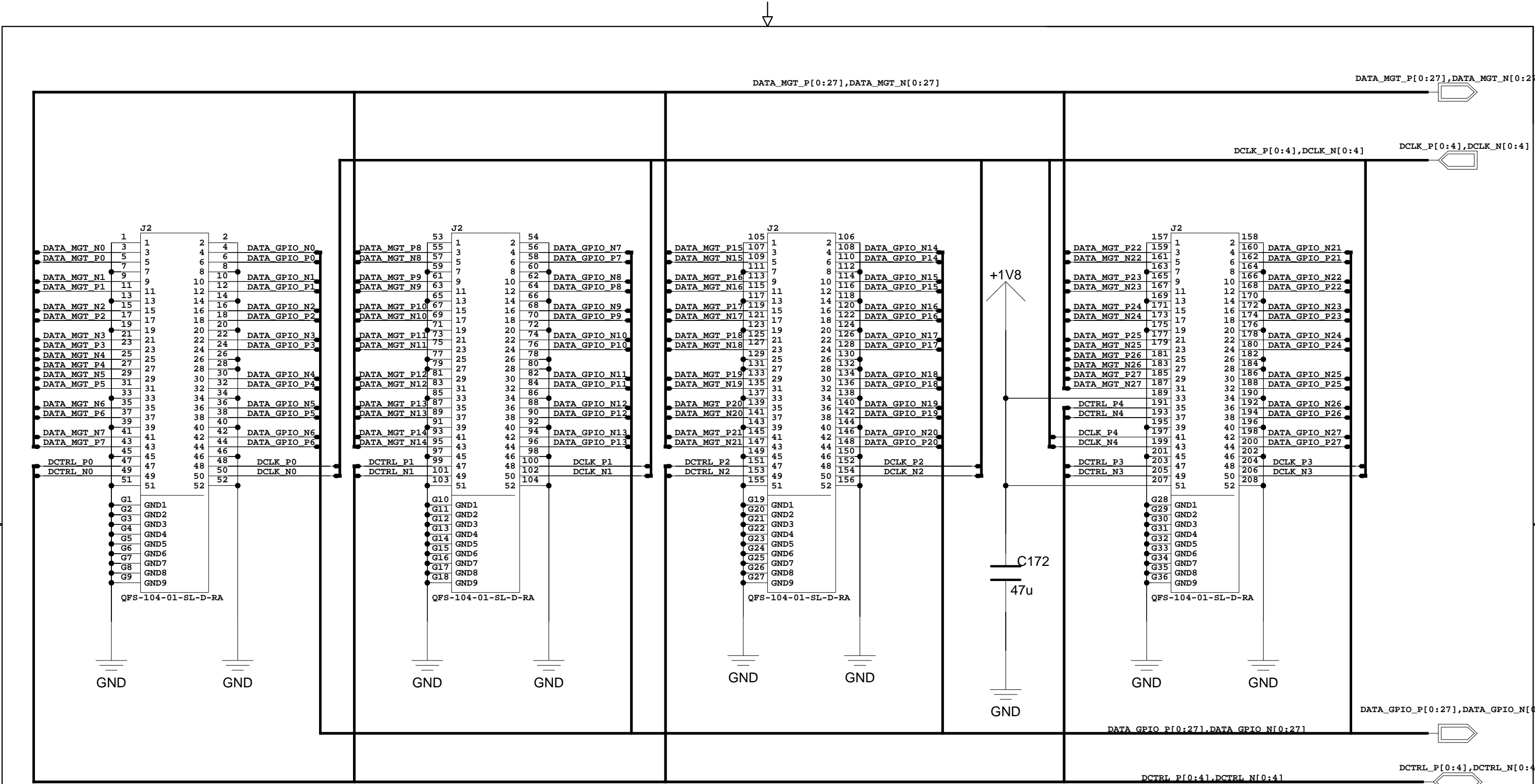

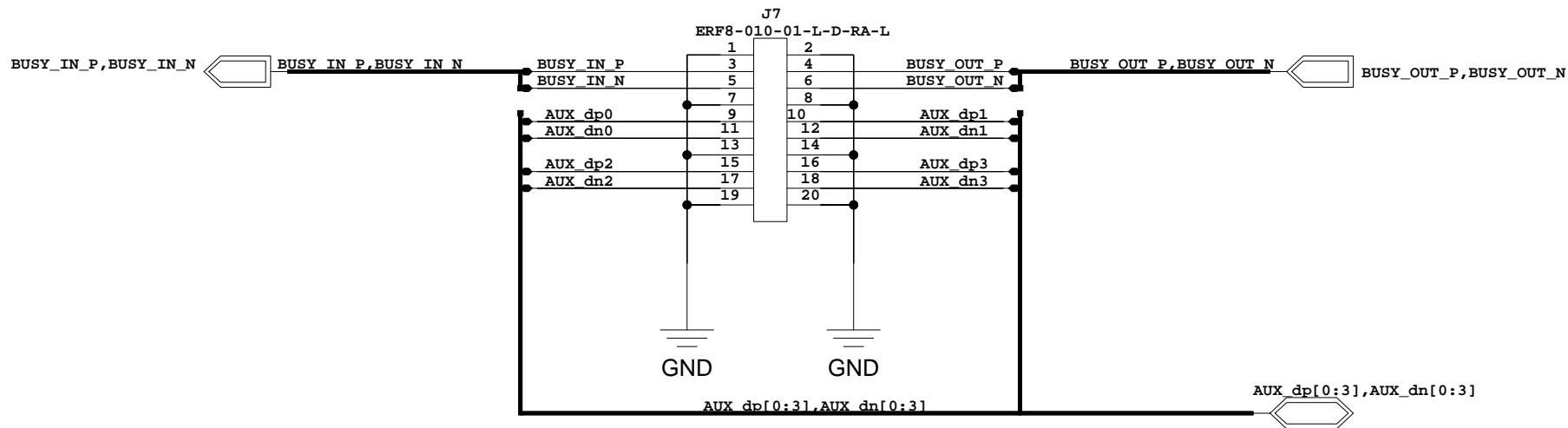



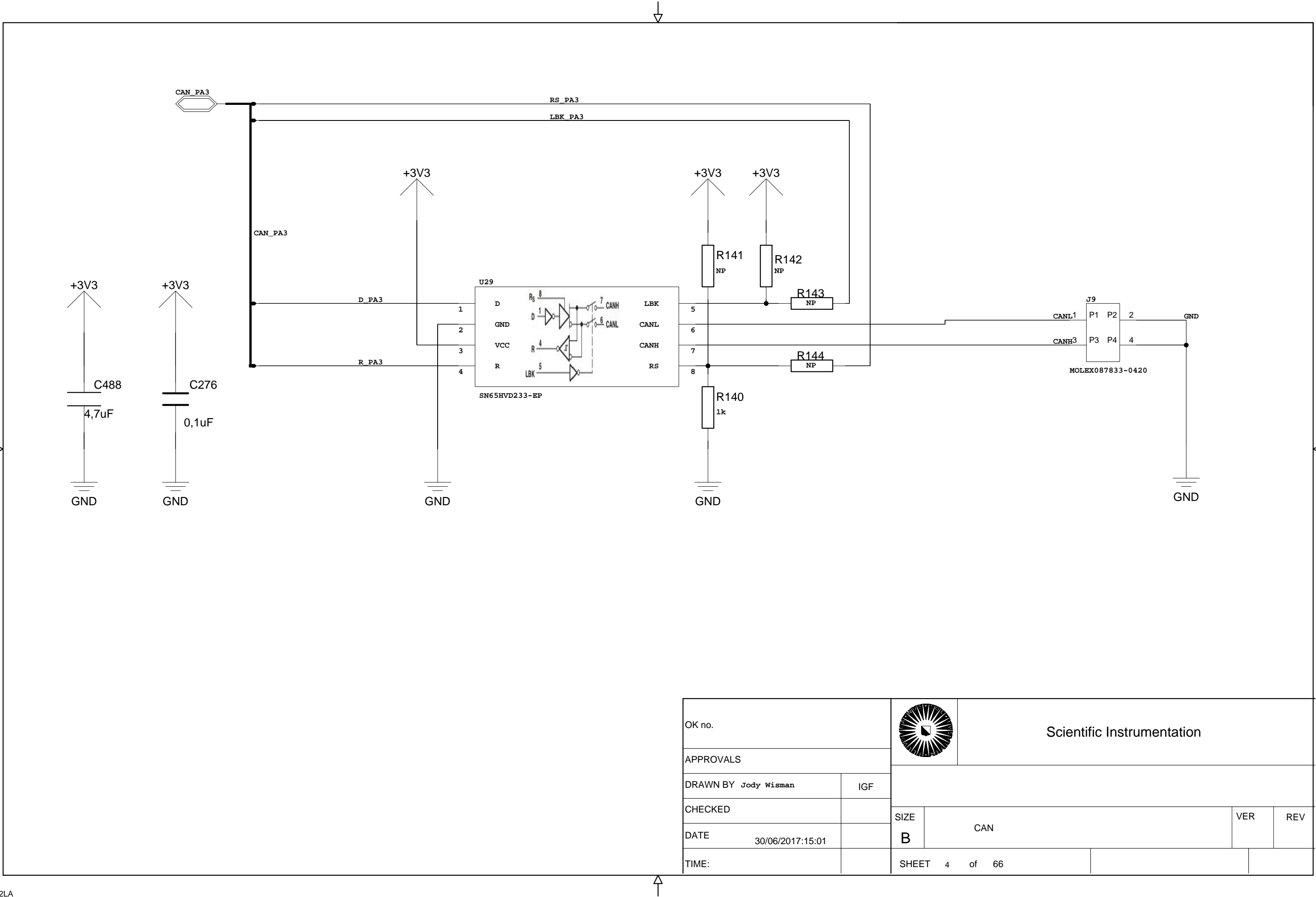
OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	RUv1				
CHECKED						
DATE	04/08/2017:15:12	SIZE	TOP		VER	REV
		C				
TIME:		SHEET 1 of 66				




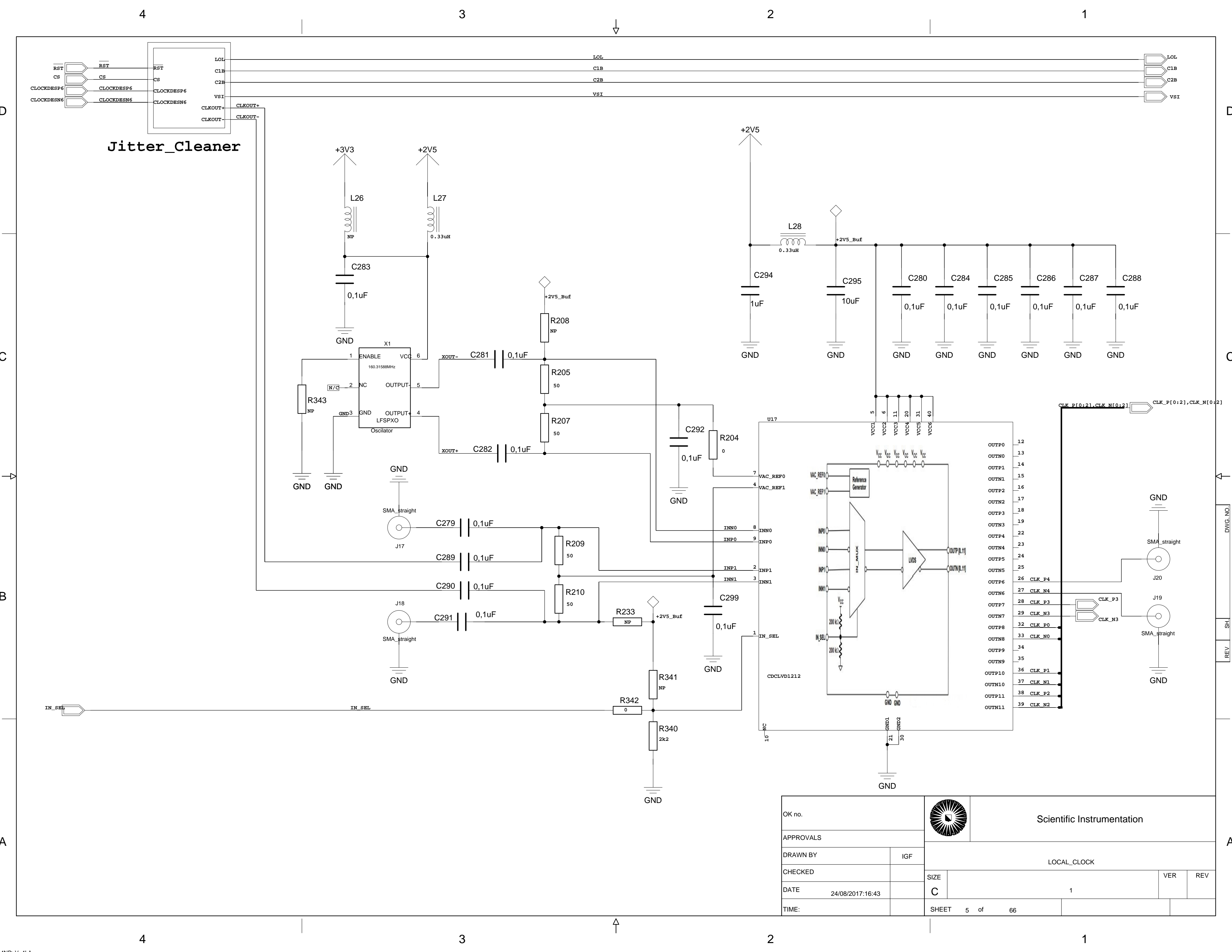
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Jody Wisman		IGF					
CHECKED							
DATE 30/06/2017:15:01			SIZE B	ALPIDE_TRANSISTIONBOARD_INTERFACE		VER	REV
TIME:			SHEET 2 of 66				




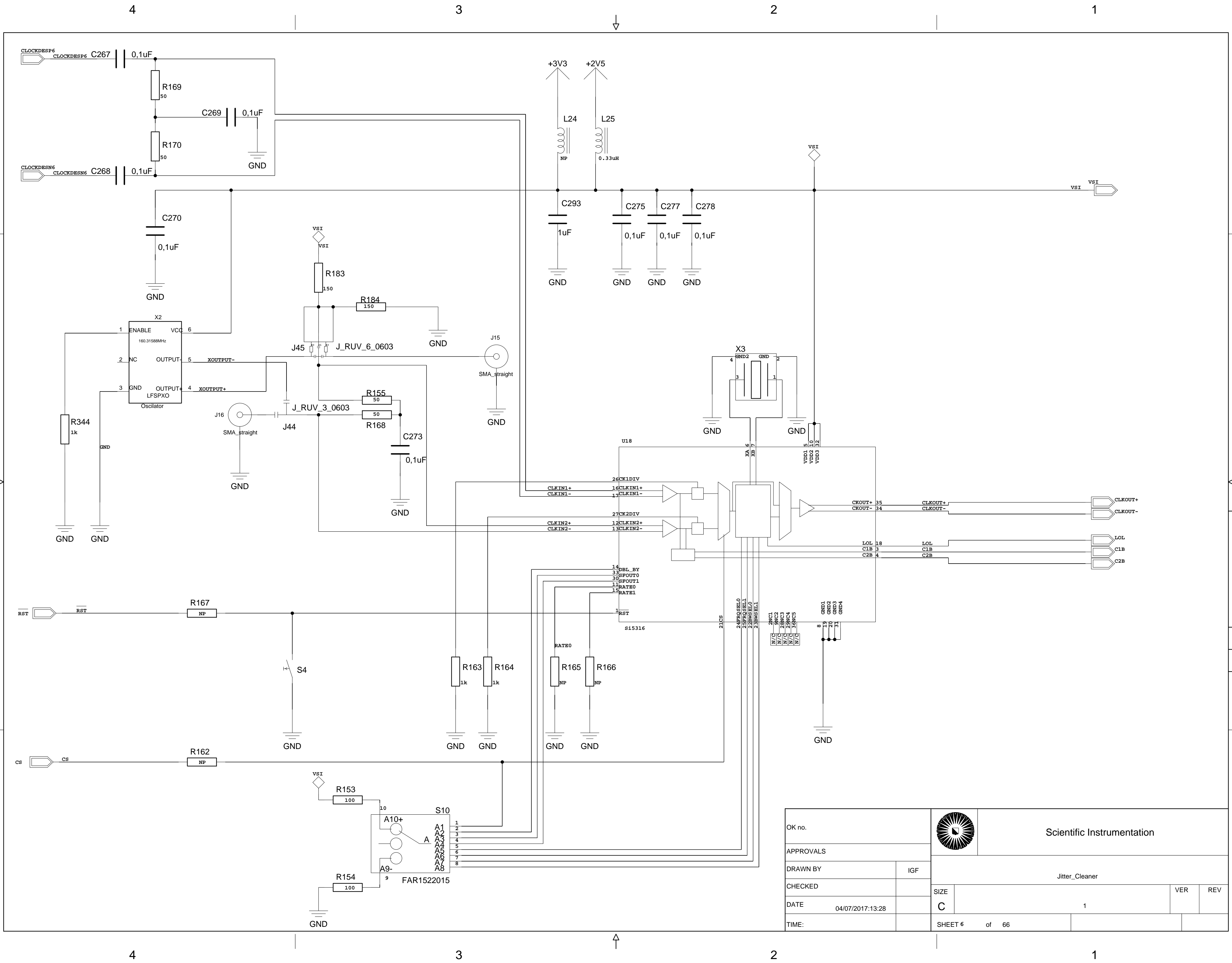
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY			IGF				
CHECKED							
DATE		30/06/2017:15:01		SIZE		A	
TIME:				SHEET		3 of 66	
				BUSY_AUX		VER	
						REV	




OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman	IGF					
CHECKED		SIZE	CAN		VER	REV
DATE 30/06/2017:15:01		B				
TIME:		SHEET 4 of 66				



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	LOCAL_CLOCK				
CHECKED						
DATE	24/08/2017:16:43	SIZE	1		VER	REV
TIME:		SHEET 5 of 66				



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	Jitter_Cleaner				
CHECKED						
DATE	04/07/2017:13:28	SIZE	1		VER	REV
TIME:		SHEET 6 of 66				



ALPIDE\_DATA\_MGT\_P[0:27],ALPIDE\_DATA\_MGT\_N[0:27]

GBTX\_CLOCKDESP[0:3],GBTX\_CLOCKDESN[0:3]

BUSY\_IN\_P,BUSY\_IN\_N

BUSY\_IN\_N

C319

0,1uF

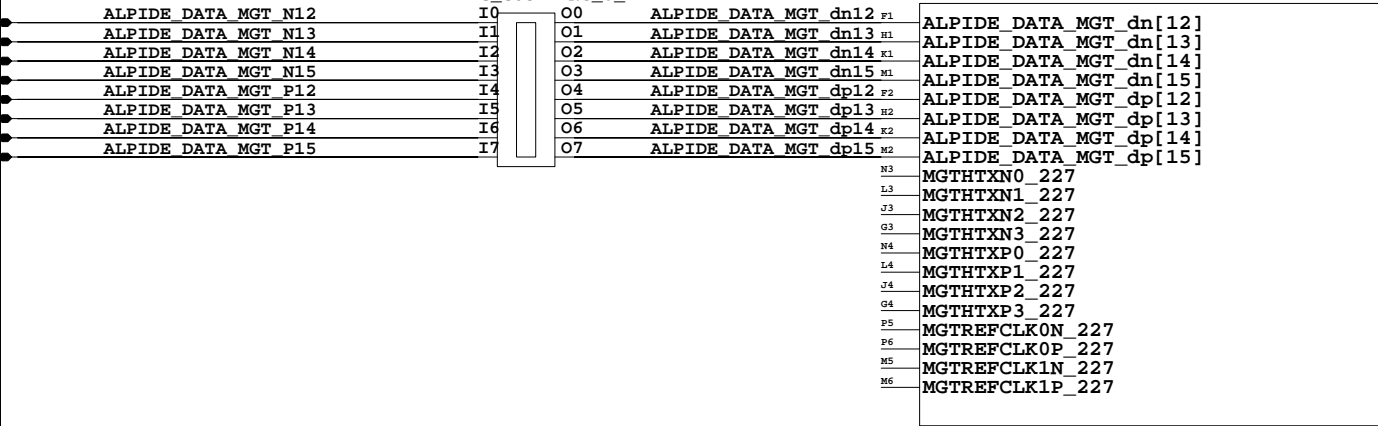
BUSY\_IN\_P

C320

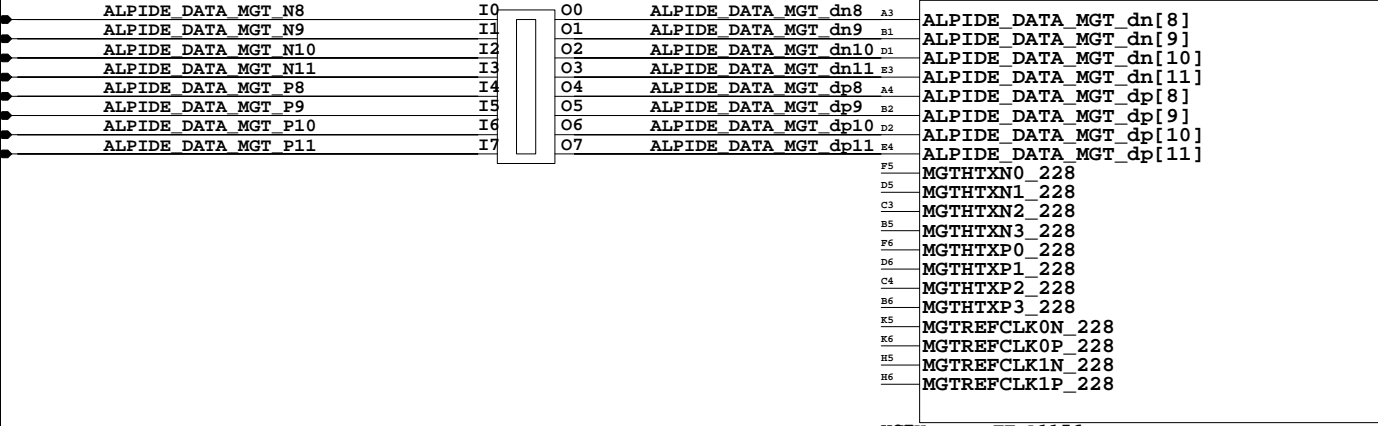
0,1uF

LOCAL\_CLK\_P[0:2],LOCAL\_CLK\_N[0:2]

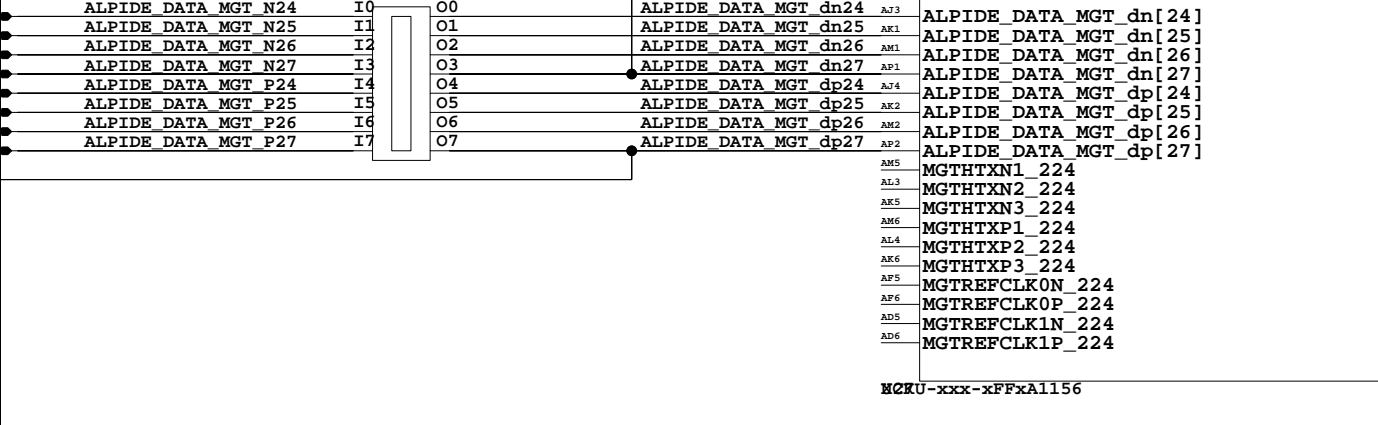
AC COUPLING 8\_1



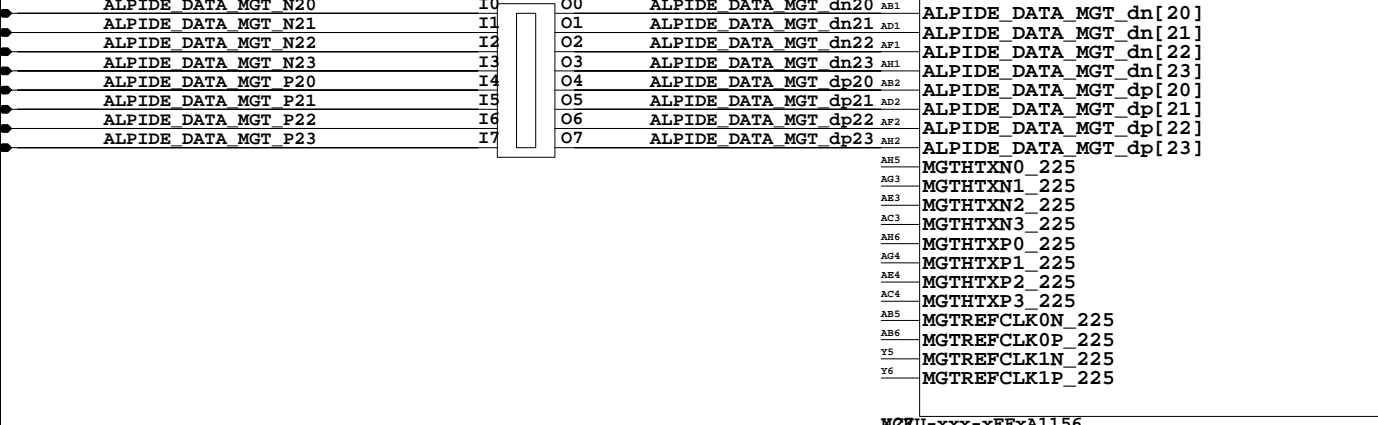
AC COUPLING 8\_2



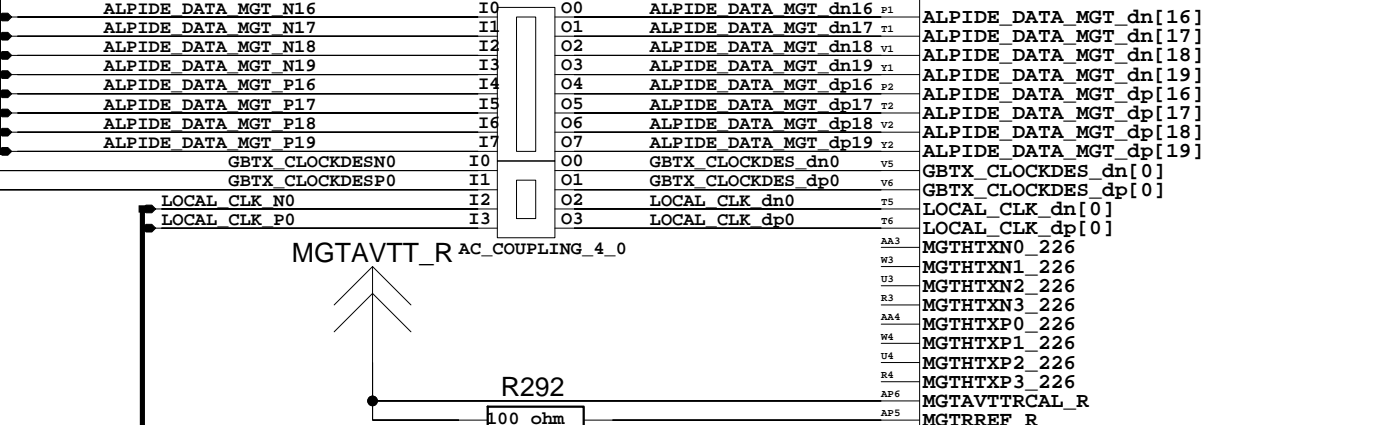
AC COUPLING 8\_3



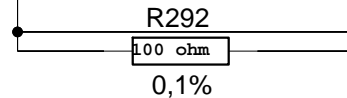
AC COUPLING 8\_4



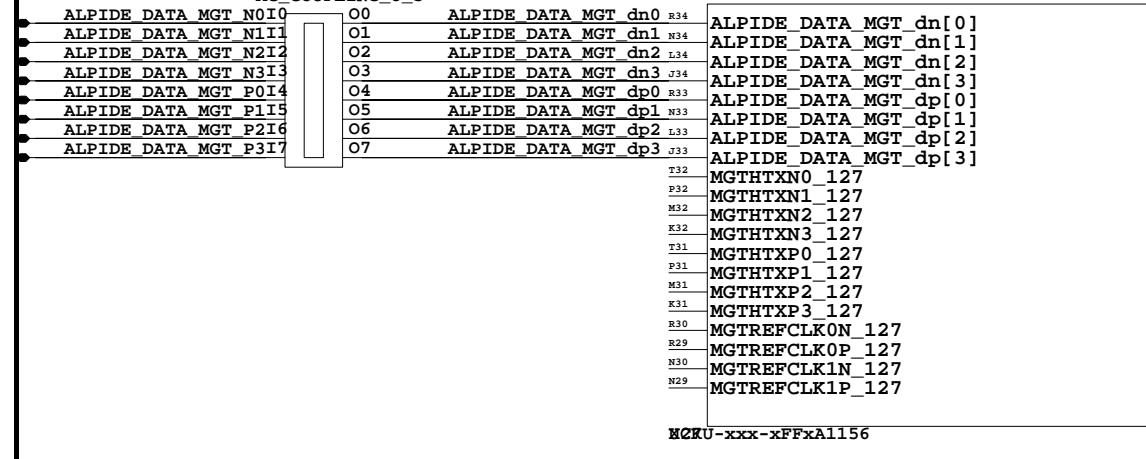
AC COUPLING 8\_0



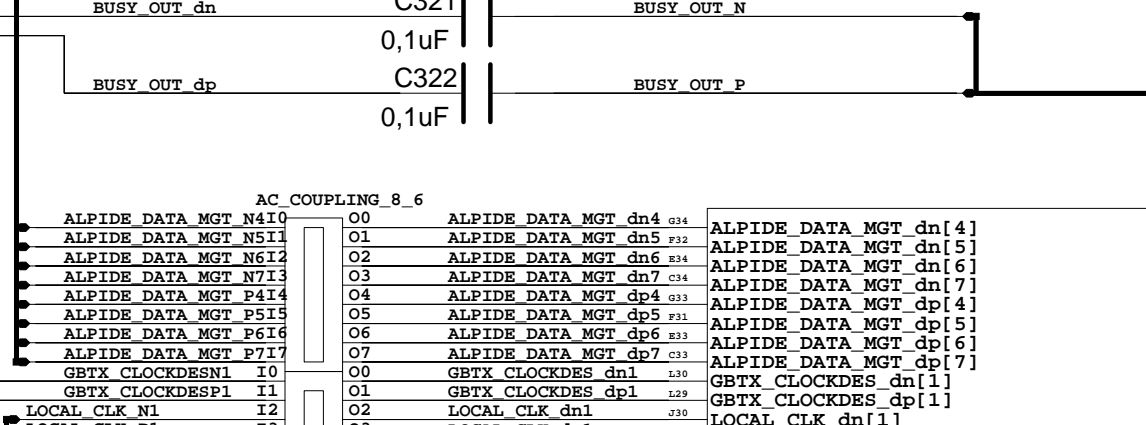
MGTAVTT\_R AC COUPLING 4\_0



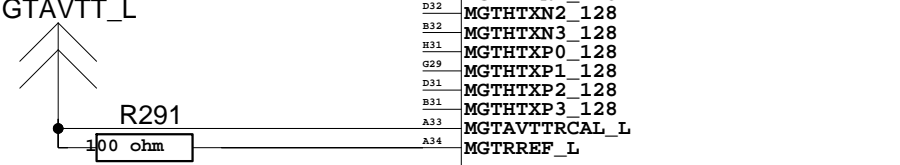
AC COUPLING 8\_5




AC COUPLING 8\_6

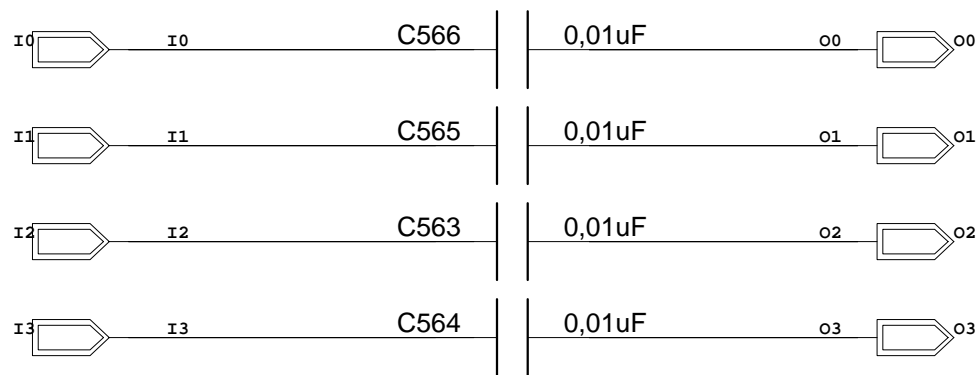


AC COUPLING 4\_1

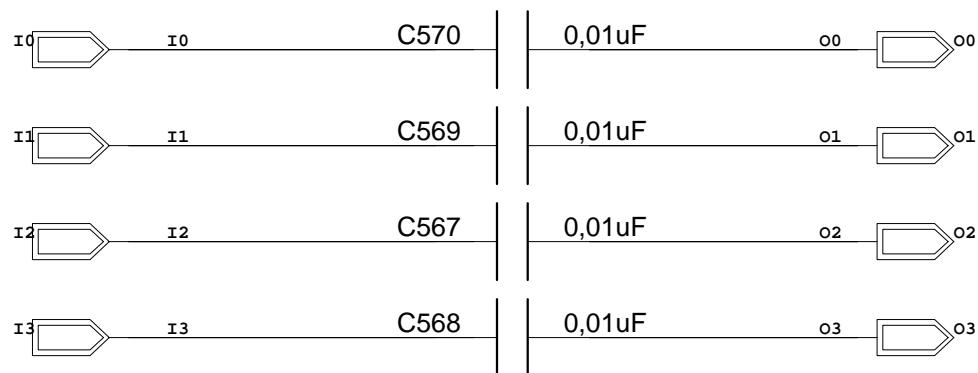



OK no.		 Scientific Instrumentation			
APPROVALS					
DRAWN BY	IGF	FPGA			
CHECKED		SIZE	BANKMGT_ALPIDE_HSDATA		VER REV
DATE	30/06/2017:15:01	C			
TIME:		SHEET 8	of	66	

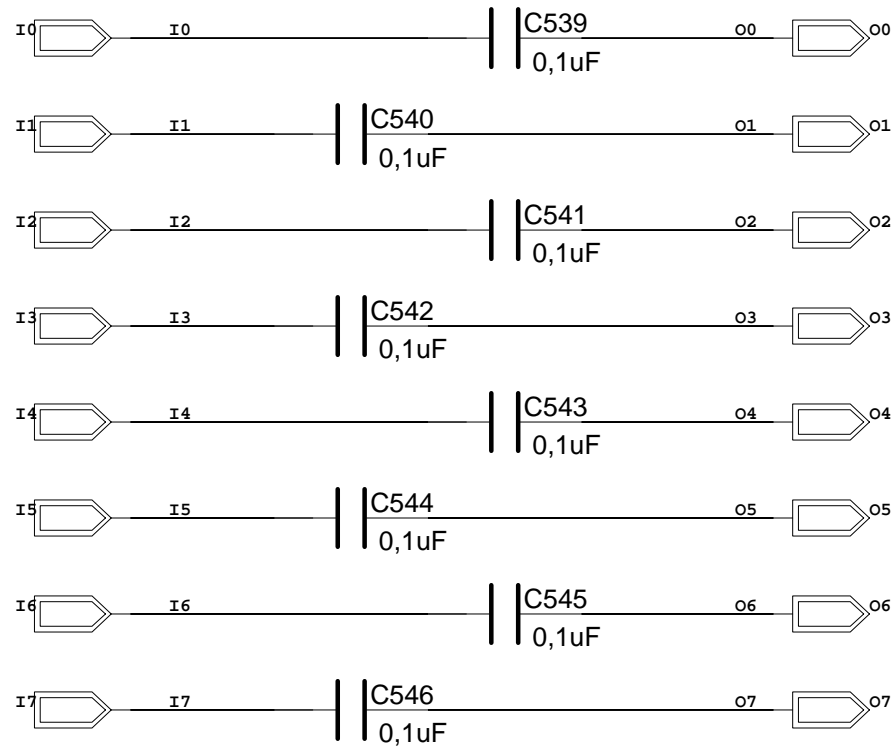





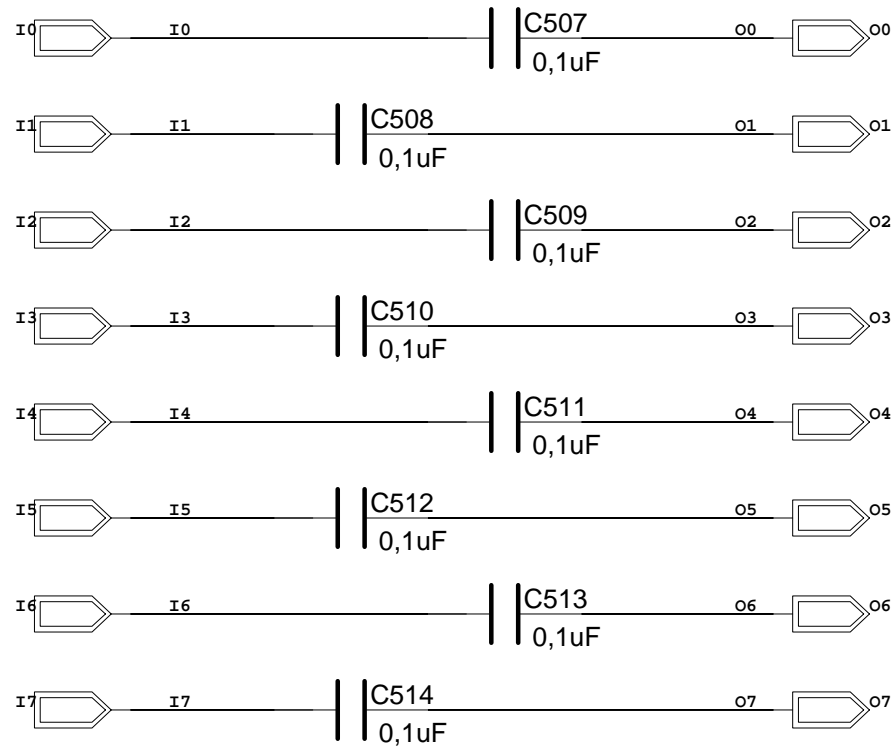
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY		IGF					
CHECKED							
DATE		30/06/2017:15:01	SIZE	1	AC_COUPLING_4_0	VER	REV
TIME:			SHEET 9 of 66				




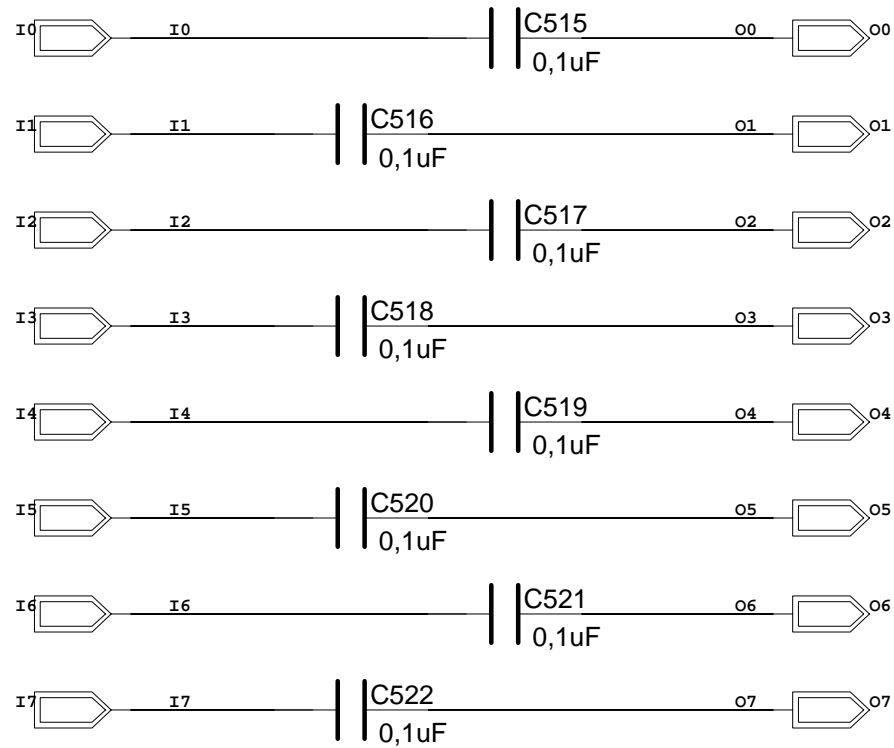
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY		IGF					
CHECKED							
DATE		30/06/2017:15:01	SIZE	1 AC_COUPLING_4_1		VER	REV
TIME:			SHEET 10 of 66				




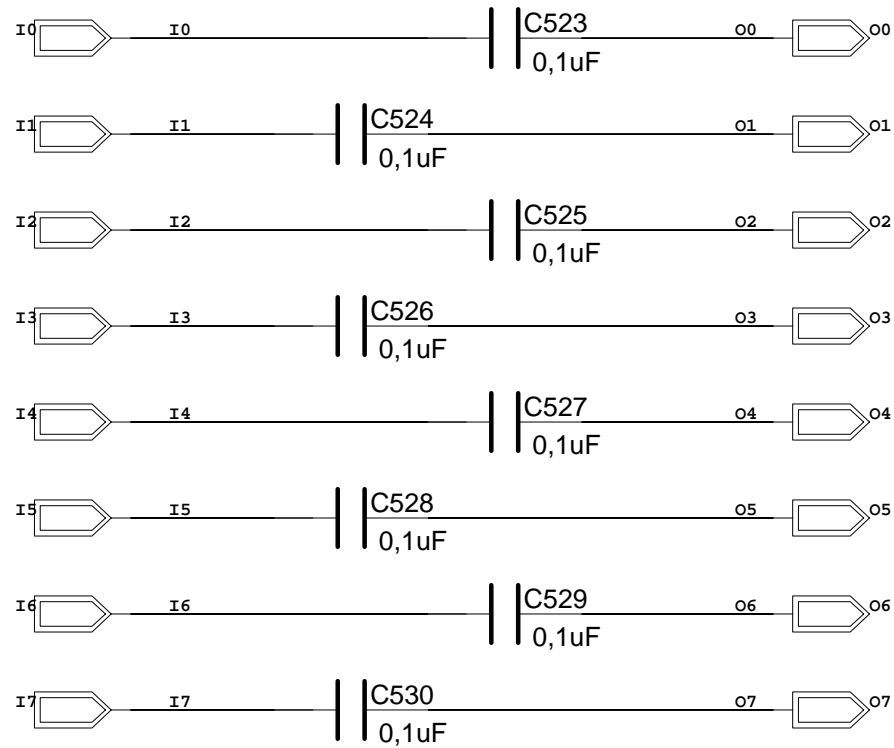
OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED		SIZE	AC_DECOUPLING_8_0		VER	REV
DATE	30/06/2017:15:01	A				
TIME:		SHEET 11 of 66				




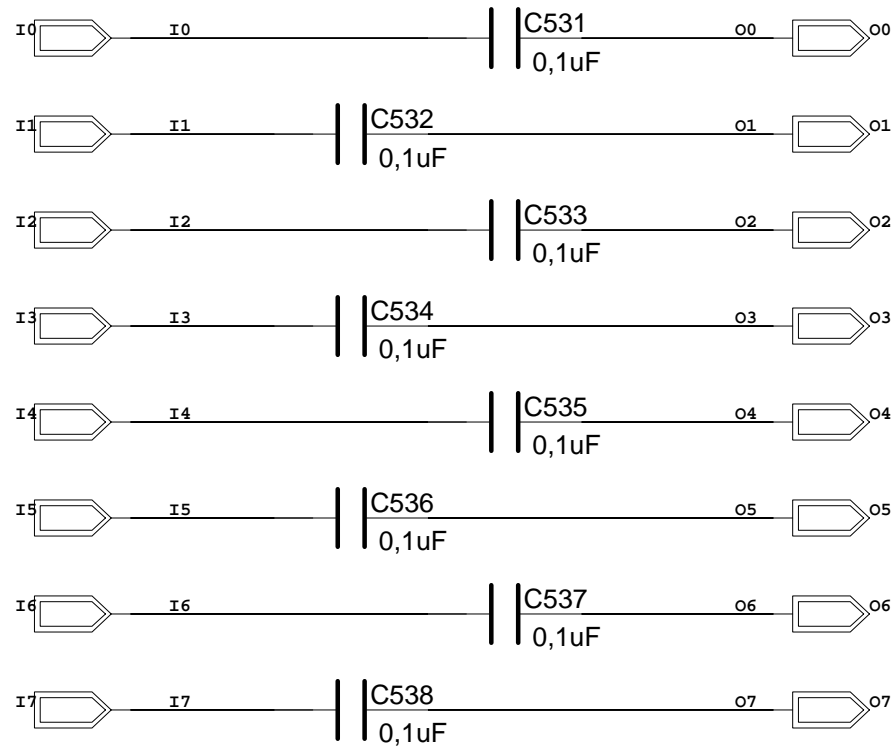
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF						
CHECKED		SIZE	AC_DECOUPLING_8_1			VER	REV
DATE	30/06/2017:15:01	A					
TIME:		SHEET 12 of 66					




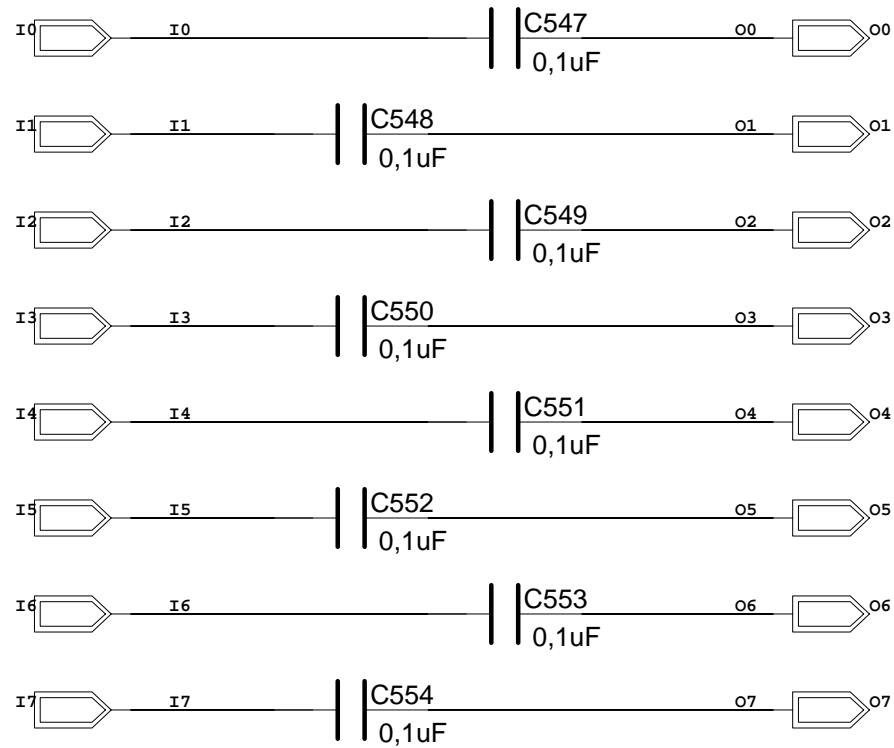
OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED		SIZE A	AC_DECOUPLING_8_2		VER	REV
DATE	30/06/2017:15:01					
TIME:		SHEET 13 of 66				




OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED		SIZE A	AC_DECOUPLING_8_3		VER	REV
DATE	30/06/2017:15:01					
TIME:		SHEET 14 of 66				

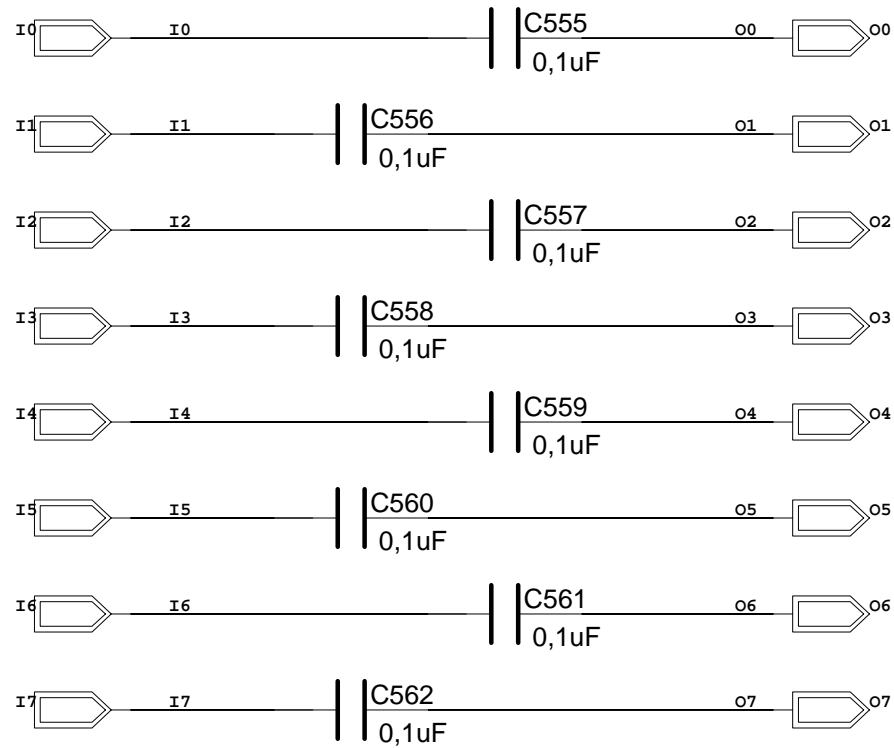



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED		SIZE	AC_DECOUPLING_8_4		VER	REV
DATE	30/06/2017:15:01	A				
TIME:		SHEET 15 of 66				

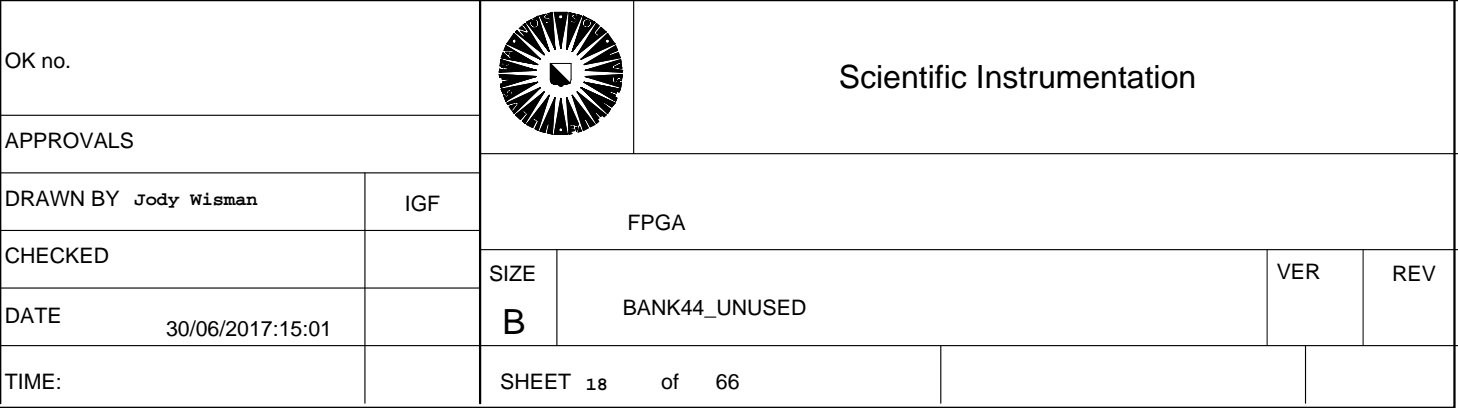


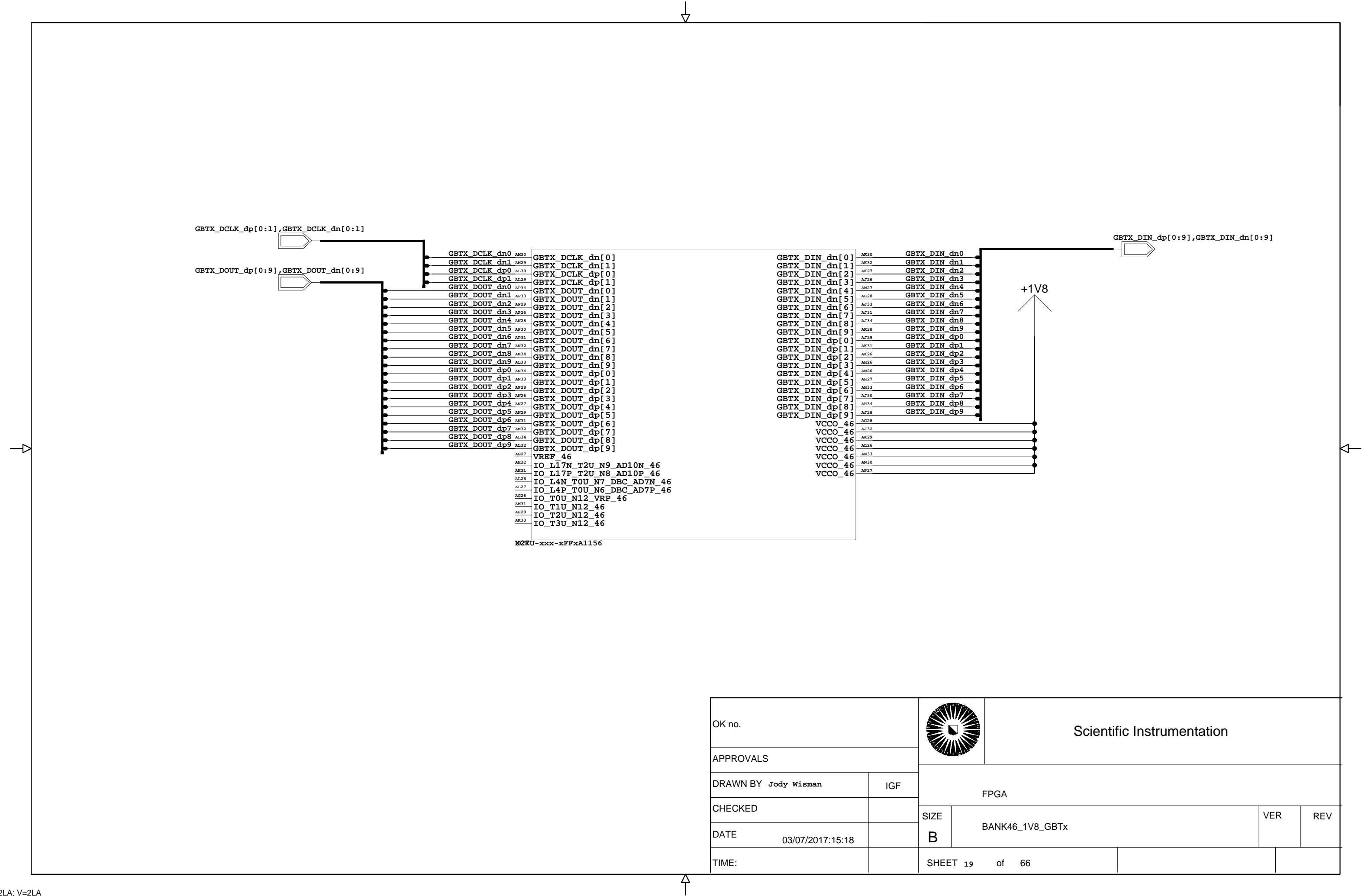
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF						
CHECKED		SIZE	AC_DECOUPLING_8_5			VER	REV
DATE	30/06/2017:15:01	A					
TIME:		SHEET 16 of 66					




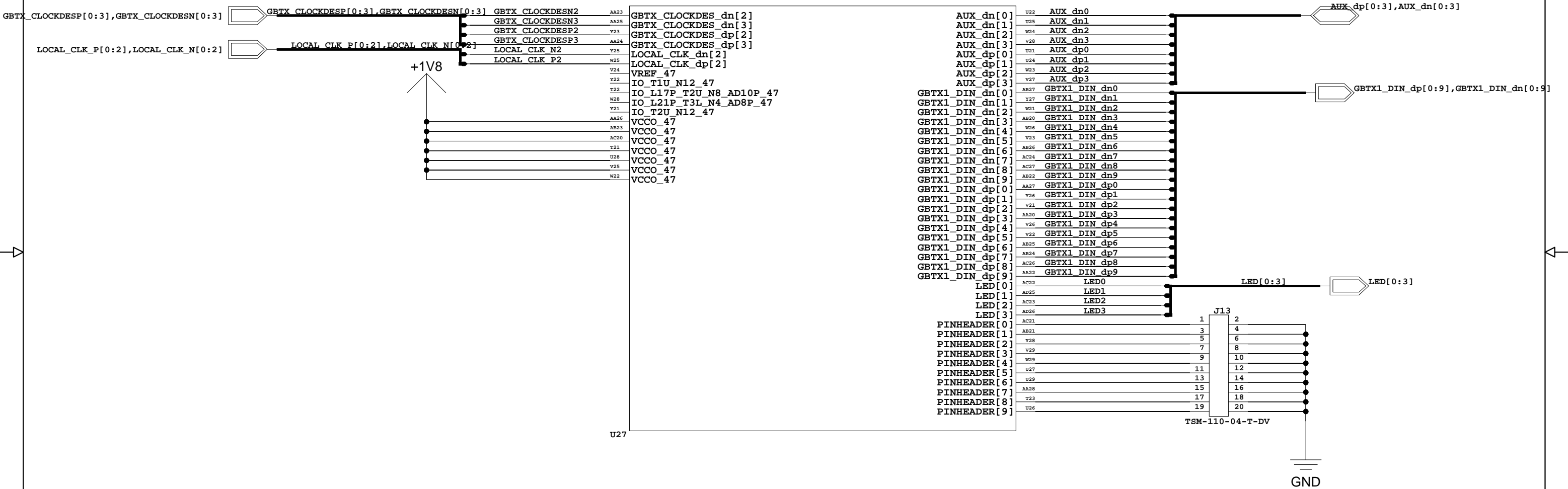



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF						
CHECKED		SIZE	AC_DECOUPLING_8_6			VER	REV
DATE	30/06/2017:15:01	A					
TIME:		SHEET 17 of 66					



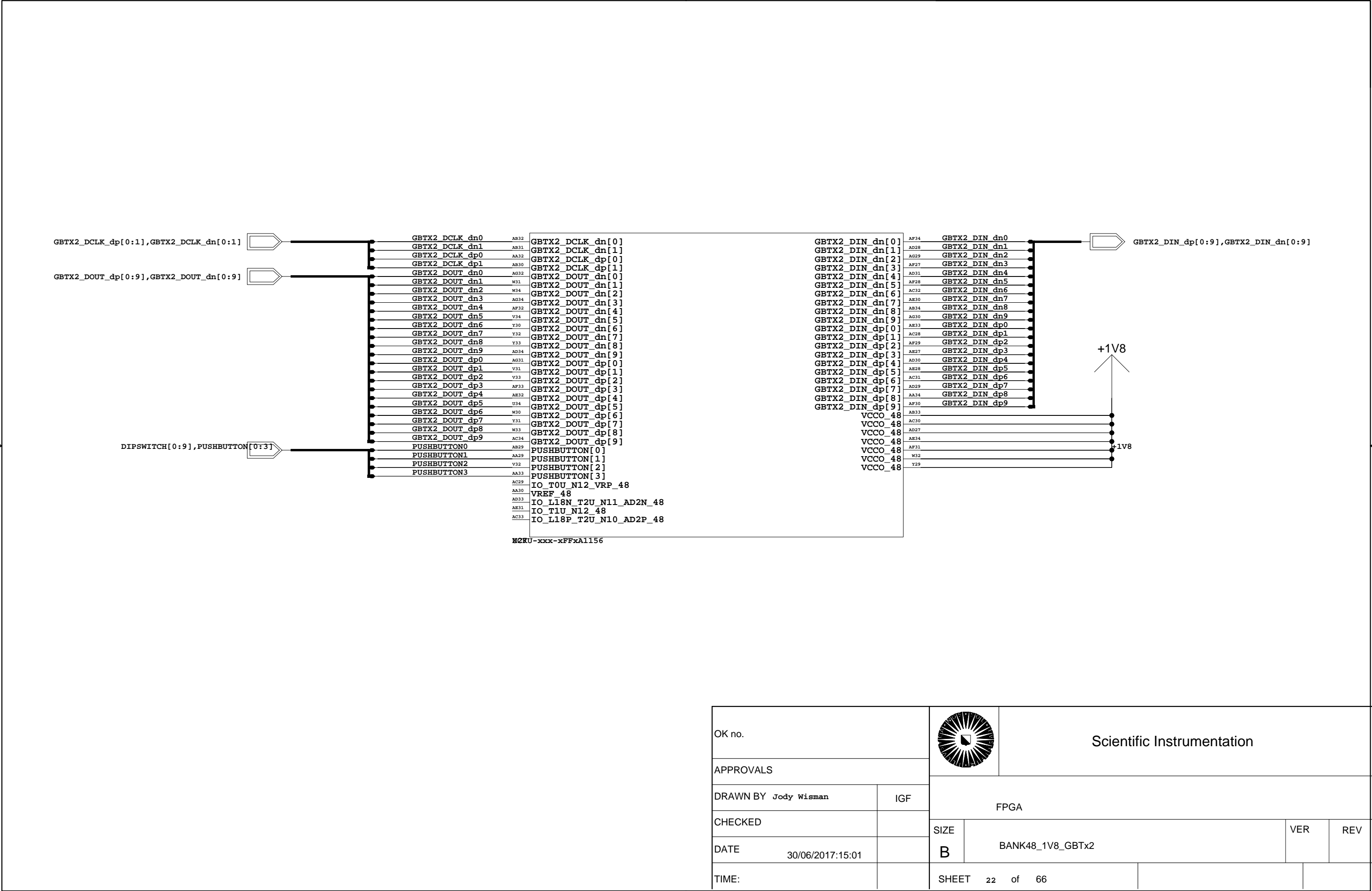


OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman		IGF	FPGA			
CHECKED						
DATE 03/07/2017:15:18		SIZE B	BANK46_1V8_GBtx		VER	REV
TIME:			SHEET 19 of 66			



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman	IGF	FPGA				
CHECKED		SIZE	BANK47_1V8_GBTx1_LED_DIPSWITCH		VER	REV
DATE 03/07/2017:15:18		B				
TIME:		SHEET 20 of 66				









SHEET 24 of 66



ALPIDE\_DATA\_GPIO\_dp[0:27],ALPIDE\_DATA\_GPIO\_dn[0:27]



ALPIDE_DATA_GPIO_dn1	C13	ALPIDE_DATA_GPIO_dn[1]
ALPIDE_DATA_GPIO_dn2	B13	ALPIDE_DATA_GPIO_dn[2]
ALPIDE_DATA_GPIO_dn3	B11	ALPIDE_DATA_GPIO_dn[3]
ALPIDE_DATA_GPIO_dn4	B12	ALPIDE_DATA_GPIO_dn[4]
ALPIDE_DATA_GPIO_dn5	G12	ALPIDE_DATA_GPIO_dn[5]
ALPIDE_DATA_GPIO_dn6	D11	ALPIDE_DATA_GPIO_dn[6]
ALPIDE_DATA_GPIO_dn7	D10	ALPIDE_DATA_GPIO_dn[7]
ALPIDE_DATA_GPIO_dn8	C8	ALPIDE_DATA_GPIO_dn[8]
ALPIDE_DATA_GPIO_dn9	C9	ALPIDE_DATA_GPIO_dn[9]
ALPIDE_DATA_GPIO_dn10	F10	ALPIDE_DATA_GPIO_dn[10]
ALPIDE_DATA_GPIO_dn11	E8	ALPIDE_DATA_GPIO_dn[11]
ALPIDE_DATA_GPIO_dn12	H9	ALPIDE_DATA_GPIO_dn[12]
ALPIDE_DATA_GPIO_dn13	F9	ALPIDE_DATA_GPIO_dn[13]
ALPIDE_DATA_GPIO_dn14	H8	ALPIDE_DATA_GPIO_dn[14]
ALPIDE_DATA_GPIO_dn15	J10	ALPIDE_DATA_GPIO_dn[15]
ALPIDE_DATA_GPIO_dn16	H13	ALPIDE_DATA_GPIO_dn[16]
ALPIDE_DATA_GPIO_dn17	J11	ALPIDE_DATA_GPIO_dn[17]
ALPIDE_DATA_GPIO_dn18	K8	ALPIDE_DATA_GPIO_dn[18]
ALPIDE_DATA_GPIO_dn19	K12	ALPIDE_DATA_GPIO_dn[19]
ALPIDE_DATA_GPIO_dn20	K13	ALPIDE_DATA_GPIO_dn[20]
ALPIDE_DATA_GPIO_dn21	A9	ALPIDE_DATA_GPIO_dn[21]
ALPIDE_DATA_GPIO_dn22	A10	ALPIDE_DATA_GPIO_dn[22]
ALPIDE_DATA_GPIO_dn23	A12	ALPIDE_DATA_GPIO_dn[23]
ALPIDE_DATA_GPIO_dn24	G11	ALPIDE_DATA_GPIO_dn[24]
ALPIDE_DATA_GPIO_dp1	D13	ALPIDE_DATA_GPIO_dp[1]
ALPIDE_DATA_GPIO_dp2	F13	ALPIDE_DATA_GPIO_dp[2]
ALPIDE_DATA_GPIO_dp3	C11	ALPIDE_DATA_GPIO_dp[3]
ALPIDE_DATA_GPIO_dp4	C12	ALPIDE_DATA_GPIO_dp[4]
ALPIDE_DATA_GPIO_dp5	H12	ALPIDE_DATA_GPIO_dp[5]
ALPIDE_DATA_GPIO_dp6	E11	ALPIDE_DATA_GPIO_dp[6]
ALPIDE_DATA_GPIO_dp7	E10	ALPIDE_DATA_GPIO_dp[7]
ALPIDE_DATA_GPIO_dp8	D8	ALPIDE_DATA_GPIO_dp[8]
ALPIDE_DATA_GPIO_dp9	D9	ALPIDE_DATA_GPIO_dp[9]
ALPIDE_DATA_GPIO_dp10	G10	ALPIDE_DATA_GPIO_dp[10]
ALPIDE_DATA_GPIO_dp11	F8	ALPIDE_DATA_GPIO_dp[11]
ALPIDE_DATA_GPIO_dp12	J9	ALPIDE_DATA_GPIO_dp[12]
ALPIDE_DATA_GPIO_dp13	G9	ALPIDE_DATA_GPIO_dp[13]
ALPIDE_DATA_GPIO_dp14	J8	ALPIDE_DATA_GPIO_dp[14]
ALPIDE_DATA_GPIO_dp15	K10	ALPIDE_DATA_GPIO_dp[15]
ALPIDE_DATA_GPIO_dp16	J13	ALPIDE_DATA_GPIO_dp[16]
ALPIDE_DATA_GPIO_dp17	K11	ALPIDE_DATA_GPIO_dp[17]
ALPIDE_DATA_GPIO_dp18	L8	ALPIDE_DATA_GPIO_dp[18]
ALPIDE_DATA_GPIO_dp19	L12	ALPIDE_DATA_GPIO_dp[19]
ALPIDE_DATA_GPIO_dp20	L13	ALPIDE_DATA_GPIO_dp[20]
ALPIDE_DATA_GPIO_dp21	B9	ALPIDE_DATA_GPIO_dp[21]
ALPIDE_DATA_GPIO_dp22	B10	ALPIDE_DATA_GPIO_dp[22]
ALPIDE_DATA_GPIO_dp23	A13	ALPIDE_DATA_GPIO_dp[23]
ALPIDE_DATA_GPIO_dp24	H11	ALPIDE_DATA_GPIO_dp[24]


IO\_T0U\_N12\_VRP\_66  
VREF\_66  
IO\_T1U\_N12\_66  
IO\_T2U\_N12\_66  
IO\_T3U\_N12\_66

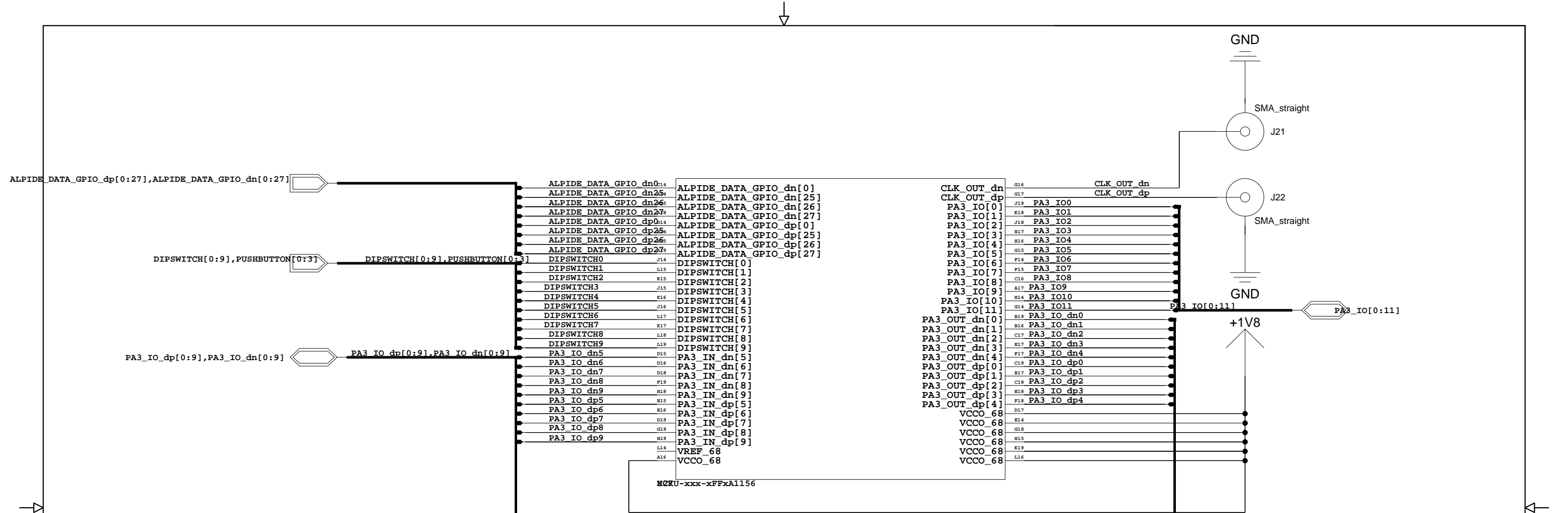
MCNU-xxx-xFFxA1156


VCCO\_66  
VCCO\_66  
VCCO\_66  
VCCO\_66  
VCCO\_66  
VCCO\_66

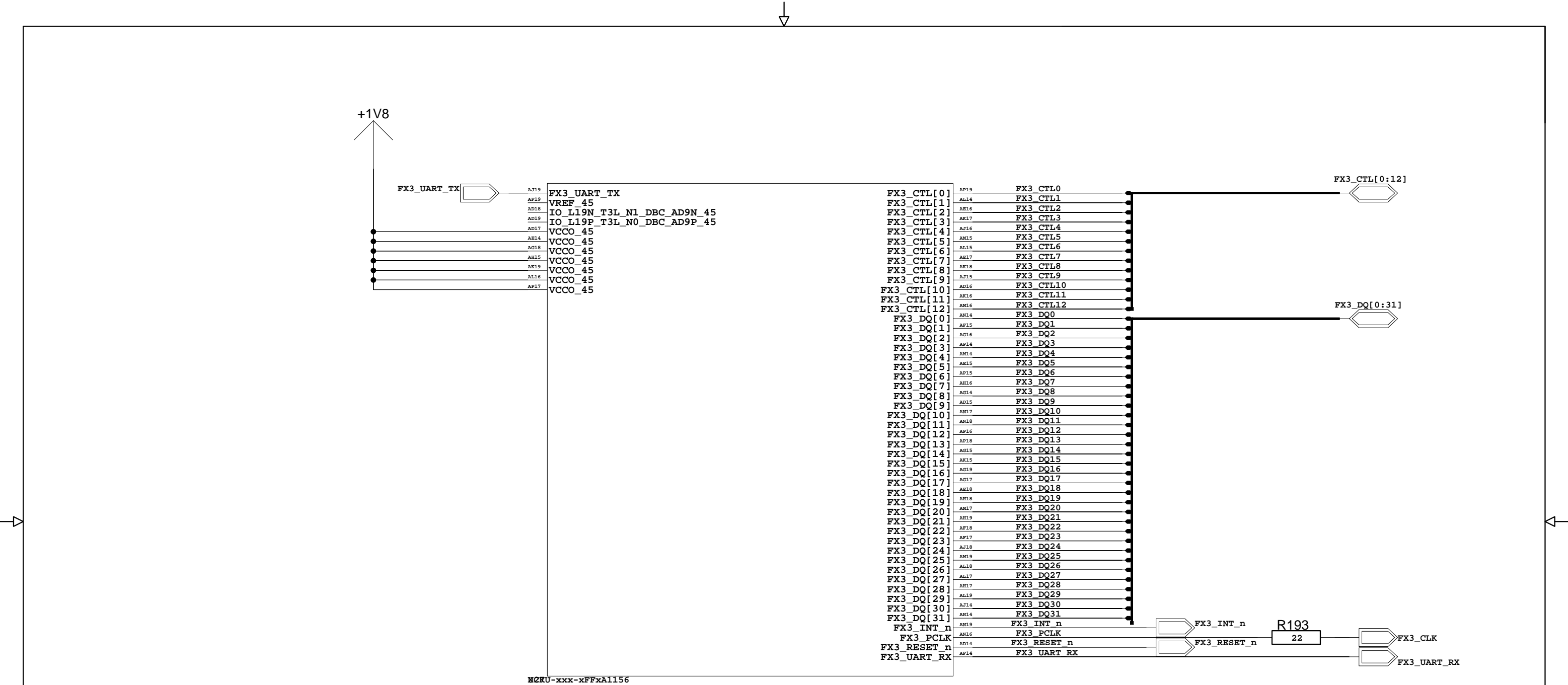
B13  
C10  
F11  
G8  
J12  
K9


+1V8

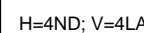
OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman		IGF	FPGA			
CHECKED			SIZE	BANK66_1V8_ALPIDE_HSDATA	VER	REV
DATE 30/06/2017:15:01			B			
TIME:			SHEET 25 of 66			

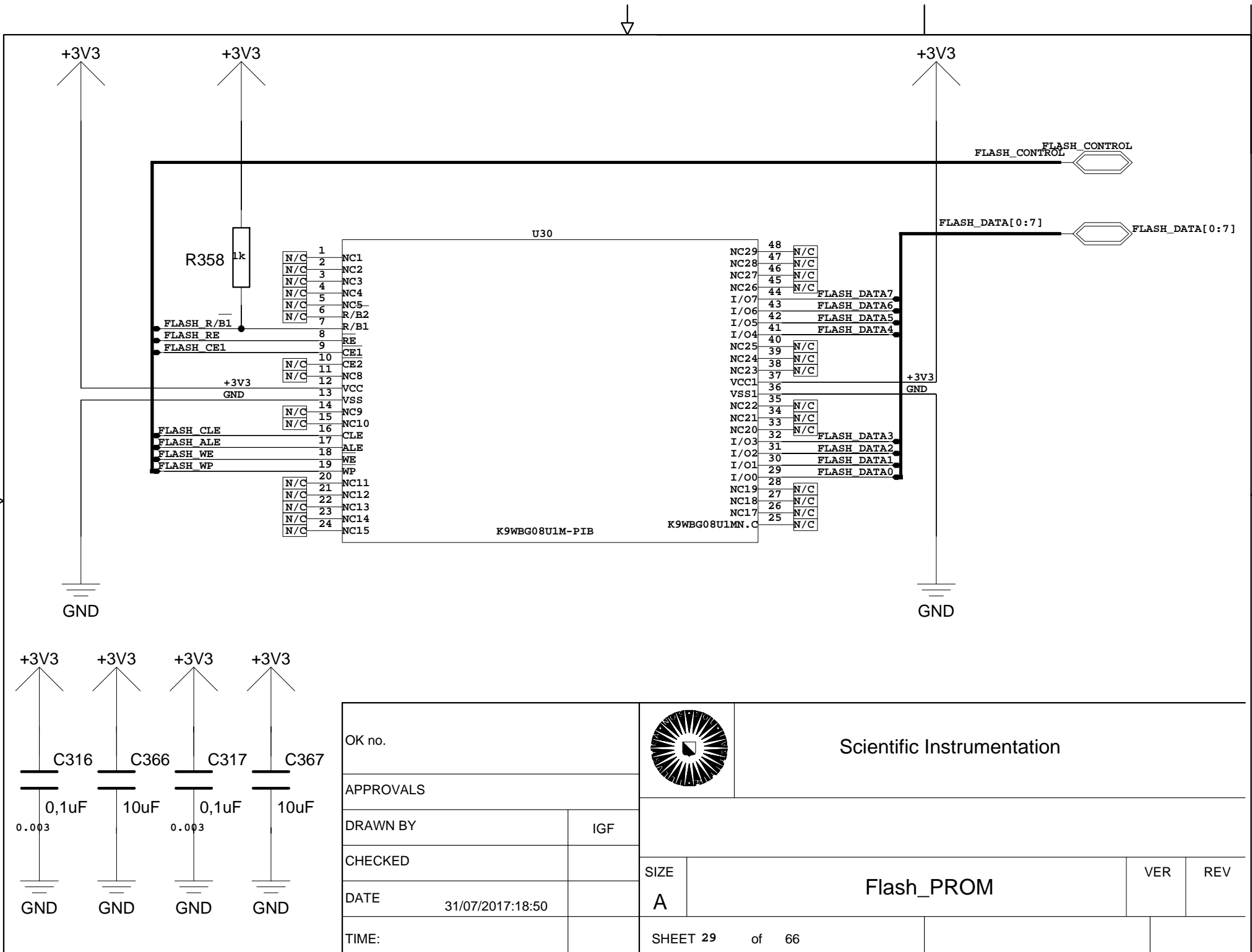



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman	IGF	FPGA				
CHECKED		SIZE	BANK68_1V8_ALPIDE_HSDATA_PA3		VER	REV
DATE 06/07/2017:08:31		B				
TIME:		SHEET 26 of 66				

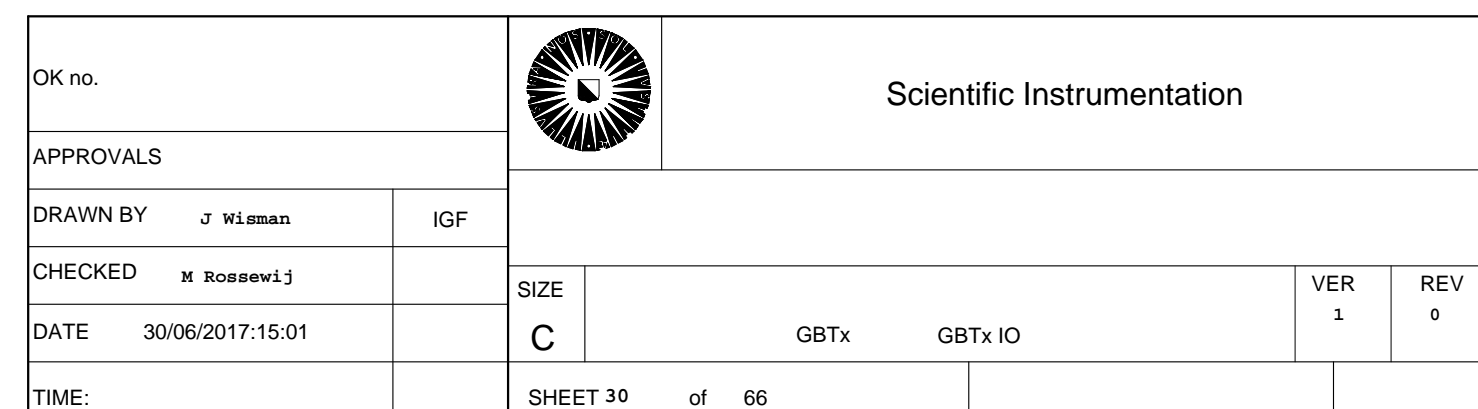


OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman	IGF	FPGA				
CHECKED		SIZE	BANK45_1V8_FX3		VER	REV
DATE 03/07/2017:15:15		B				
TIME:		SHEET 27 of 66				





OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY		IGF					
CHECKED							
DATE			SIZE	Flash_PROM		VER	REV
31/07/2017:18:50			A				
TIME:			SHEET 29 of 66				



D

C

B

A

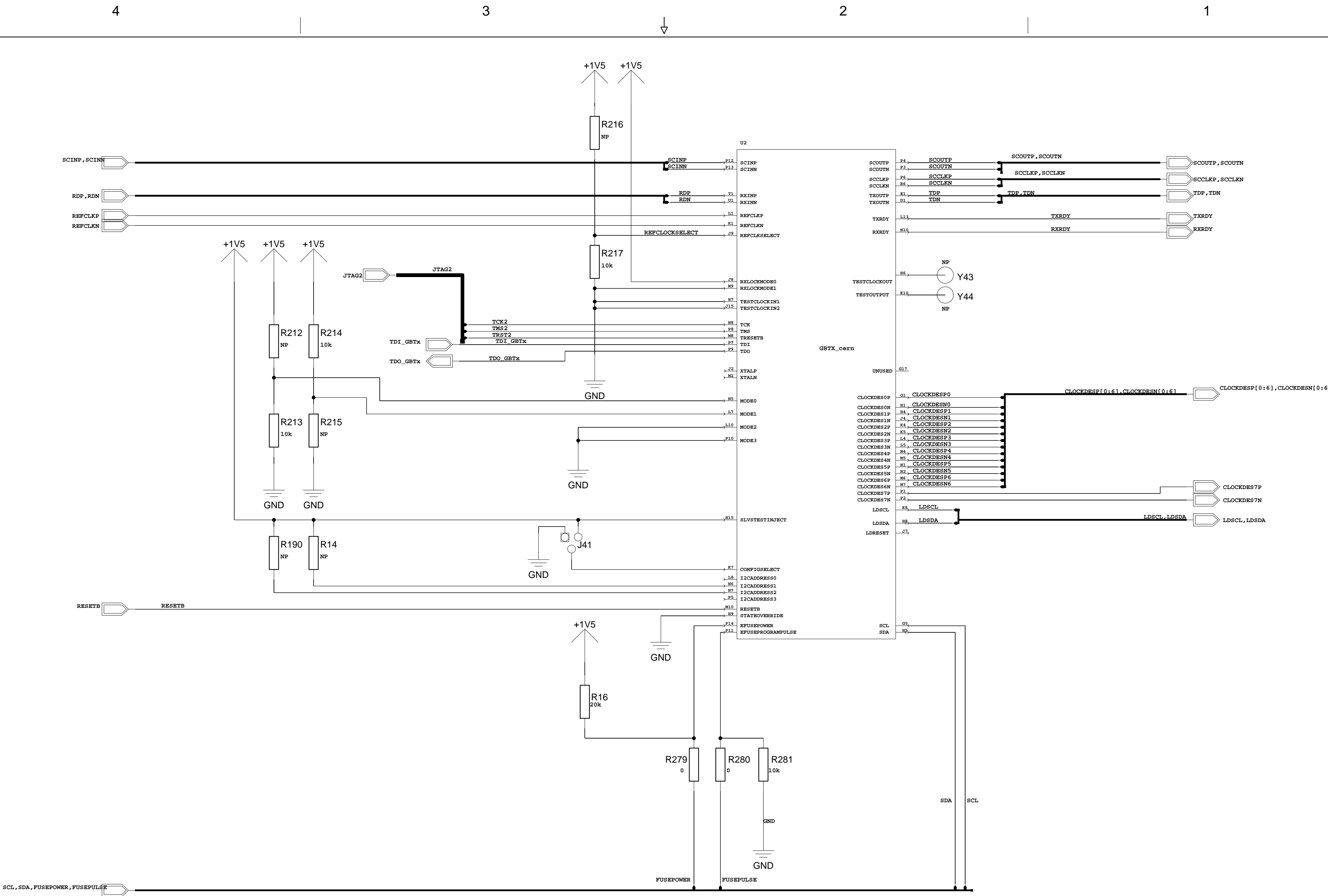
D


C

SH

REV

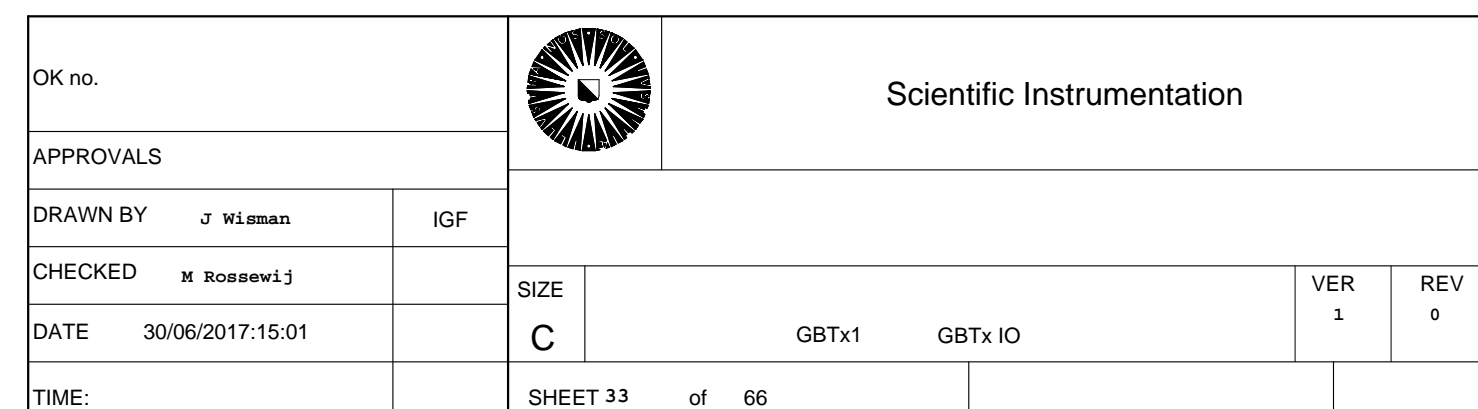
A

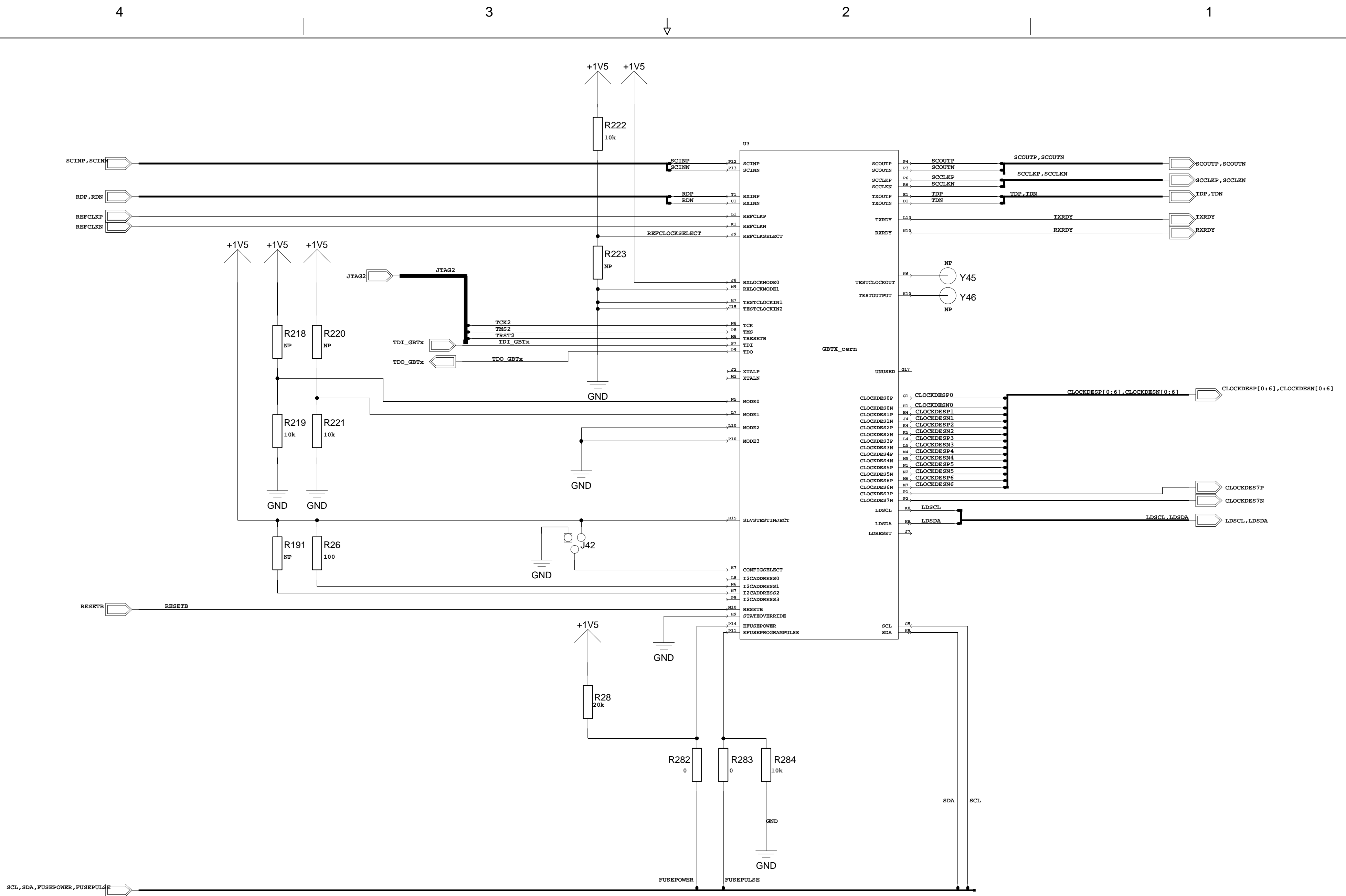



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY J Wisman		IGF					
CHECKED M Rossewij							
DATE 04/08/2017:15:12			SIZE C	GBTx GBTx Control		VER 1	REV 0
TIME:			SHEET 31 of 66				









OK no.			Scientific Instrumentation					
APPROVALS								
DRAWN BY	J Wisman	IGF						
CHECKED	M Rossewij		SIZE	GBTx1		GBTx Control	VER	REV
DATE	04/08/2017:15:12		C				1	0
TIME:			SHEET 34 of 66					

D

C

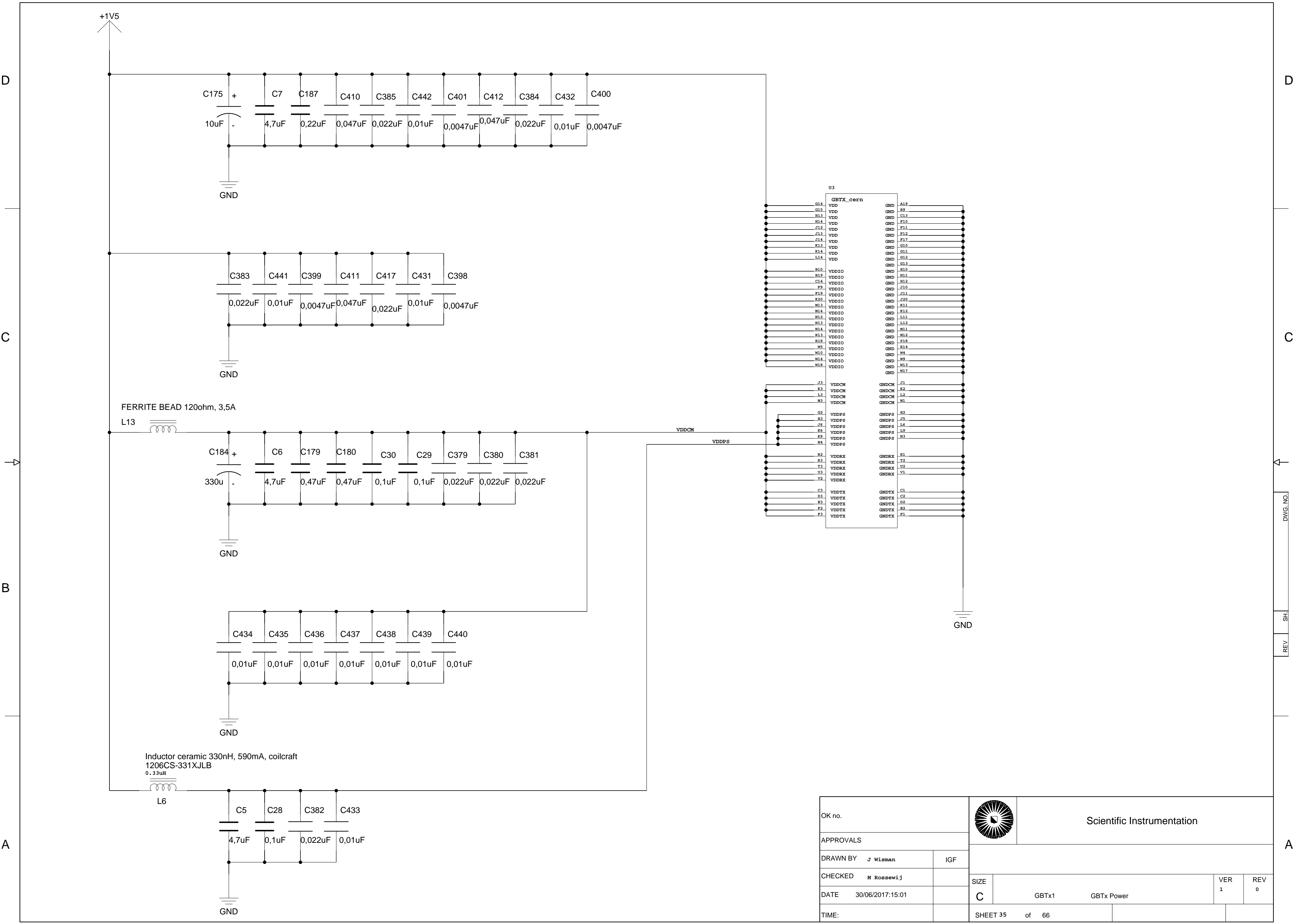
B

A

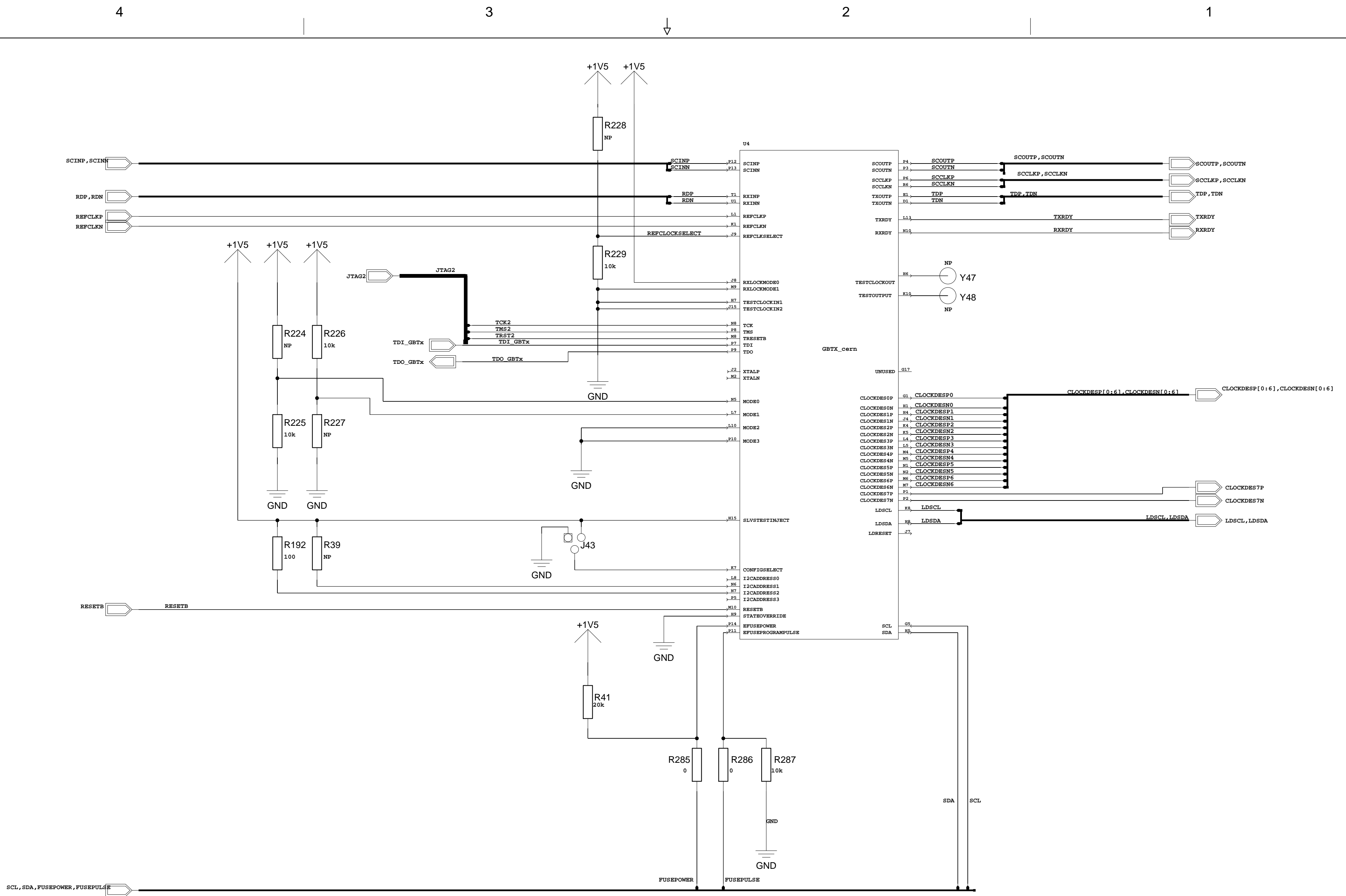
D


C

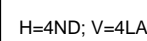
A

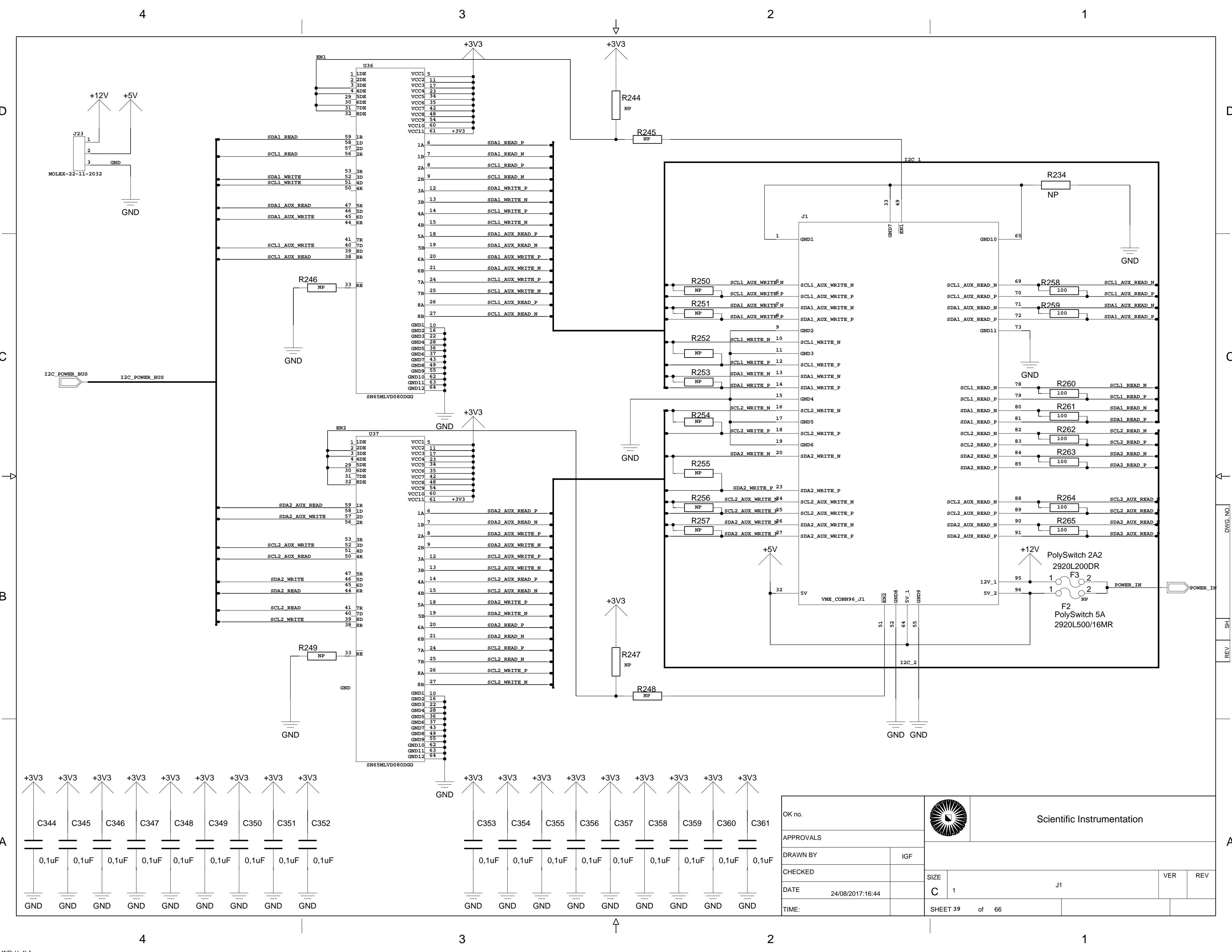





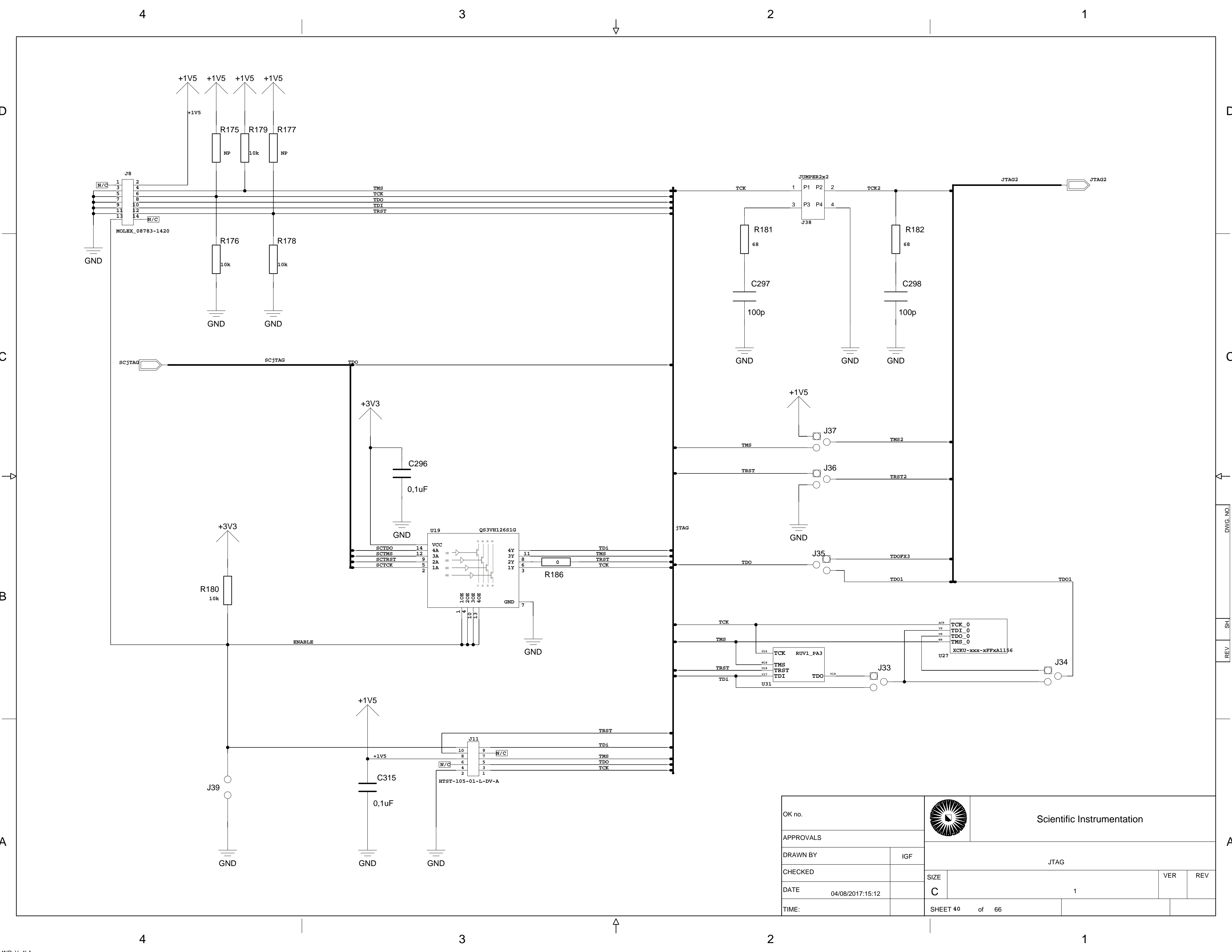



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	J Wisman	IGF					
CHECKED	M Rossewij		SIZE			VER	
DATE	04/08/2017:15:12		C	GBTx2		GBTx Control	10
TIME:			SHEET 37		of 66		



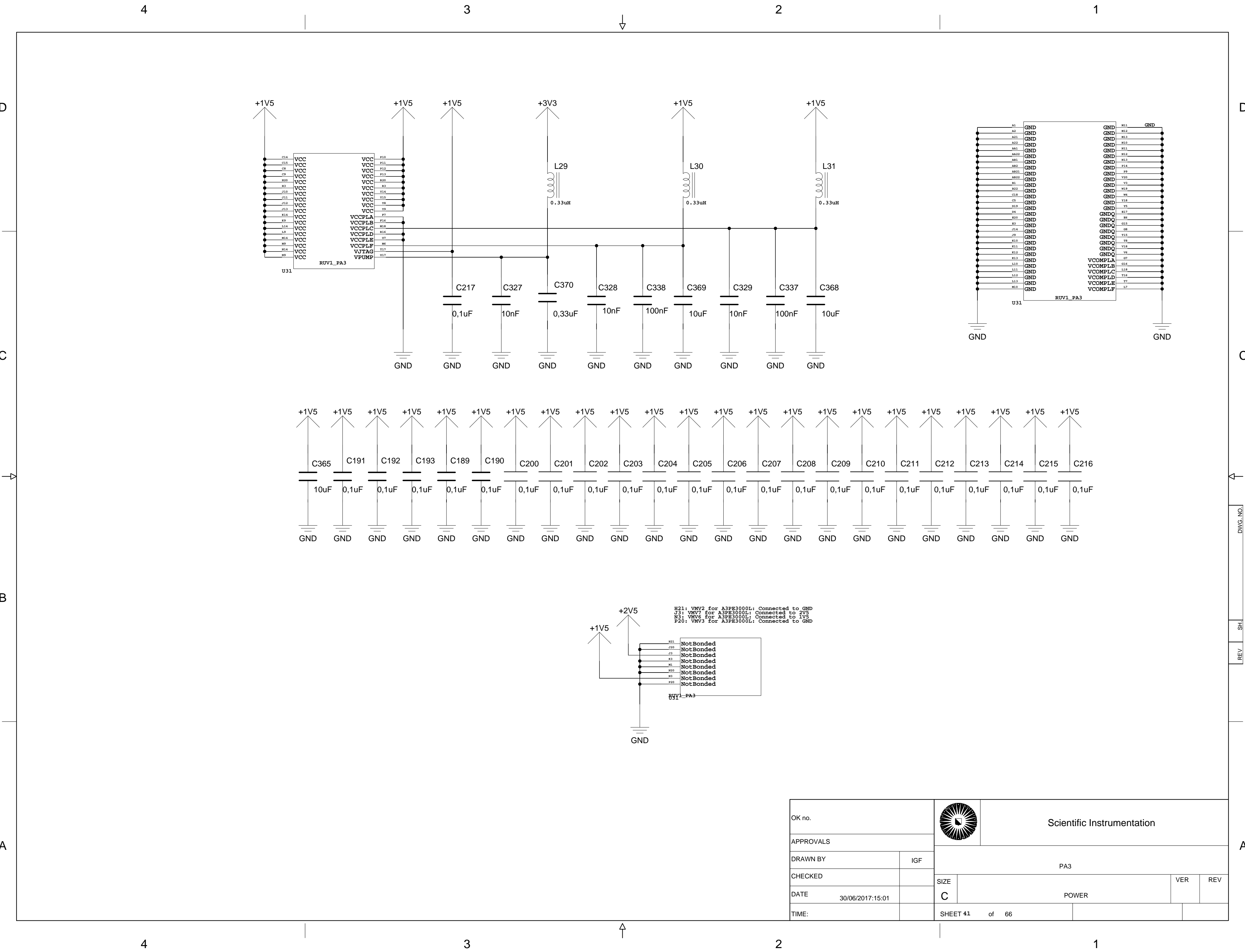



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY		IGF					
CHECKED							
DATE		24/08/2017:16:44					
		SIZE	J1			VER	REV
		C	1				
TIME:		SHEET 39 of 66					

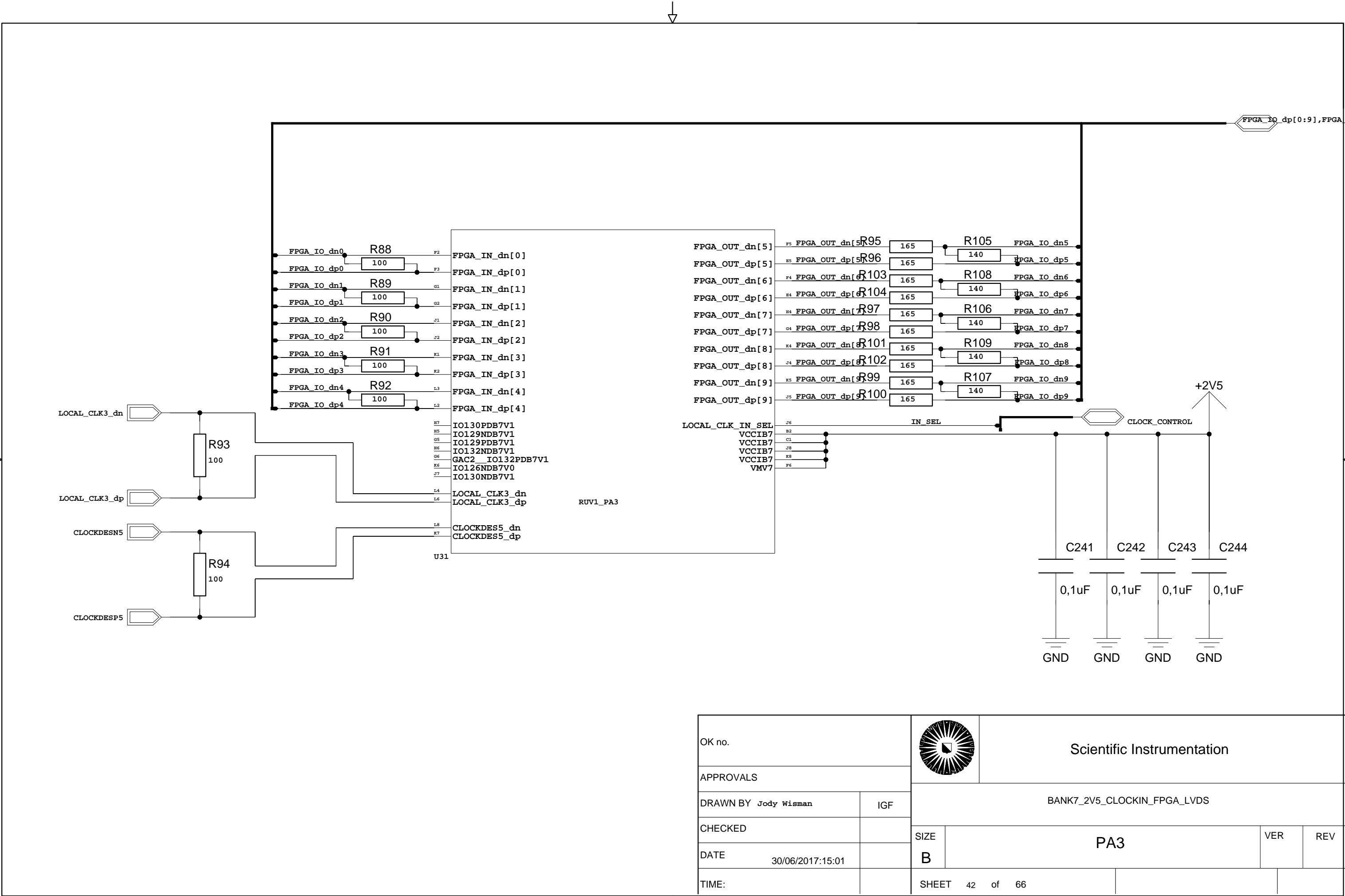



OK no.			Scientific Instrumentation		
APPROVALS					
DRAWN BY	IGF	JTAG			
CHECKED		SIZE		VER	REV
DATE	04/08/2017:15:12	C	1		
TIME:		SHEET 40 of 66			

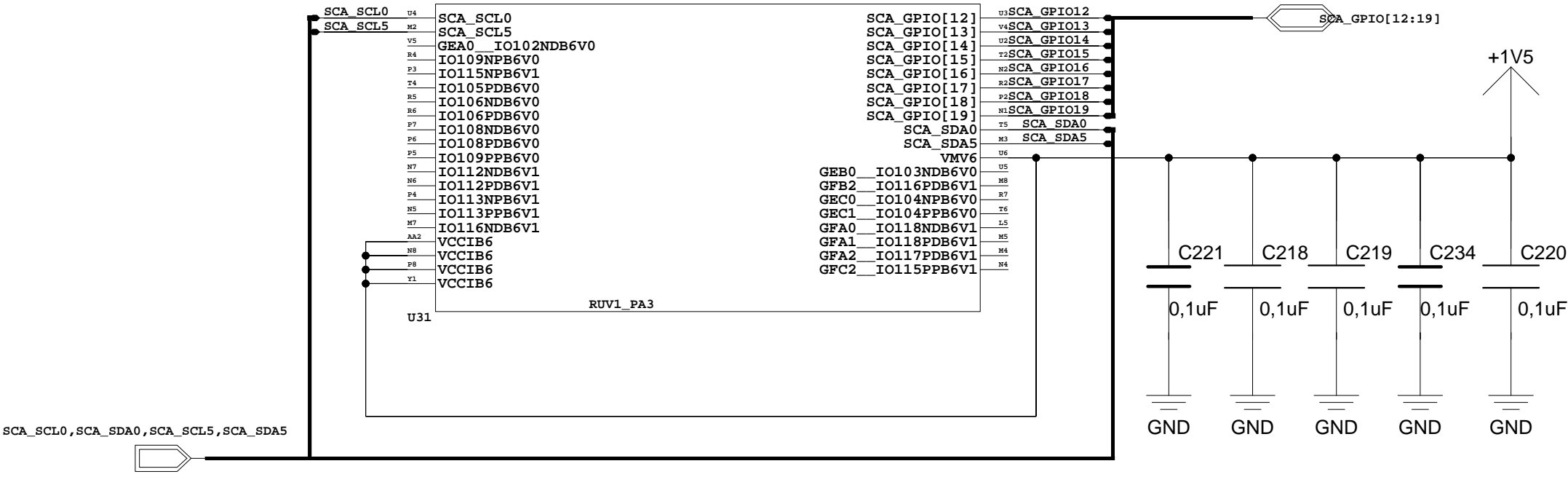





OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	PA3				
CHECKED		SIZE			VER	REV
DATE	30/06/2017:15:01	C	POWER			
TIME:		SHEET 41 of 66				



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Jody Wisman		IGF	BANK7_2V5_CLOCKIN_FPGA_LVDS				
CHECKED			SIZE	PA3		VER	REV
DATE 30/06/2017:15:01			B				
TIME:			SHEET 42 of 66				



SCA\_SCL0,SCA\_SDA0,SCA\_SCL5,SCA\_SDA5

OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman		IGF	BANK6_1V5_SCA			
CHECKED			SIZE	PA3	VER	REV
DATE 24/08/2017:16:58						
TIME:			SHEET 43 of 66			

A19	Not Bonded
AA14	Not Bonded
AA15	Not Bonded
AA18	Not Bonded
AA19	Not Bonded
AA20	Not Bonded
AA3	Not Bonded
AB10	Not Bonded
AB11	Not Bonded
AB18	Not Bonded
AB19	Not Bonded
B14	Not Bonded
B15	Not Bonded
B19	Not Bonded
B20	Not Bonded
B3	Not Bonded
C12	Not Bonded
C13	Not Bonded
C16	Not Bonded
C17	Not Bonded
C19	Not Bonded
C2	Not Bonded
C20	Not Bonded
C21	Not Bonded
C3	Not Bonded
C4	Not Bonded
D1	Not Bonded
D2	Not Bonded
D20	Not Bonded
D21	Not Bonded
D22	Not Bonded
D3	Not Bonded
E1	Not Bonded
E2	Not Bonded
E21	Not Bonded
E22	Not Bonded
F1	Not Bonded
F20	Not Bonded
F21	Not Bonded
F22	Not Bonded
G22	Not Bonded
G3	Not Bonded
H1	Not Bonded
H2	Not Bonded
H22	Not Bonded
L1	Not Bonded
L22	Not Bonded
F1	Not Bonded
R1	Not Bonded
T1	Not Bonded
T20	Not Bonded
T3	Not Bonded
U1	Not Bonded
U20	Not Bonded
V1	Not Bonded
V2	Not Bonded
V21	Not Bonded
W1	Not Bonded
W2	Not Bonded
W20	Not Bonded
W21	Not Bonded
W22	Not Bonded
W3	Not Bonded
Y13	Not Bonded
Y16	Not Bonded
Y17	Not Bonded
Y19	Not Bonded
Y2	Not Bonded
Y20	Not Bonded
Y21	Not Bonded
Y3	Not Bonded

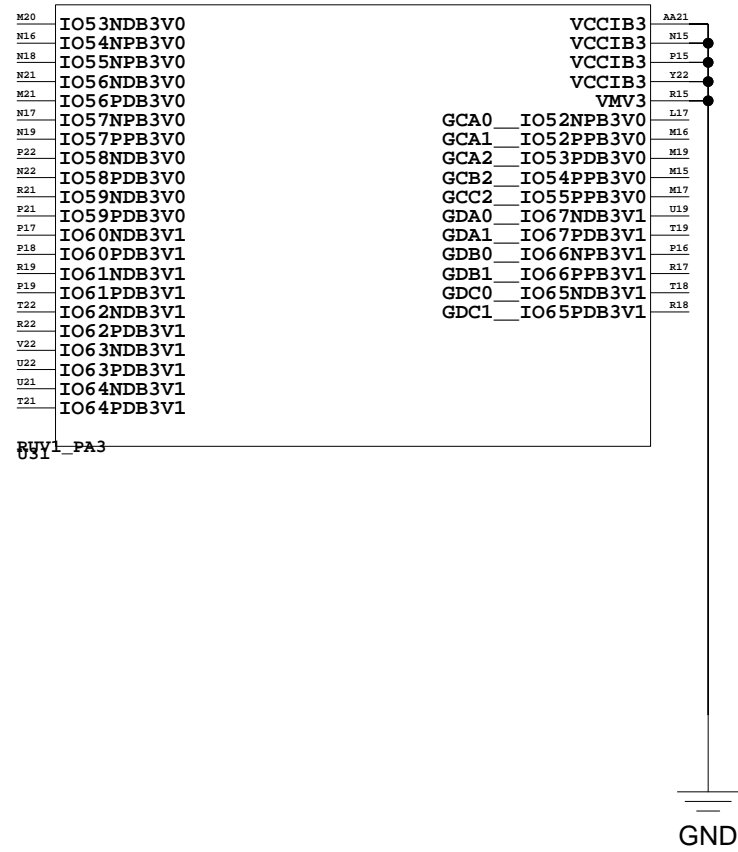
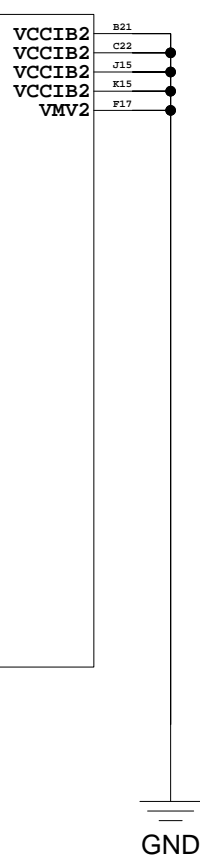
RUV1\_PA3


U31

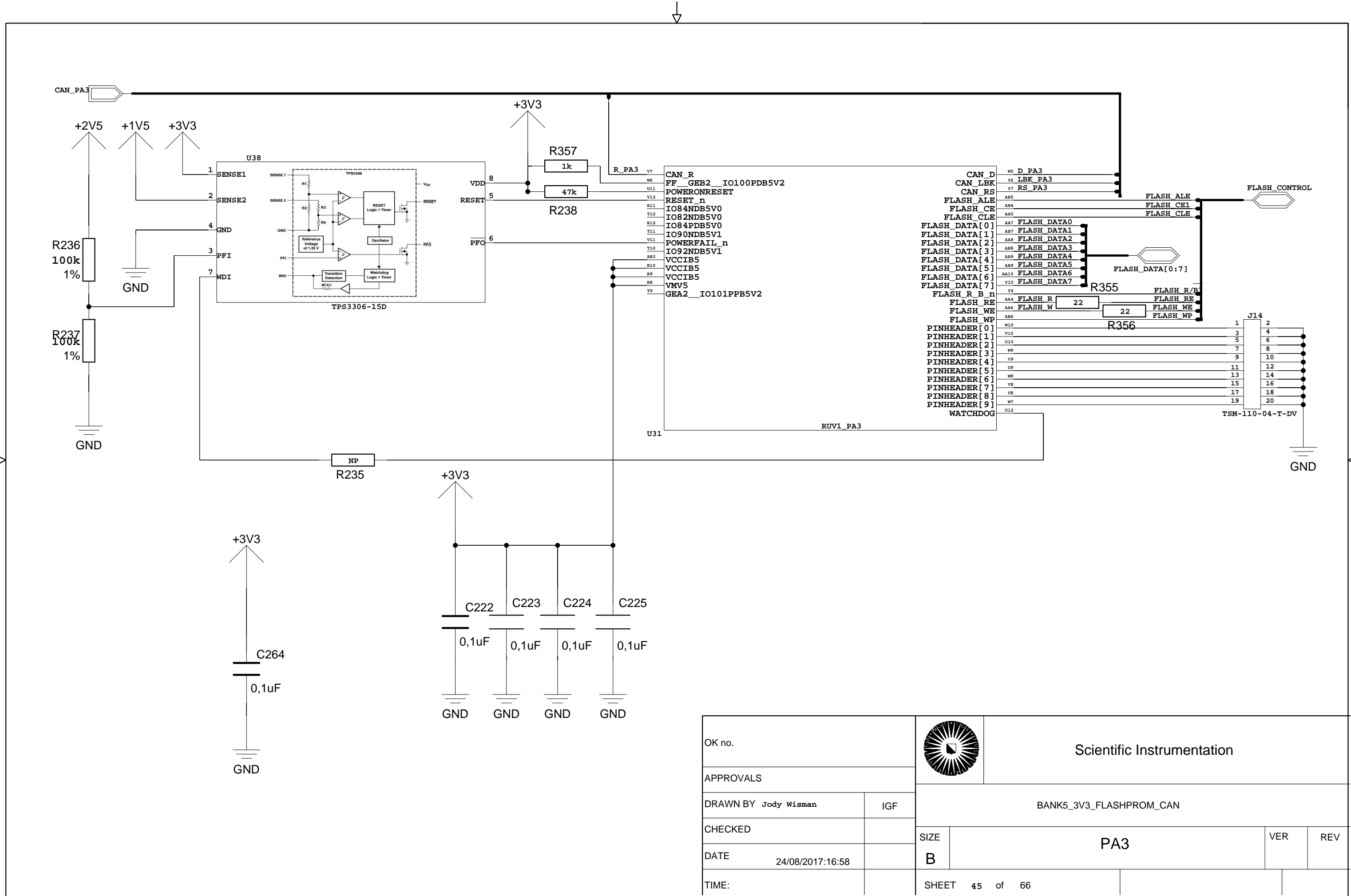
B18	GBA2	IO36PDB2V0
G17	GBB2	IO37PDB2V0
H16	GBC2	IO38PDB2V0
L19	GCB0	IO51NPB2V1
L16	GCB1	IO51PPB2V1
L15	GCC0	IO50NPB2V1
K16	GCC1	IO50PPB2V1
F18	IO36NDB2V0	
H17	IO37NDB2V0	
J16	IO38NDB2V0	
Q19	IO39NDB2V0	
Q18	IO39PDB2V0	
J17	IO40NDB2V0	
J18	IO40PDB2V0	
H18	IO41NDB2V0	
H19	IO41PDB2V0	
E19	IO42NDB2V0	
F19	IO42PDB2V0	
G21	IO43NDB2V0	
G20	IO43PDB2V0	
K17	IO44NDB2V1	
K18	IO44PDB2V1	
K20	IO45NPB2V1	
J19	IO45PPB2V1	
K22	IO46NDB2V1	
J22	IO46PDB2V1	
L21	IO47NDB2V1	
L22	IO47PDB2V1	
K21	IO48NDB2V1	
J21	IO48PDB2V1	
K19	IO49NPB2V1	
L20	IO49PPB2V1	


RUV1\_PA3

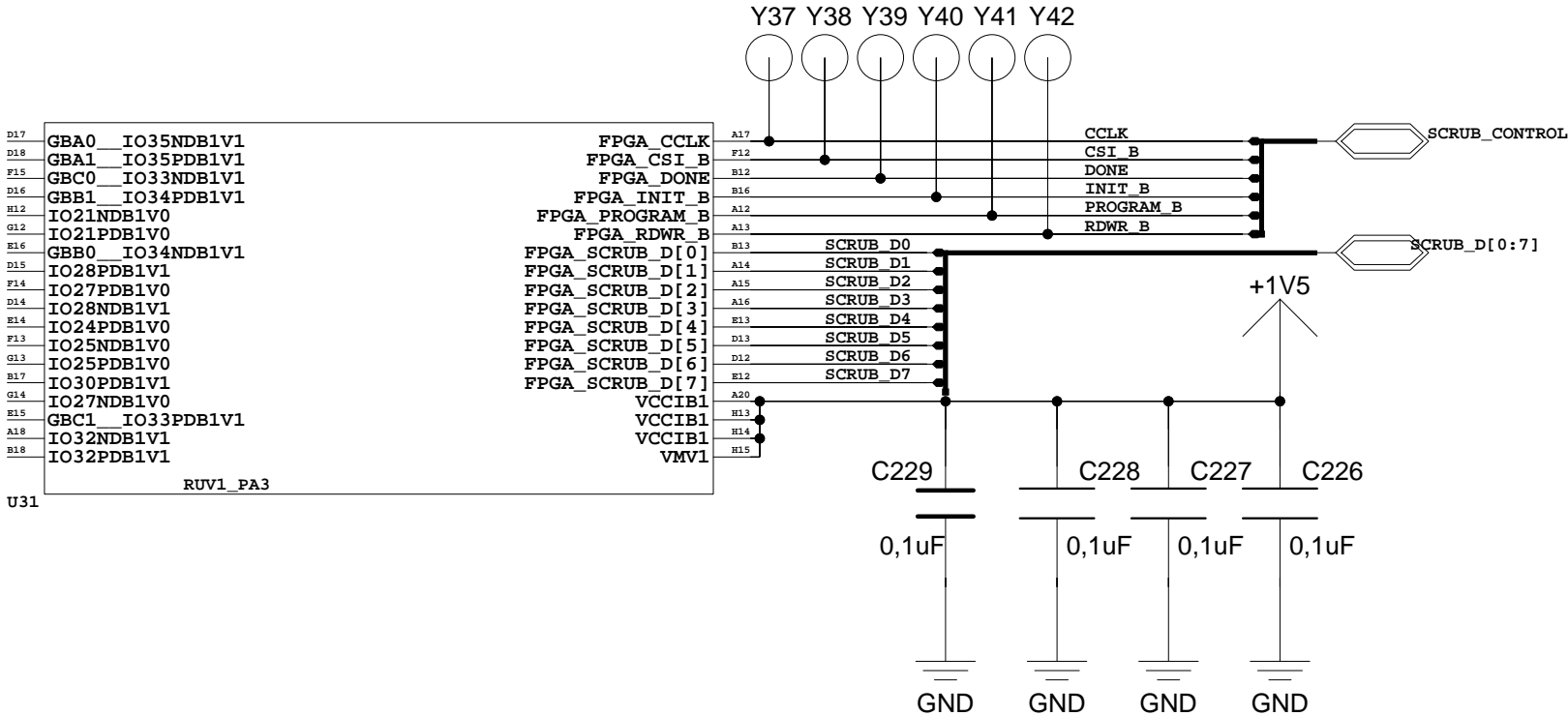
U31




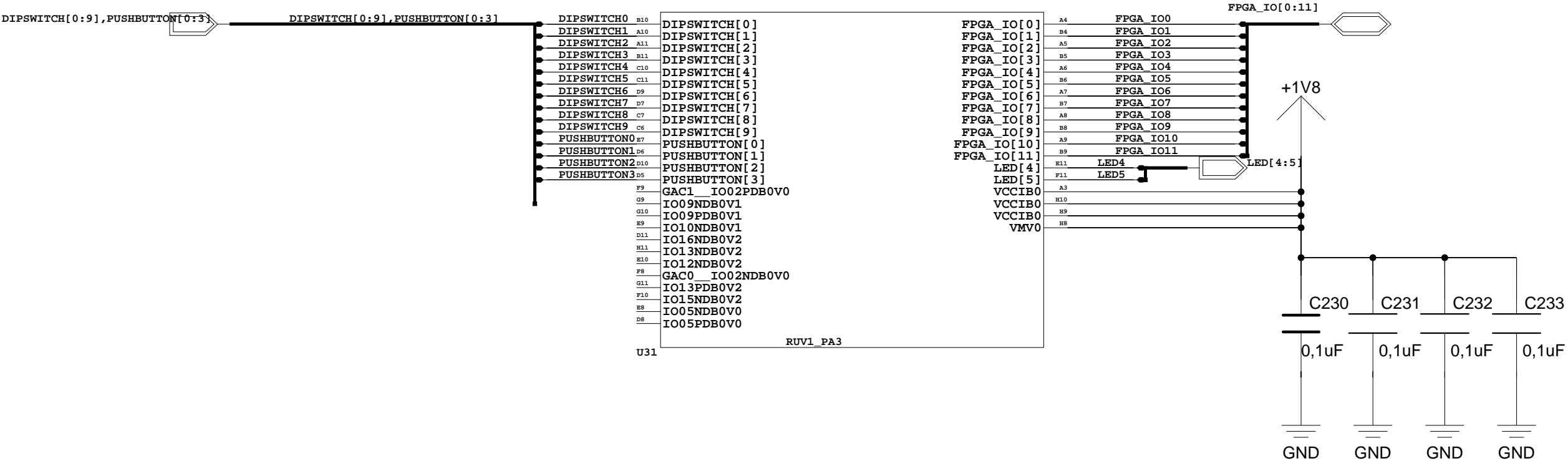
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Jody Wisman		IGF	BANK_UNUSED				
CHECKED			SIZE	PA3		VER	REV
DATE 24/08/2017:16:58			B				
TIME:			SHEET 44 of 66				




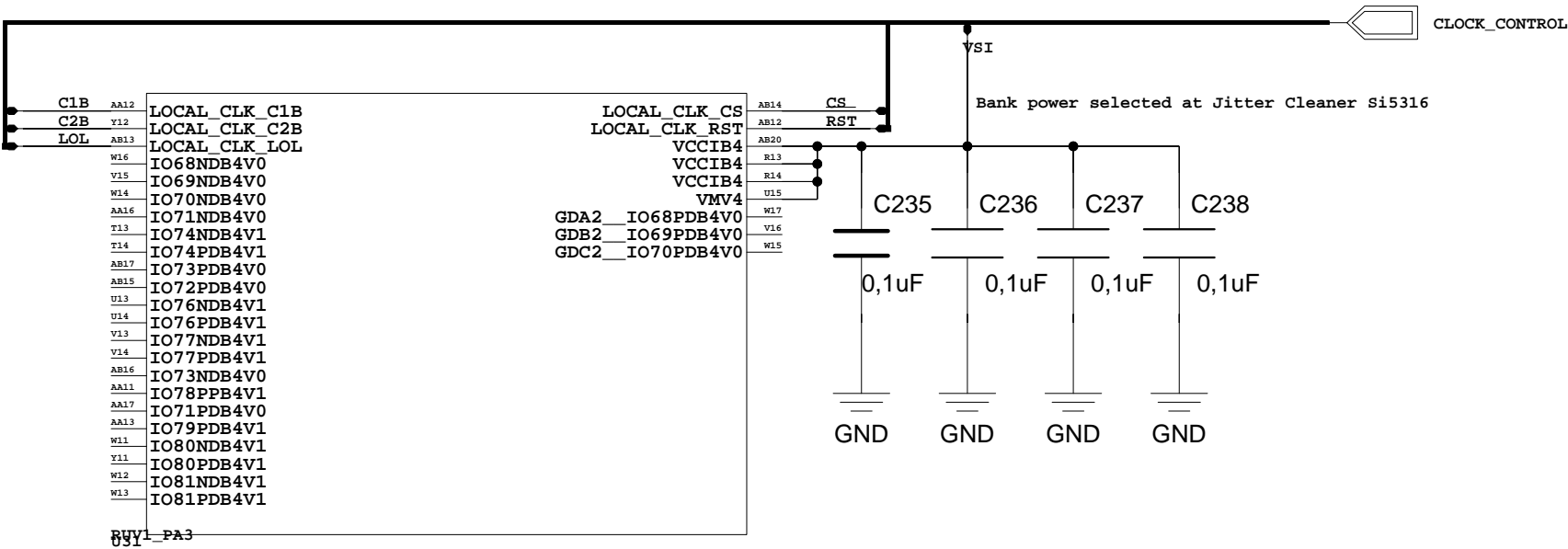
OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman		IGF	BANK5_3V3_FLASHPROM_CAN			
CHECKED			SIZE  PA3		VER	REV
DATE 24/08/2017:16:58						
TIME:			SHEET 45 of 66			




OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman	IGF	BANK1_1V5_FPGA_SCRUBBING				
CHECKED		SIZE	PA3		VER	REV
DATE 24/08/2017:14:00		B				
TIME:		SHEET 46 of 66				

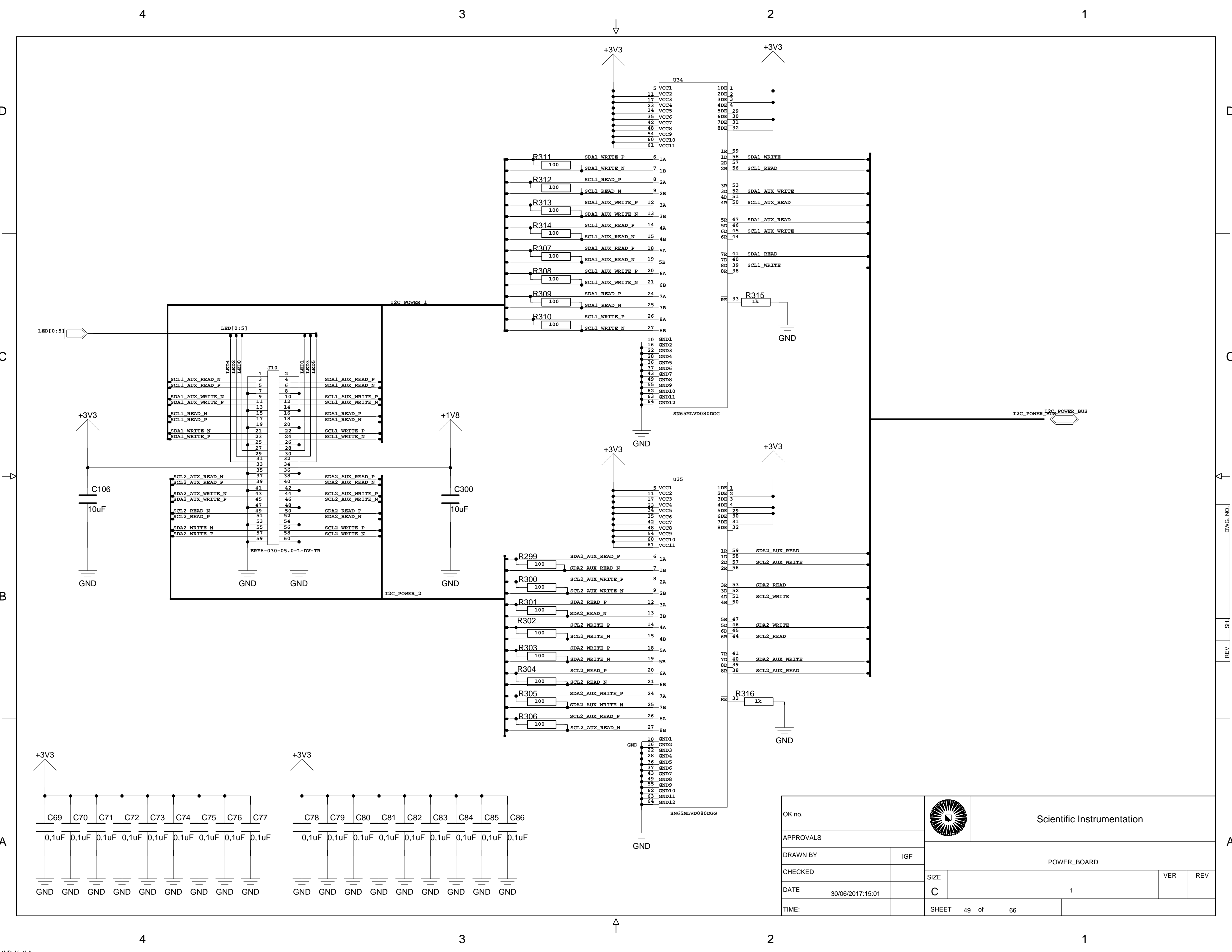



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Jody Wisman		IGF	BANK0_1V8_DIPSWITCH_LED-FPGA_IO				
CHECKED			SIZE	PA3		VER	REV
DATE 24/08/2017:14:00			B				
TIME:			SHEET 47 of 66				

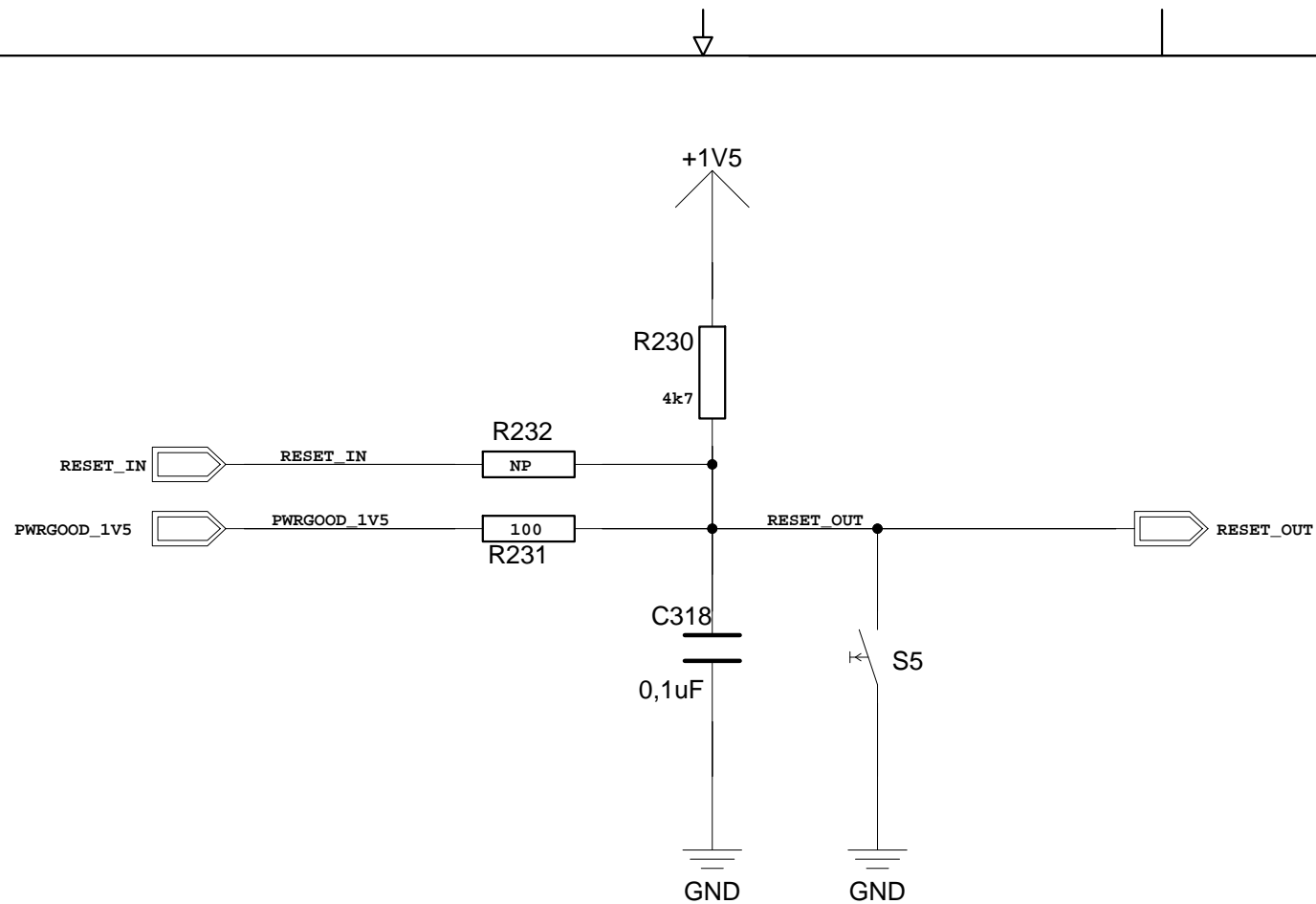



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Jody Wisman		IGF	BANK4_VSI_SI5316_CONFIG				
CHECKED			SIZE	PA3		VER	REV
DATE 24/08/2017:14:01			B				
TIME:			SHEET 48 of 66				

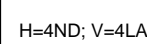




OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	POWER_BOARD				
CHECKED						
DATE	30/06/2017:15:01	SIZE	1		VER	REV
TIME:		C				
		SHEET 49 of 66				



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF						
CHECKED		SIZE	1      RESET			VER	REV
DATE	30/06/2017:15:01	A					
TIME:		SHEET 50      of      66					



4

3

2

1

D

D

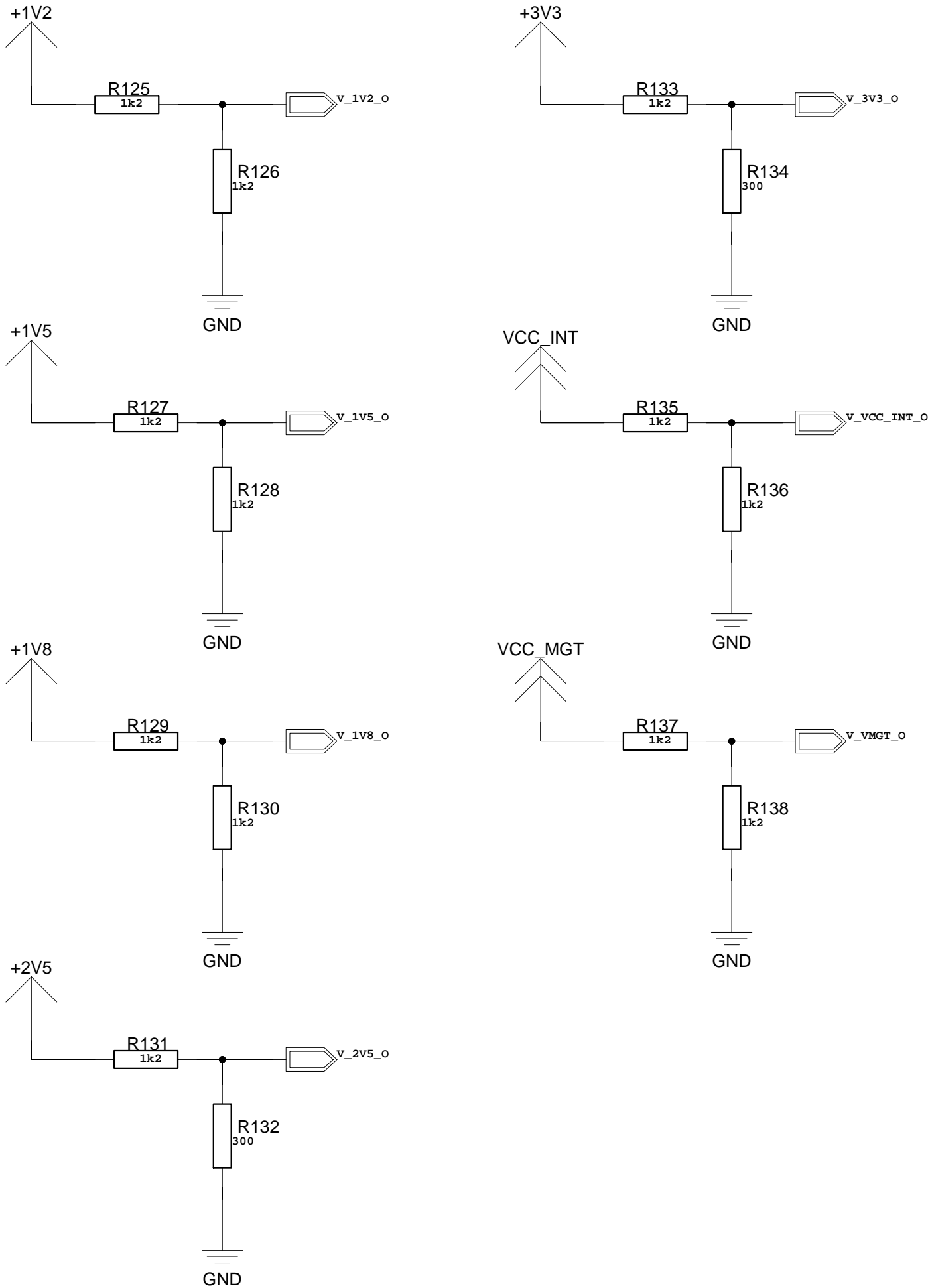
C


C

B

A

A



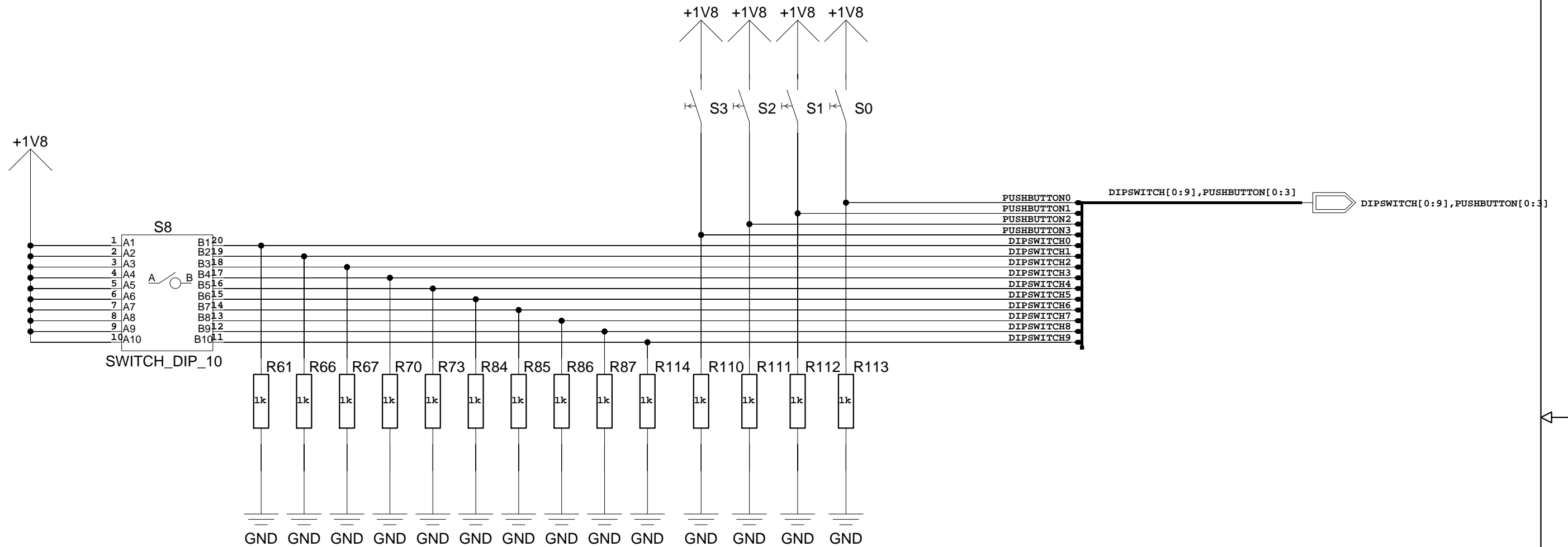
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF	Voltage_Divider					
CHECKED		SIZE				VER	REV
DATE	30/06/2017:15:01	C	1				
TIME:		SHEET 52 of 66					


4

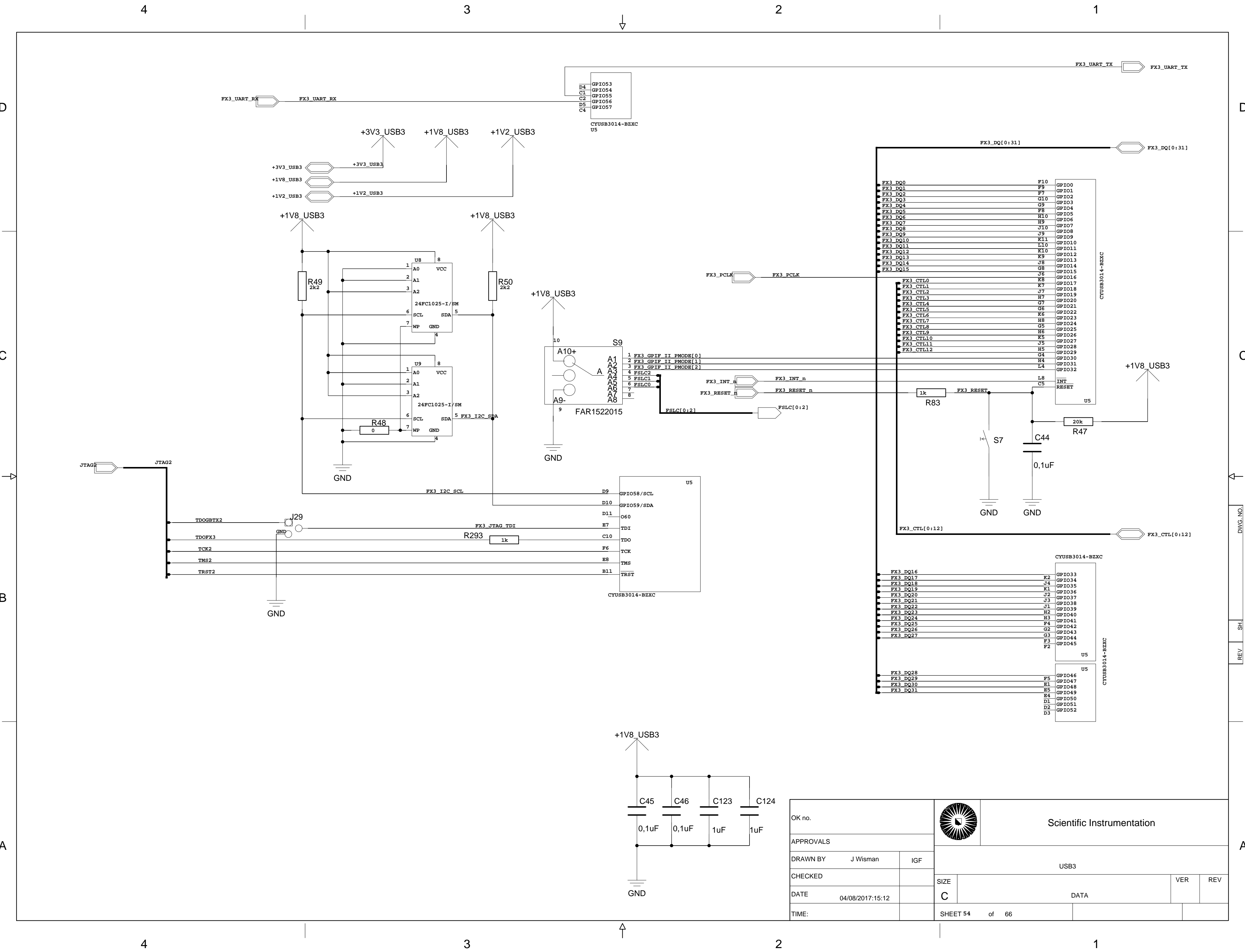
3


2

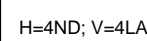
1

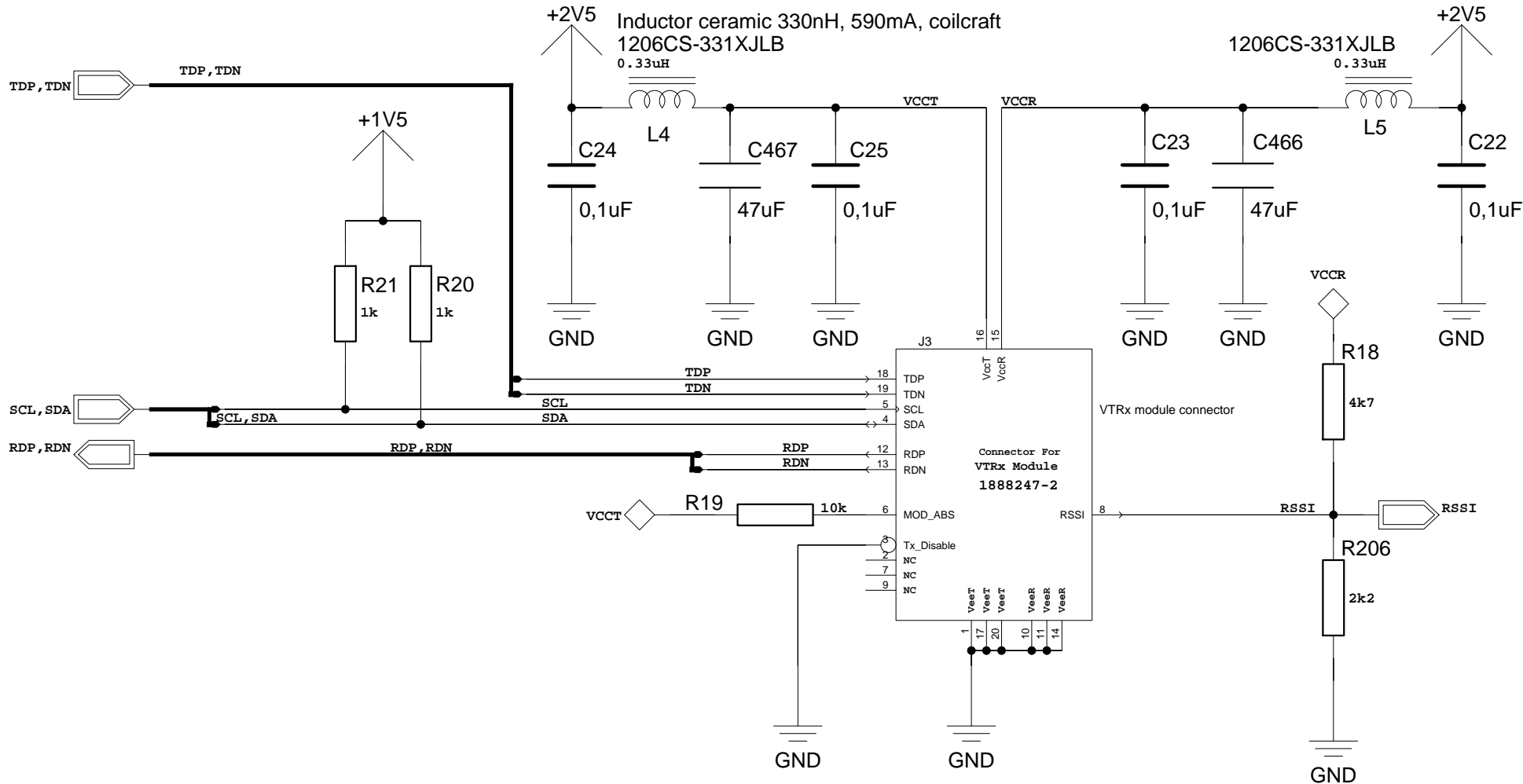



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY Jody Wisman	IGF					
CHECKED		SIZE	DIPswitch		VER	REV
DATE 30/06/2017:15:01		B				
TIME:		SHEET 53 of 66				



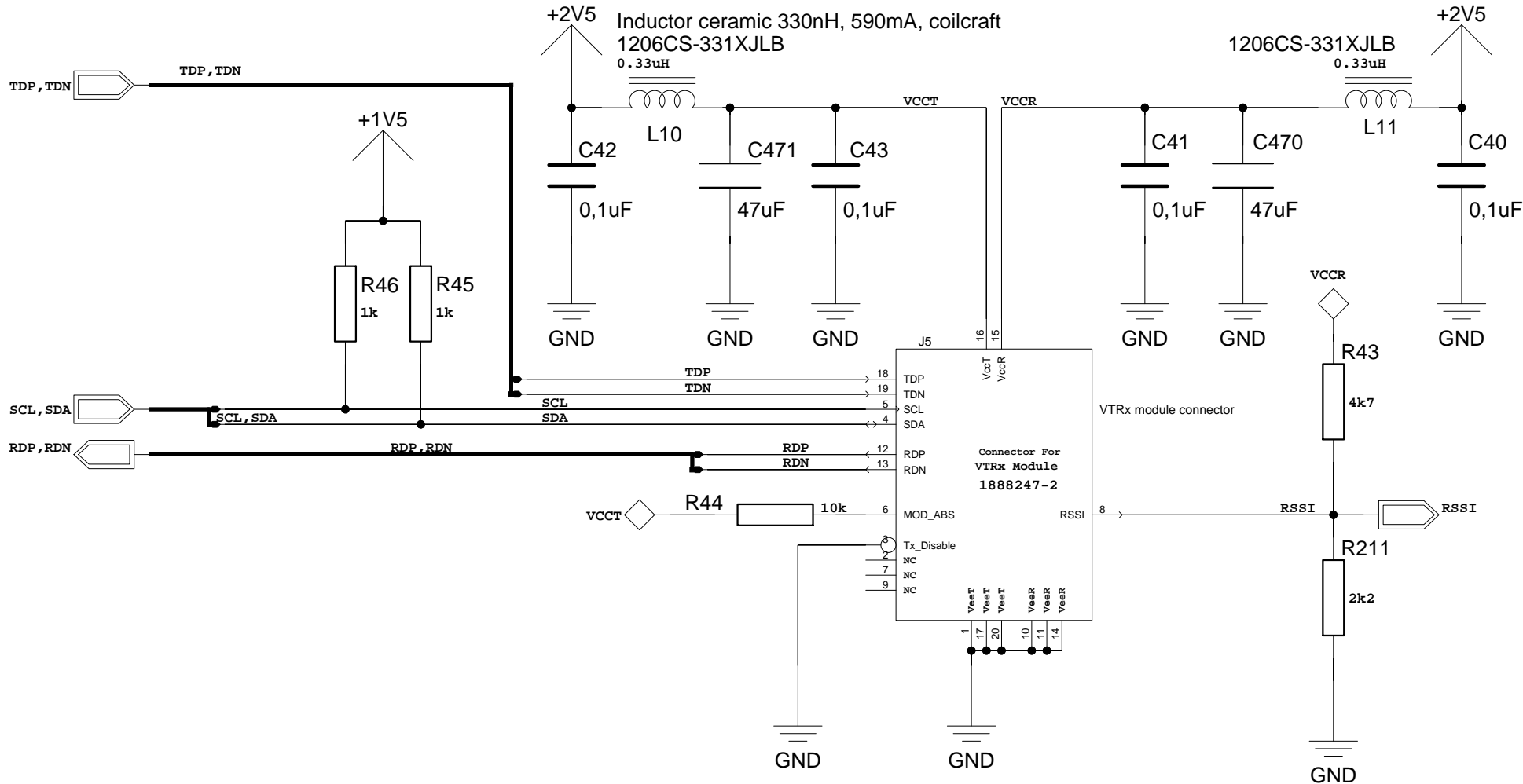
OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	J Wisman	IGF	USB3			
CHECKED						
DATE	04/08/2017:15:12		SIZE	DATA		VER REV
TIME:			C			
		SHEET 54 of 66				






OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF						
CHECKED		SIZE	1 VTRx1			VER	REV
DATE	31/07/2017:18:11	A					
TIME:		SHEET 56 of 66					





OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF					
CHECKED		SIZE	1 VTRx2		VER	REV
DATE	31/07/2017:18:11	A				
TIME:		SHEET 57 of 66				



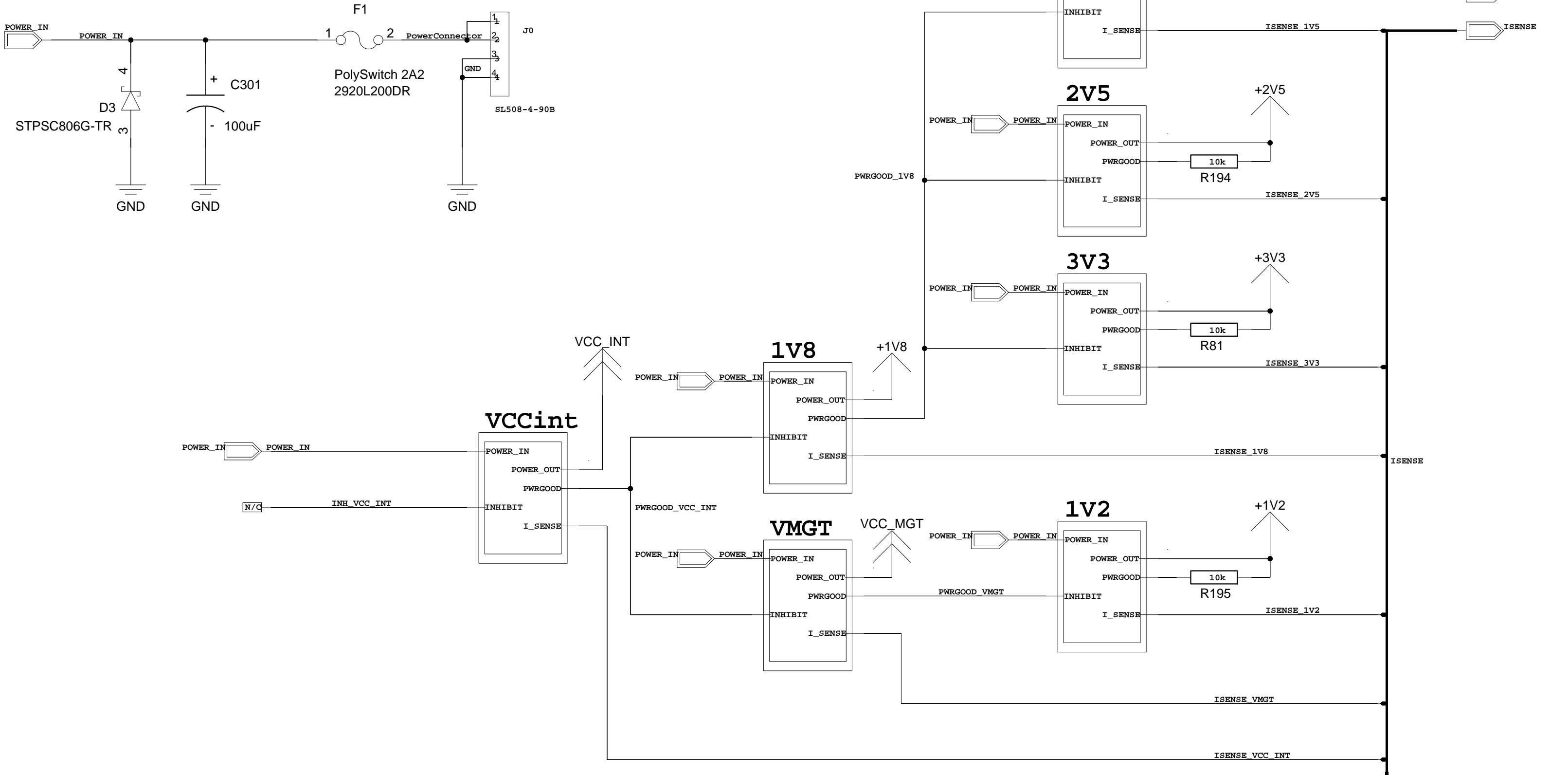
D

C

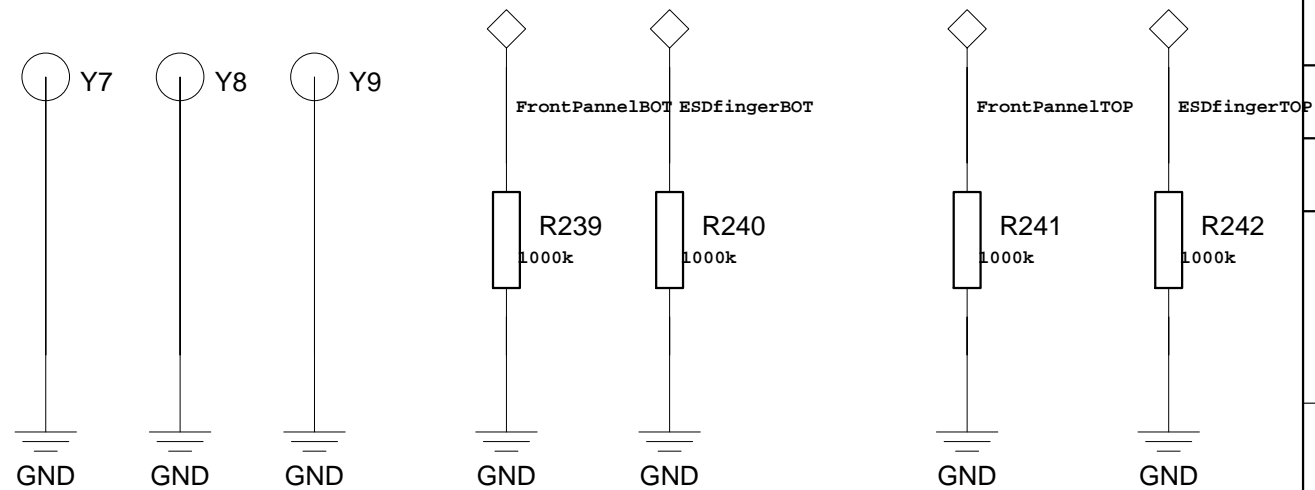
B


A

H=4ND; V=4LA



Power	(V)	R <sub>SET</sub>	R <sub>RT</sub>	Device	Rail	I(mA)	I <sub>TOTAL</sub> (A)	R <sub>SENSE</sub>	R <sub>DIV</sub>
VCC_INT	0,95	2k45	1000k	XCKU060	V <sub>CCINT</sub> , V <sub>CCINT_IO</sub> , V <sub>CCBRAM</sub>	4200	4,2	5	5 (1k2/300)
VCC_MGT	1	2k15	1000k	XCKU060	V <sub>MGTAVCC</sub>	2300	2,3	10	10 (2k7/300)
+1V2	1.2	1k43	487k	XCKU060	V <sub>MGTAVTT</sub>	2400	2,6	10	10 (2k7/300)
				FX3		200			
				3*GBTx		3046			
+1V5	1.5	953	487k	SCA		64	3,4	5	5 (1k2/300)
				PA3		336			
+1V8	1.8	715	169k	XCKU060	V <sub>CCAUX</sub> , V <sub>CCAUXIO</sub>	1581	1,6	10	10 (2k7/300)
					V <sub>MGTCCAUX</sub>	19			
					V <sub>CCADC</sub>				
+2V5	2.5	453	90k9	PA3	V <sub>CCI_LVDS</sub>	14	0,9	20	2 (1k2/1k2)
				V <sub>TTx</sub> +2*V <sub>TRx</sub>		900			
+3V3	3.3	318	90k9	7*AD626		2	24m	500	5 (1k2/300)
				2*CAN		1			
				FLASH_MEM		1			
				MLVDS_BUF		20			



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	J Wisman		IGF				
CHECKED							
DATE	30/06/2017:15:01						
TIME:			SIZE	1		VER	REV
			C				
SHEET 59		of 66					

D

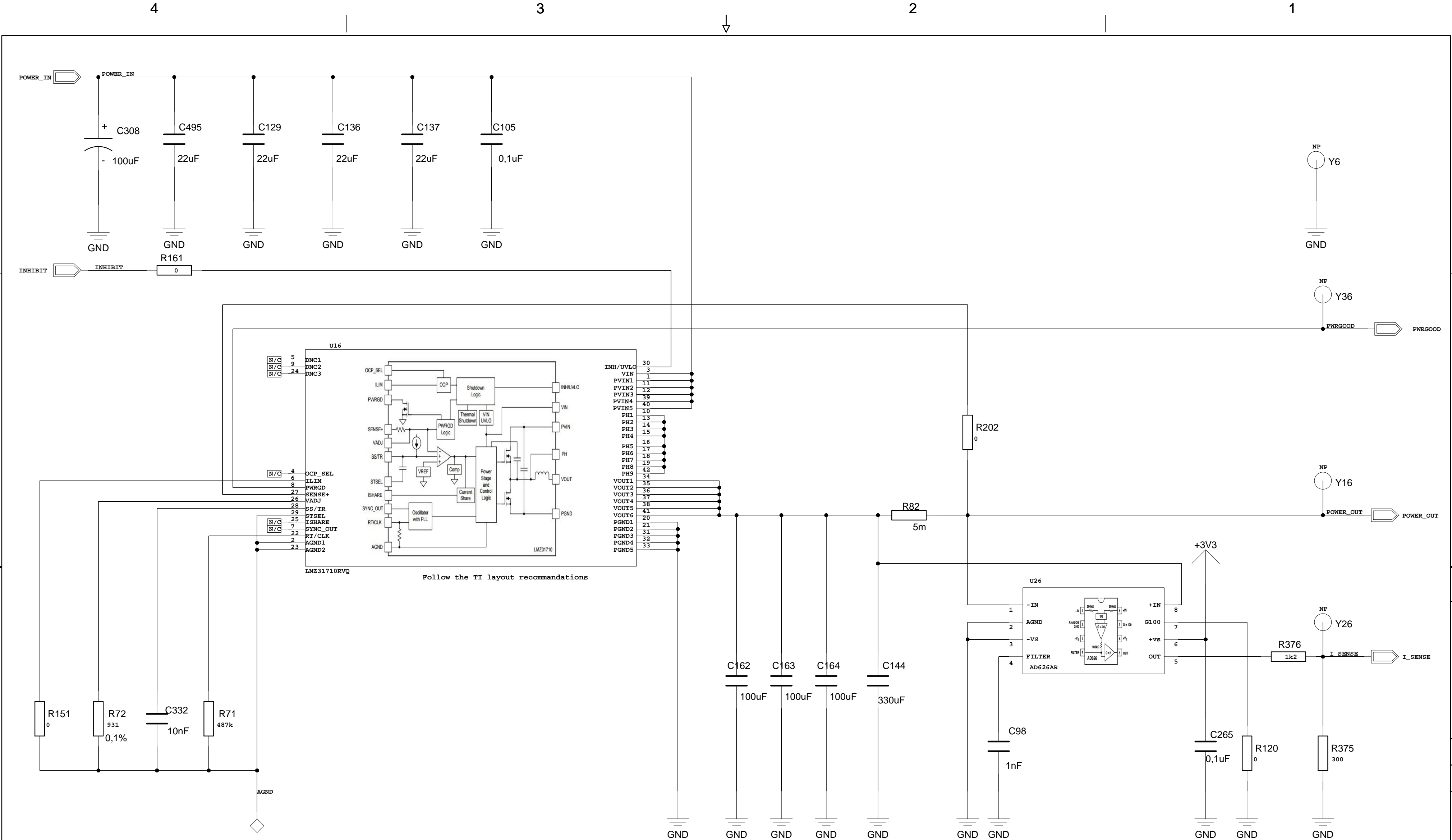
C


SH

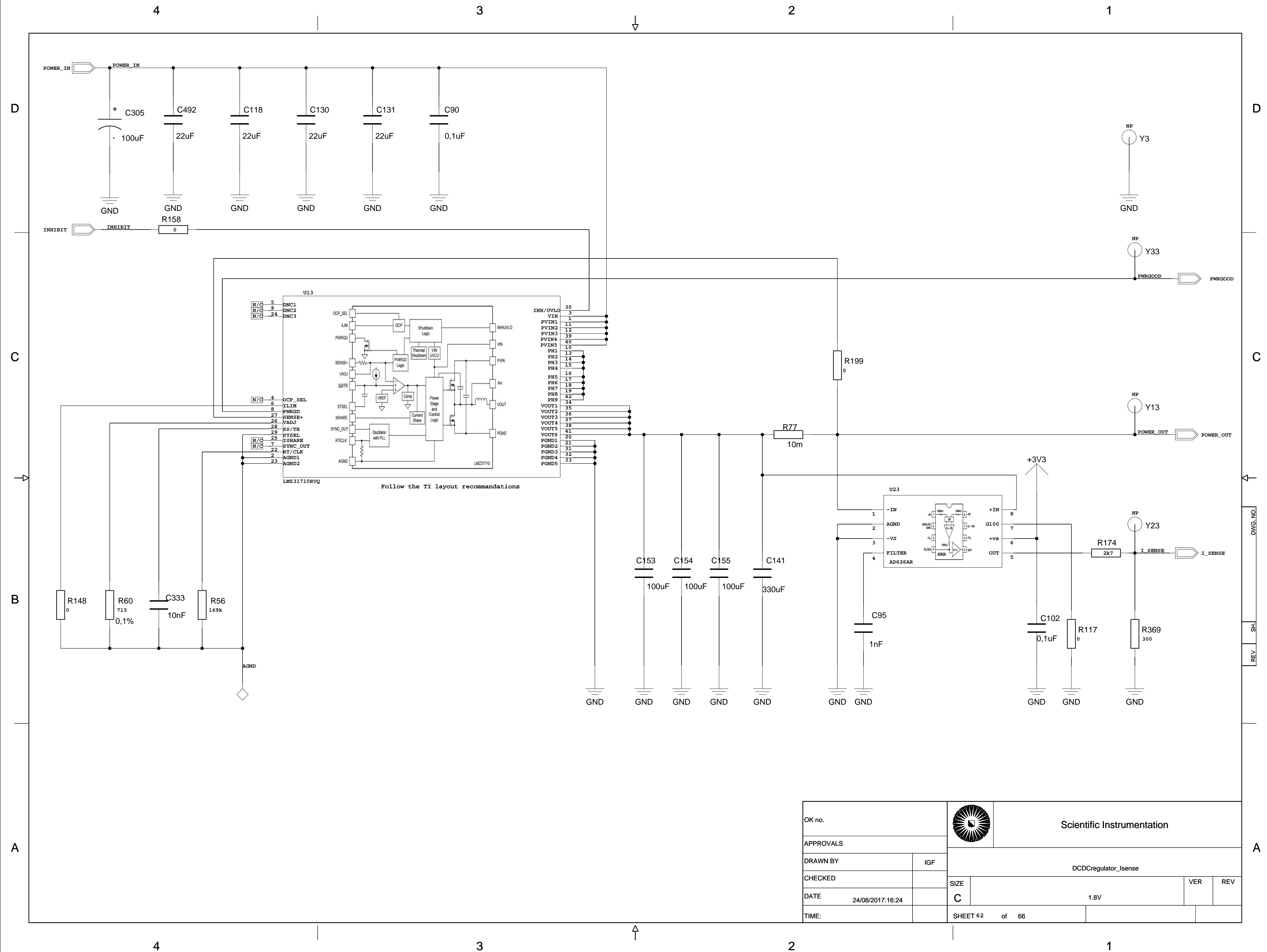
REV

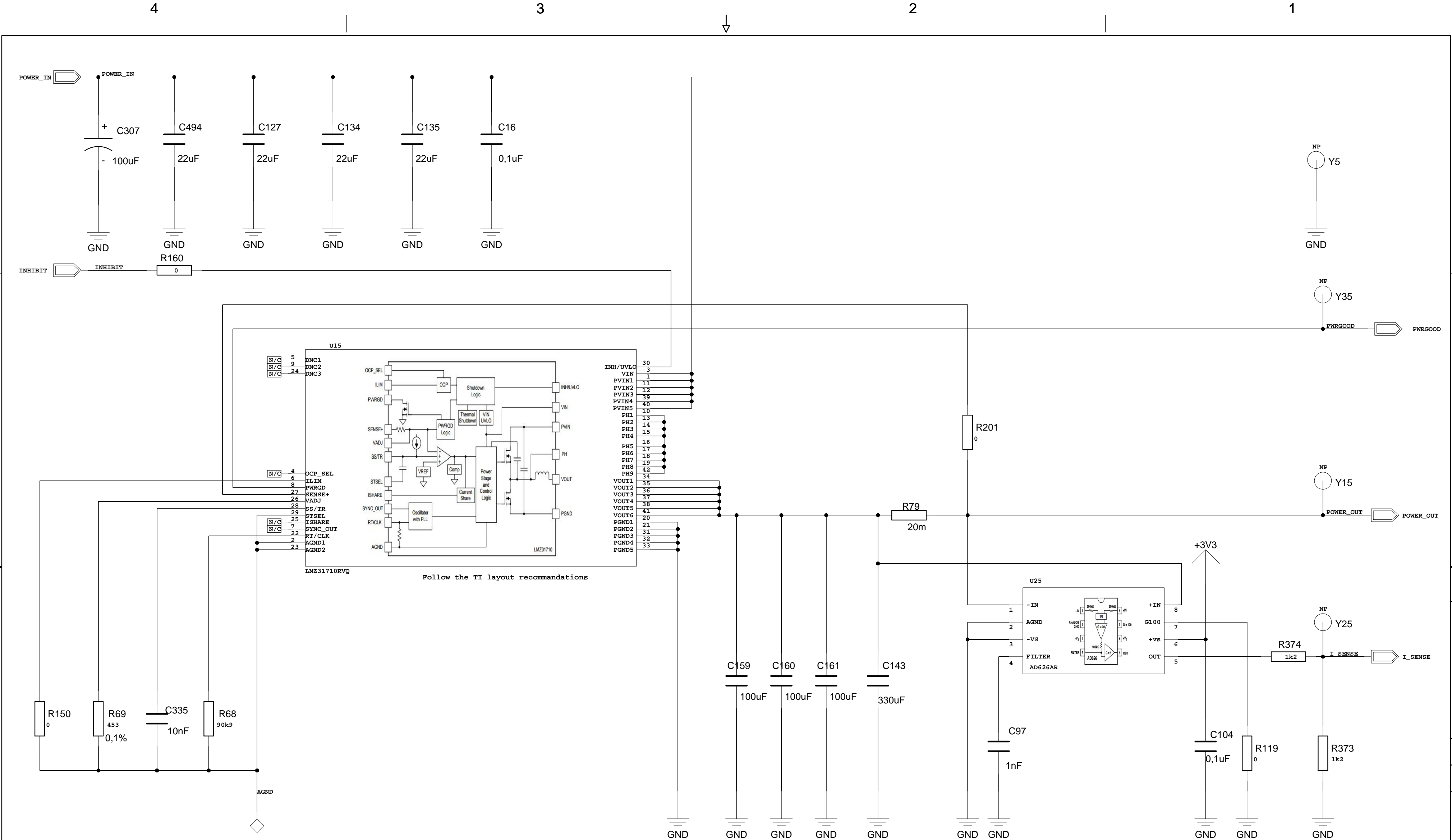
A




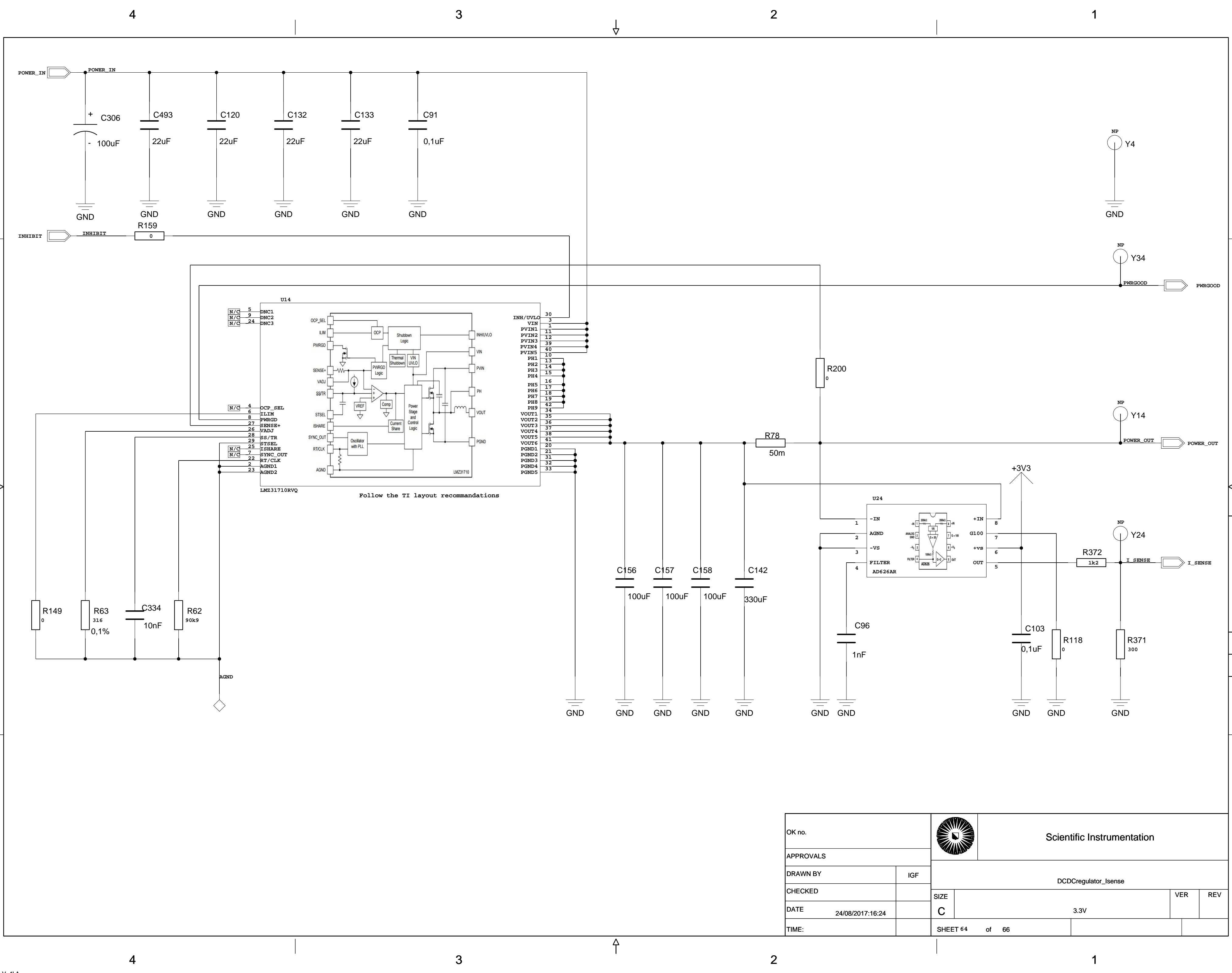



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY	IGF	DCDCregulator_Isense					
CHECKED		SIZE				VER	REV
DATE	24/08/2017:16:24	C	1.5V				
TIME:		SHEET 61 of 66					



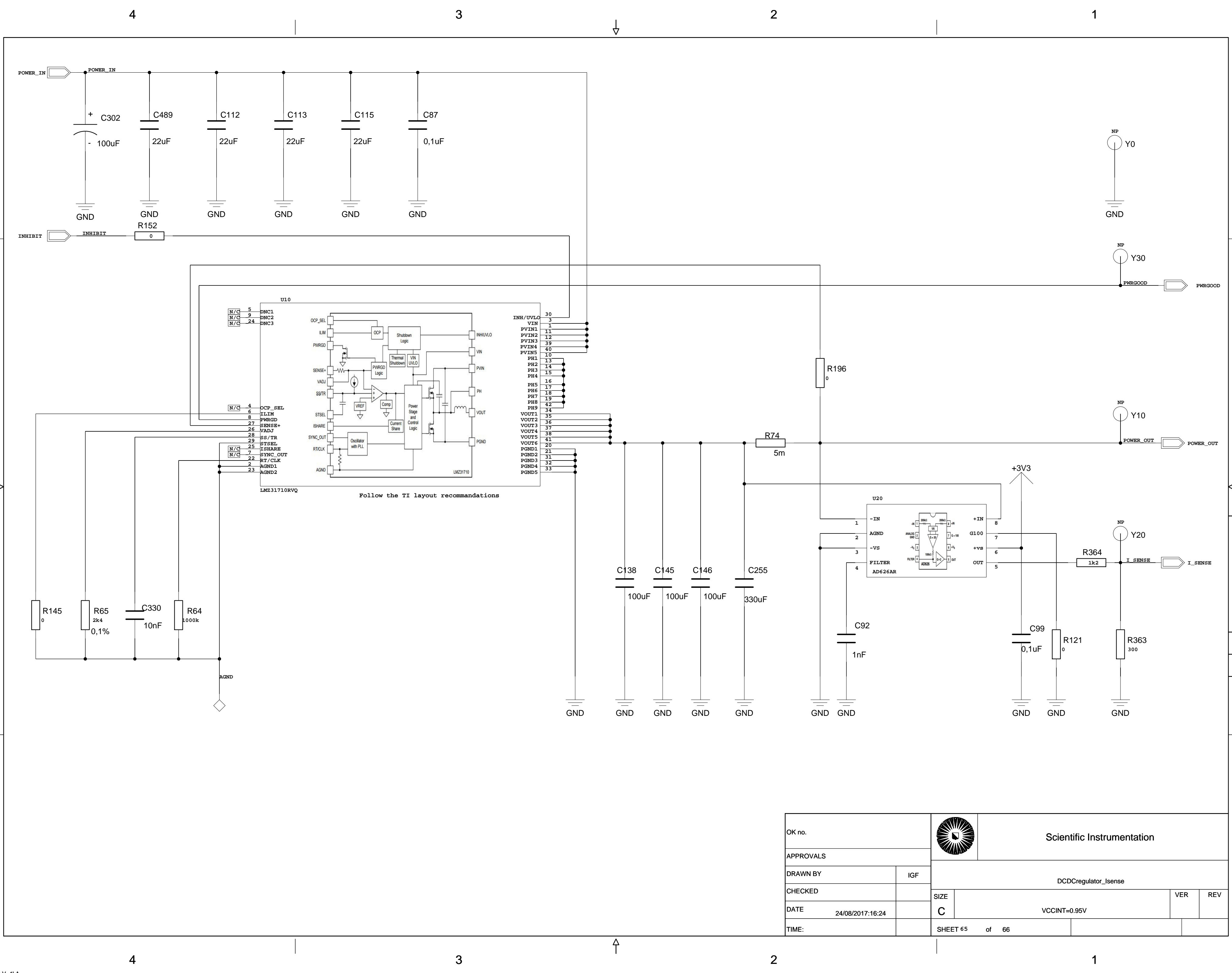



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY		IGF	DCDCregulator_Isense				
CHECKED							
DATE		24/08/2017:16:24	SIZE	2.5V		VER	REV
			C				
TIME:			SHEET 63 of 66				

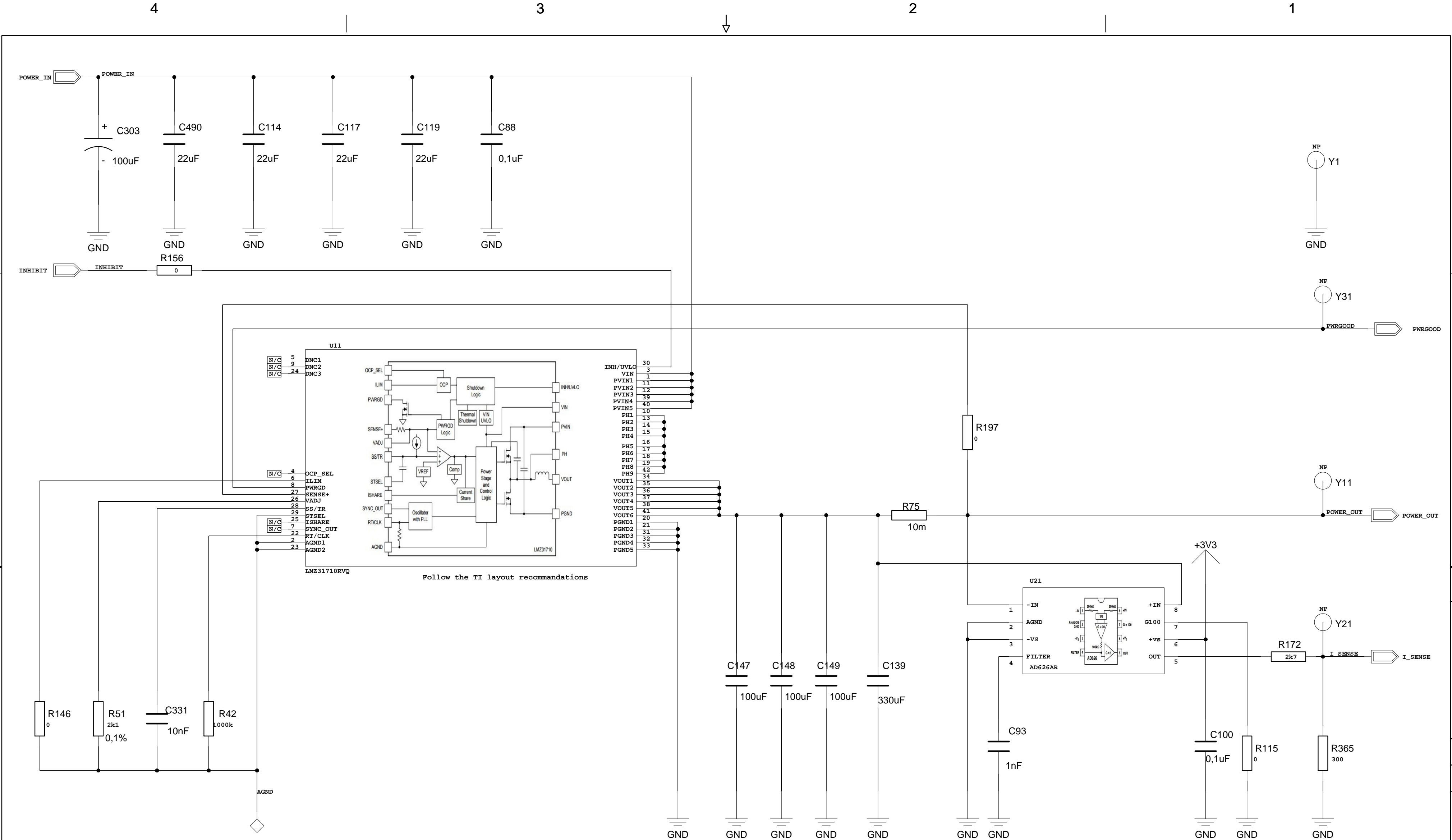



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY		IGF	DCDCregulator_Isense				
CHECKED							
DATE			SIZE	3.3V		VER	REV
24/08/2017:16:24		C					
TIME:			SHEET 64 of 66				





OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	DCDCregulator_Isense				
CHECKED		SIZE			VER	REV
DATE	24/08/2017:16:24	C	VCCINT=0.95V			
TIME:		SHEET 65 of 66				



OK no.			Scientific Instrumentation			
APPROVALS						
DRAWN BY	IGF	DCDCregulator_Isense				
CHECKED						
DATE	24/08/2017:16:24	SIZE	VMGT=1V		VER	REV
		C				
TIME:		SHEET 66 of 66				