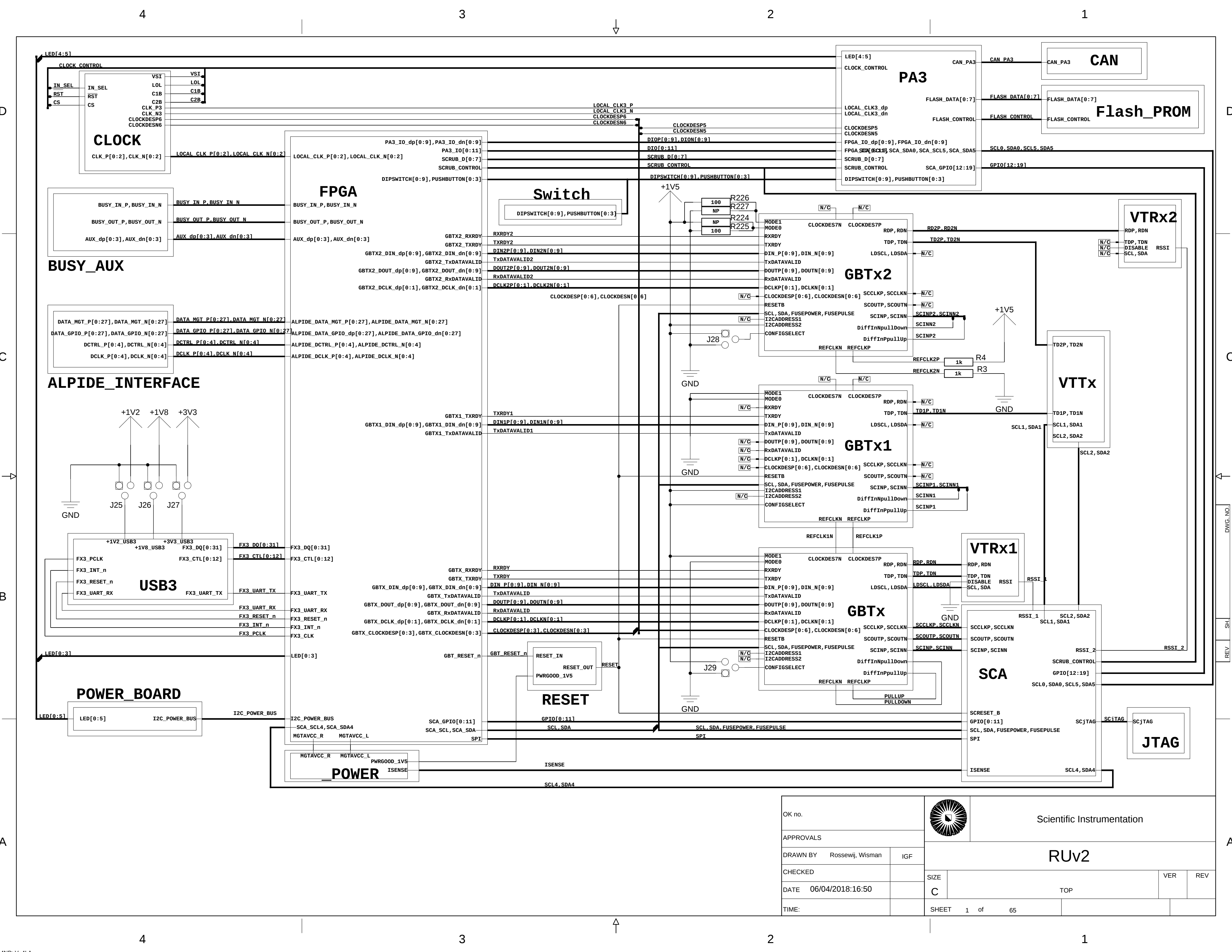
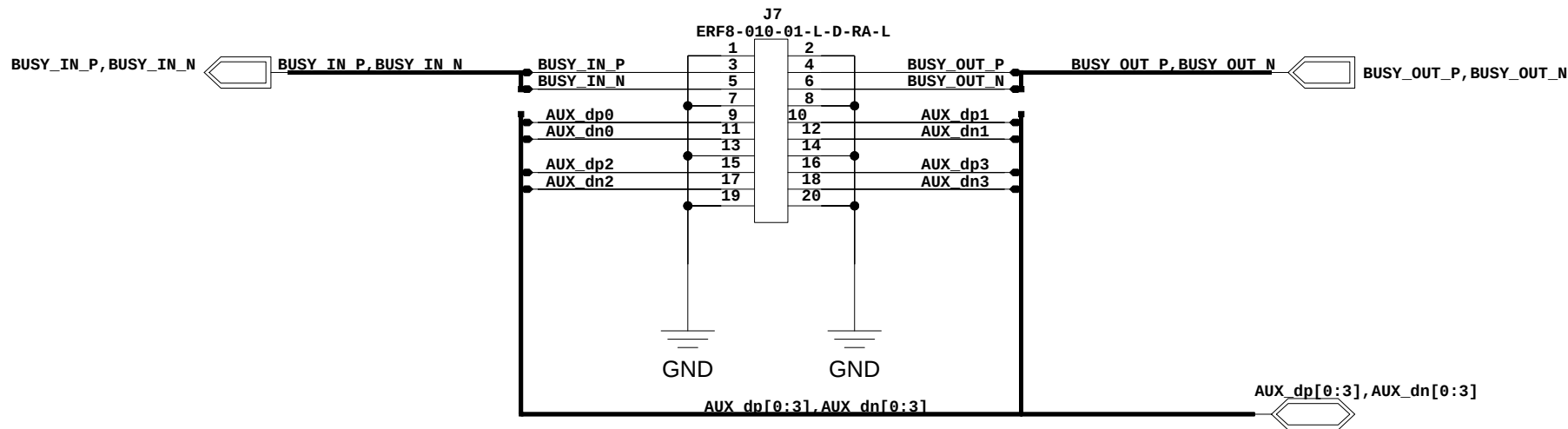
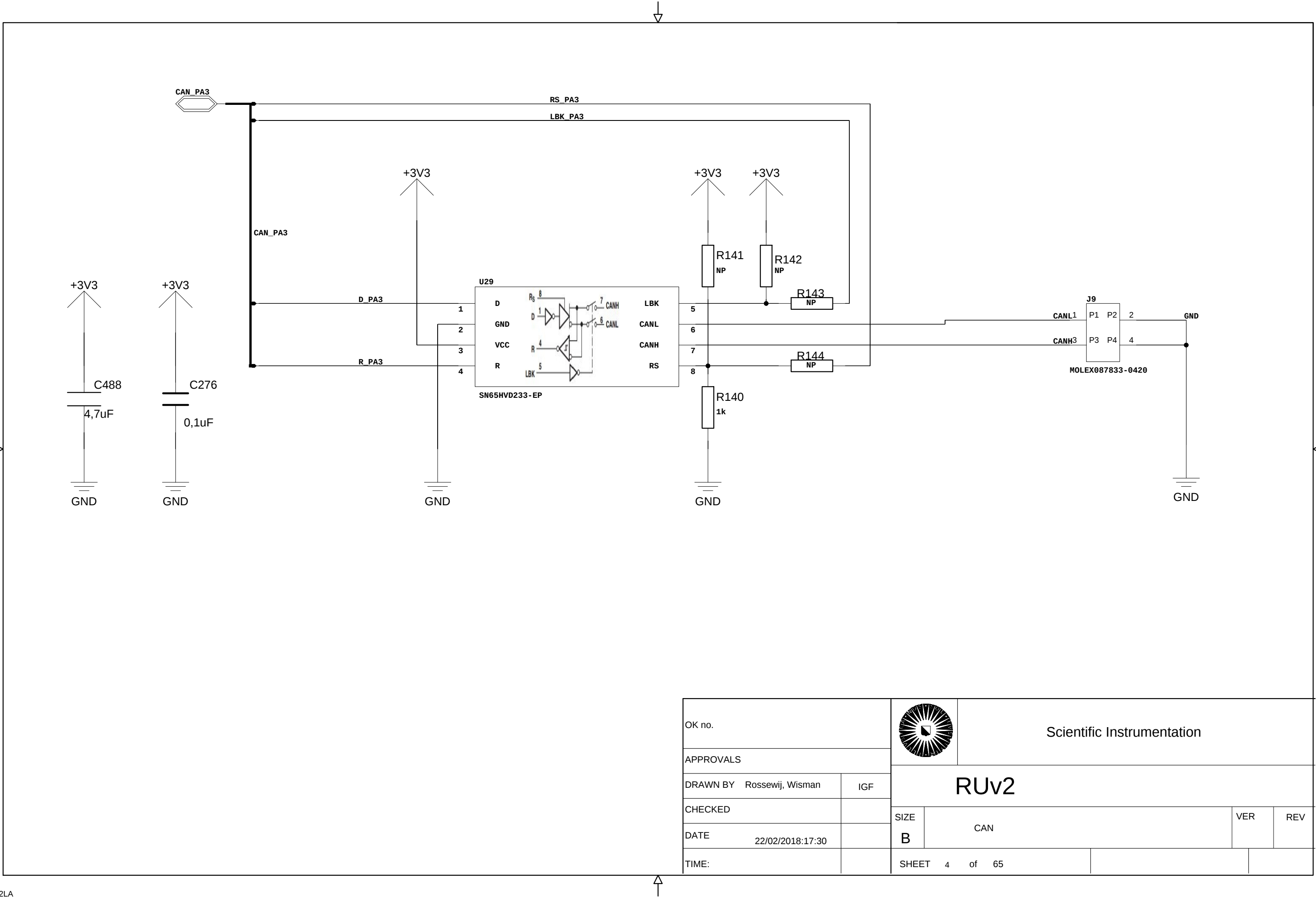




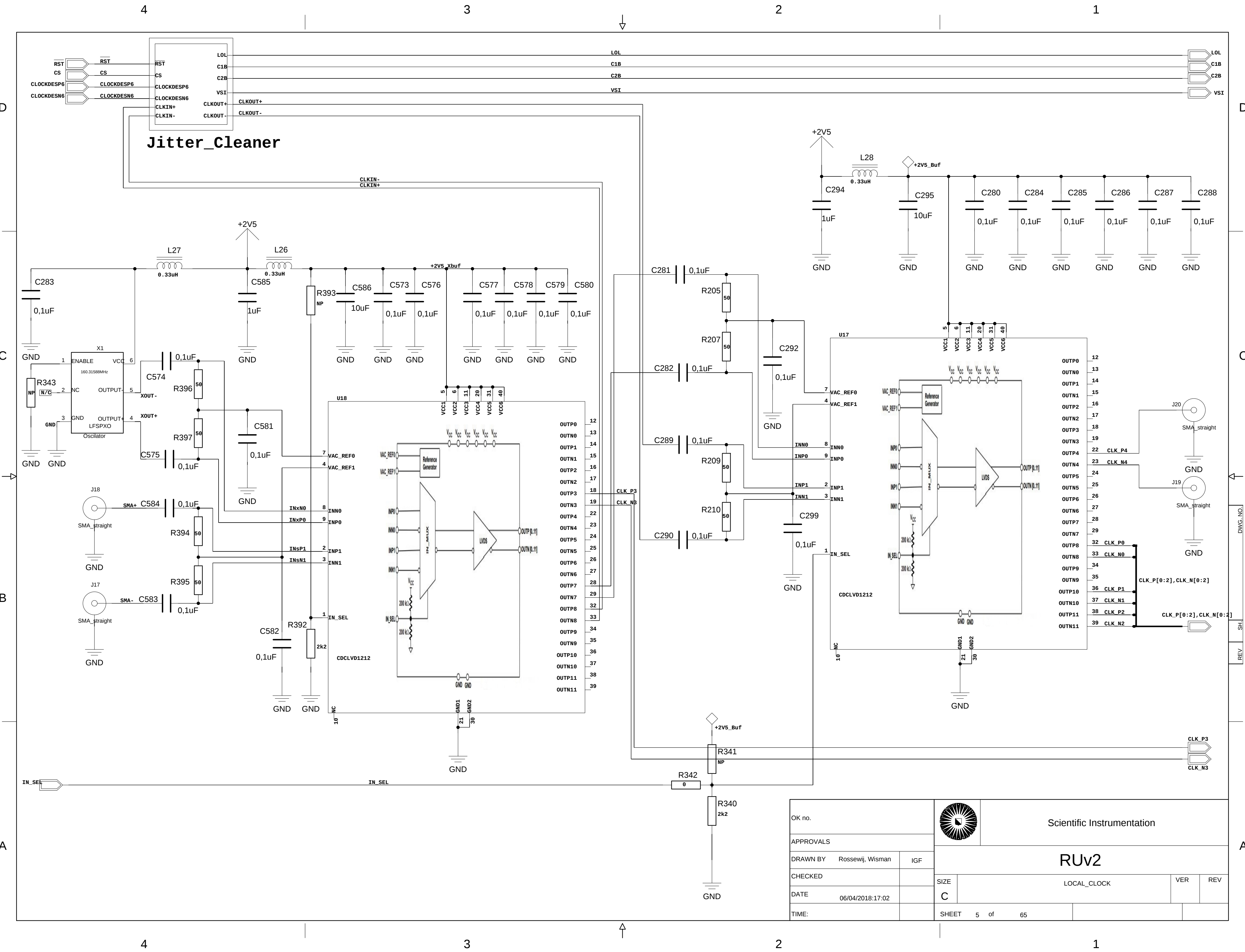
OK no.			Scientific Instrumentation			
APPROVALS			RUV2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE			VER	REV
DATE	06/04/2018:16:50	C	TOP			
TIME:		SHEET 1 of 65				



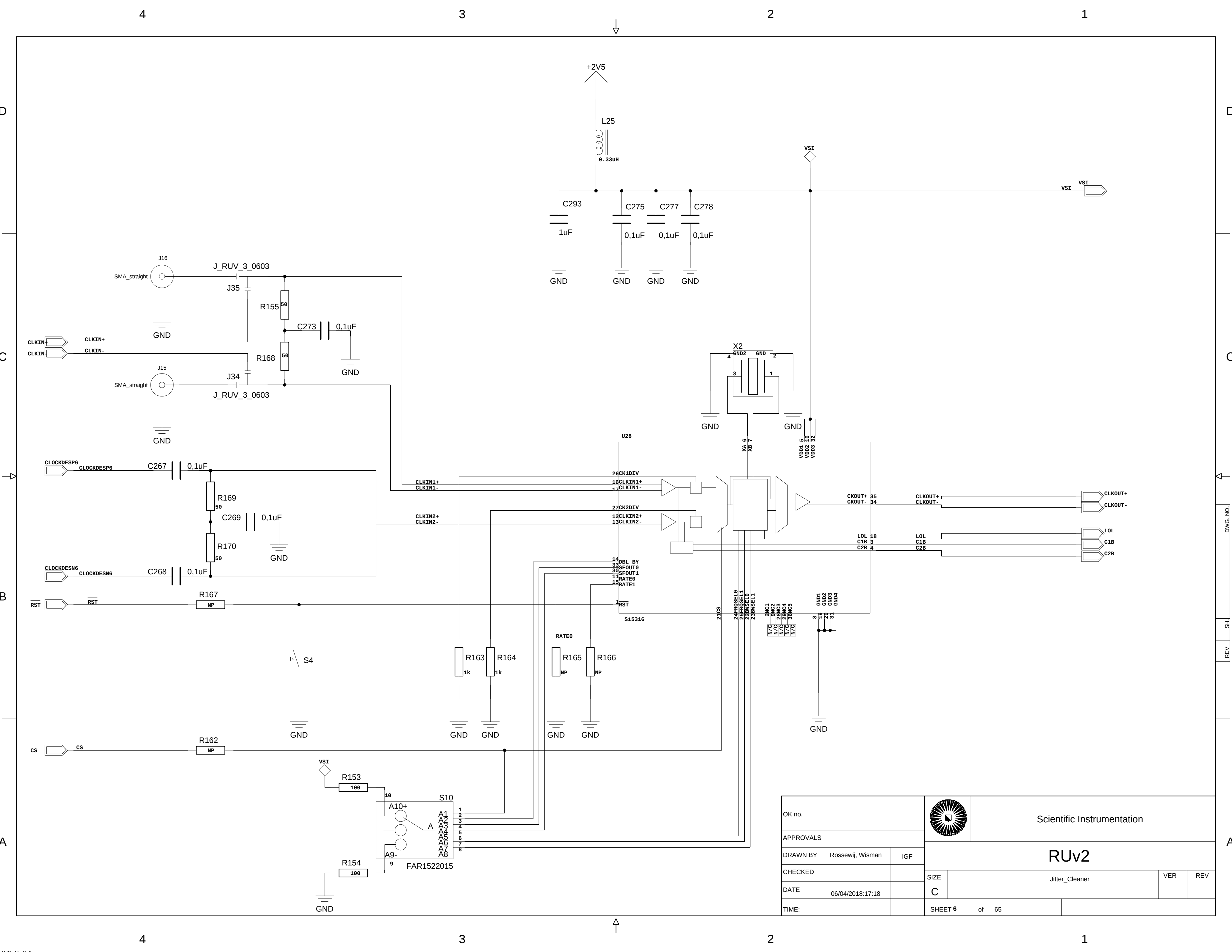
OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Rossewij, Wisman		IGF	RUv2				
CHECKED							
DATE 22/02/2018:15:25			SIZE A	BUSY_AUX		VER	REV
TIME:			SHEET 3 of 65				



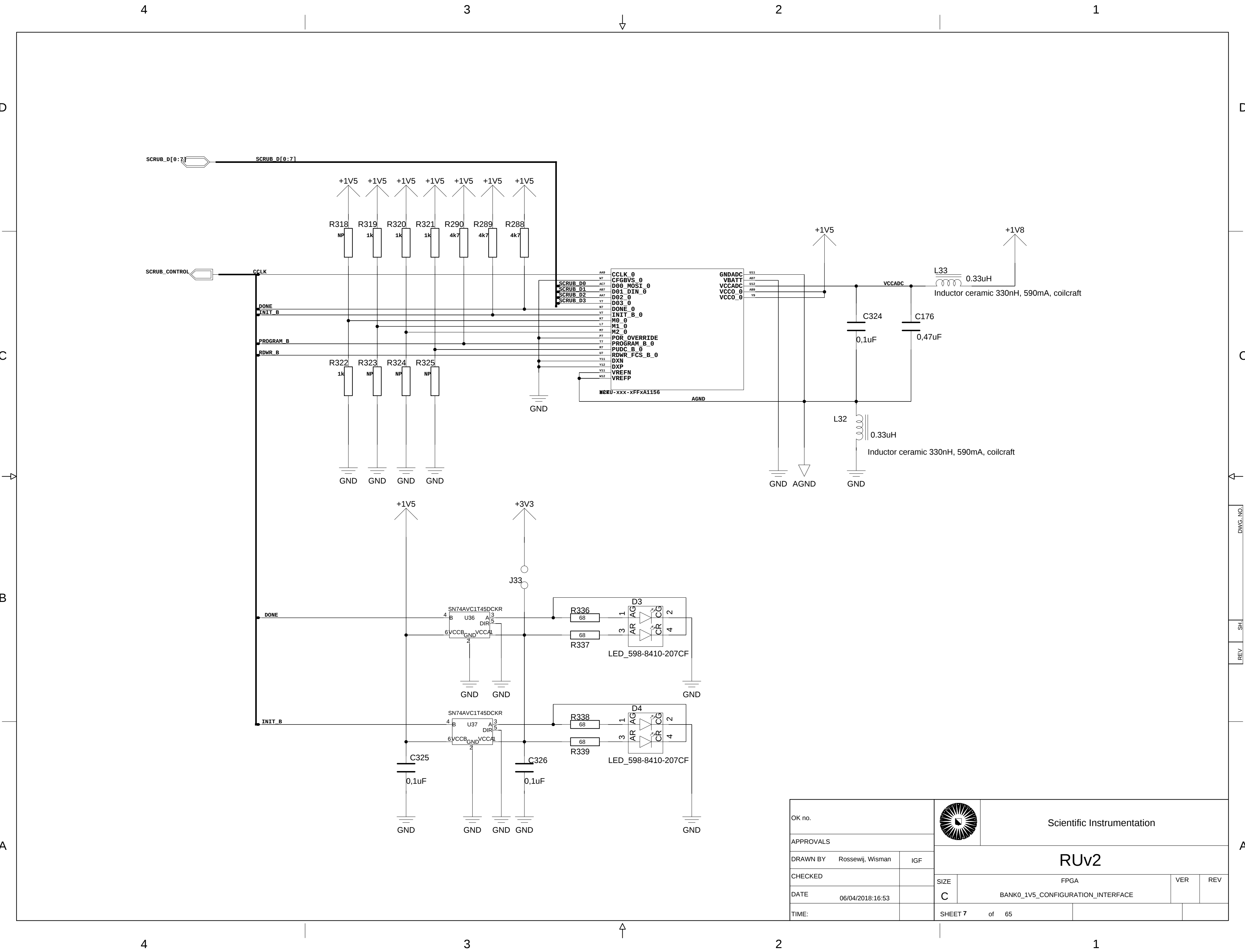
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	CAN		VER	REV
DATE	22/02/2018:17:30	B				
TIME:		SHEET 4 of 65				



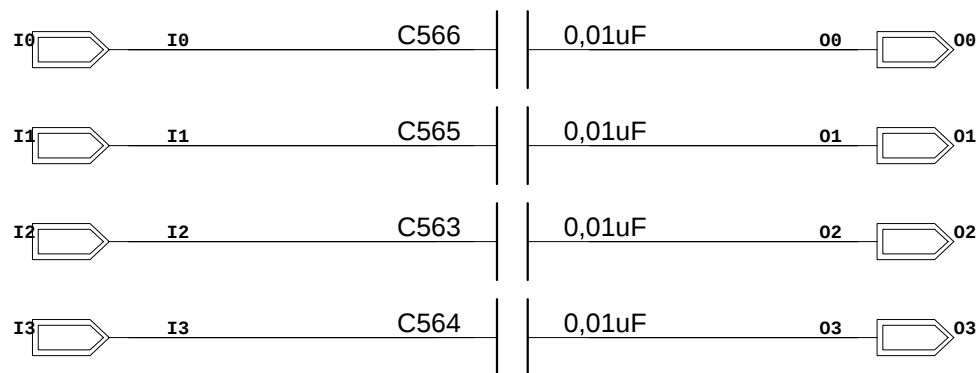
OK no.			Scientific Instrumentation		
APPROVALS			RUV2		
DRAWN BY	Rossewij, Wisman	IGF			
CHECKED		SIZE	LOCAL_CLOCK	VER	REV
DATE	06/04/2018:17:02	C			
TIME:		SHEET 5 of 65			



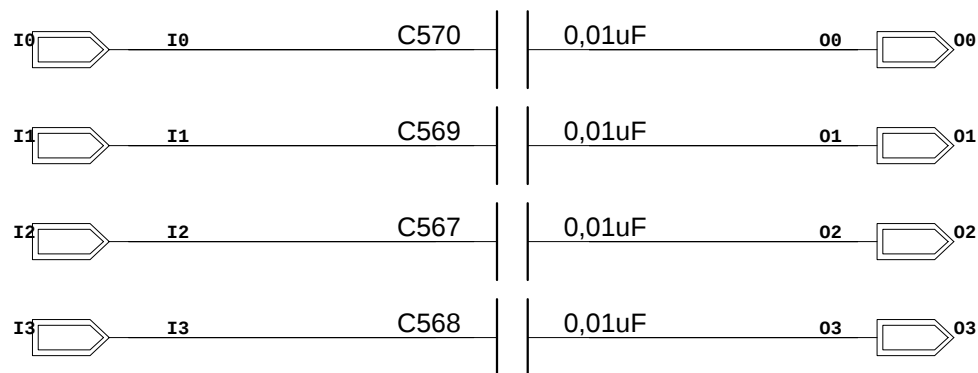
OK no.			Scientific Instrumentation				
APPROVALS			RUv2				
DRAWN BY	Rossewij, Wisman	IGF					
CHECKED		SIZE			Jitter_Cleaner	VER	REV
DATE	06/04/2018:17:18	C					
TIME:		SHEET 6 of 65					



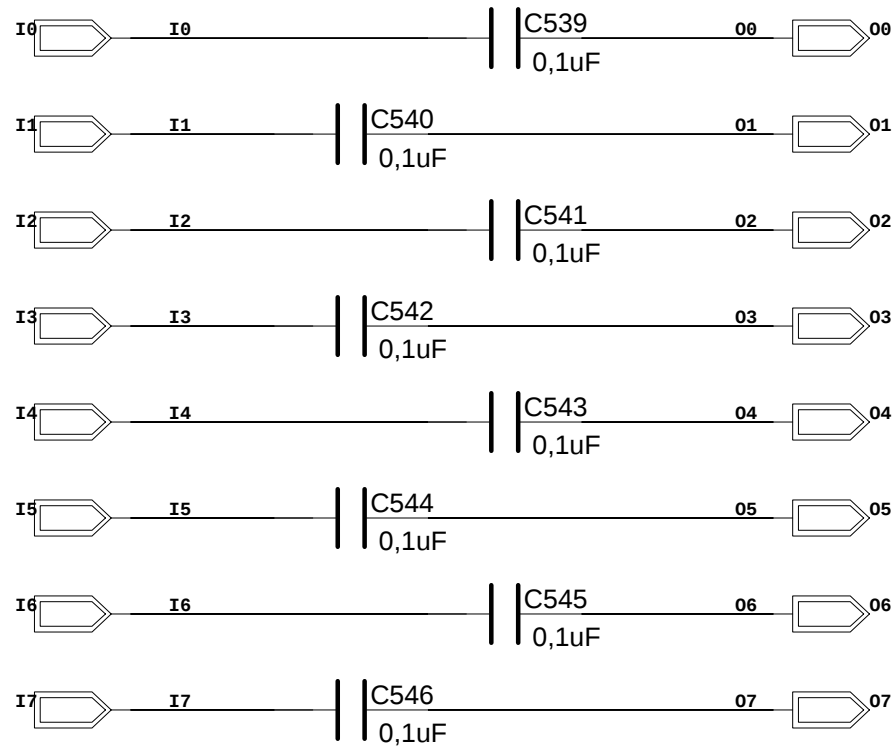
OK no.			Scientific Instrumentation					
APPROVALS			RUv2					
DRAWN BY	Rossewij, Wisman	IGF						
CHECKED			SIZE		FPGA	VER	REV	
DATE	06/04/2018:16:53		C	BANK0_1V5_CONFIGURATION_INTERFACE				
TIME:			SHEET 7 of 65					



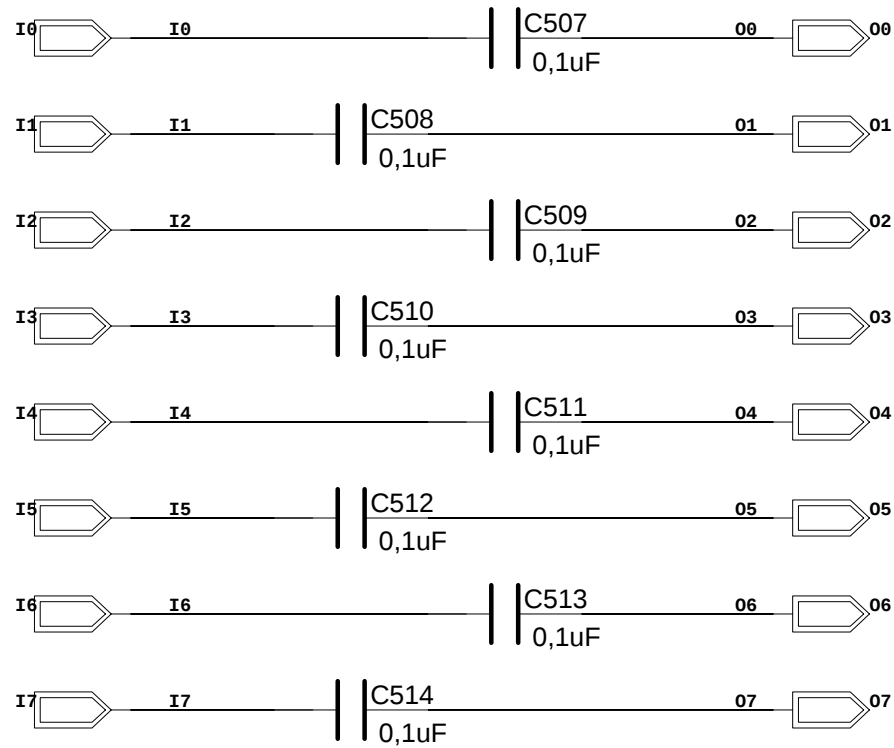
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED			A			
DATE	22/02/2018:15:25					
TIME:			SHEET 9 of 65			



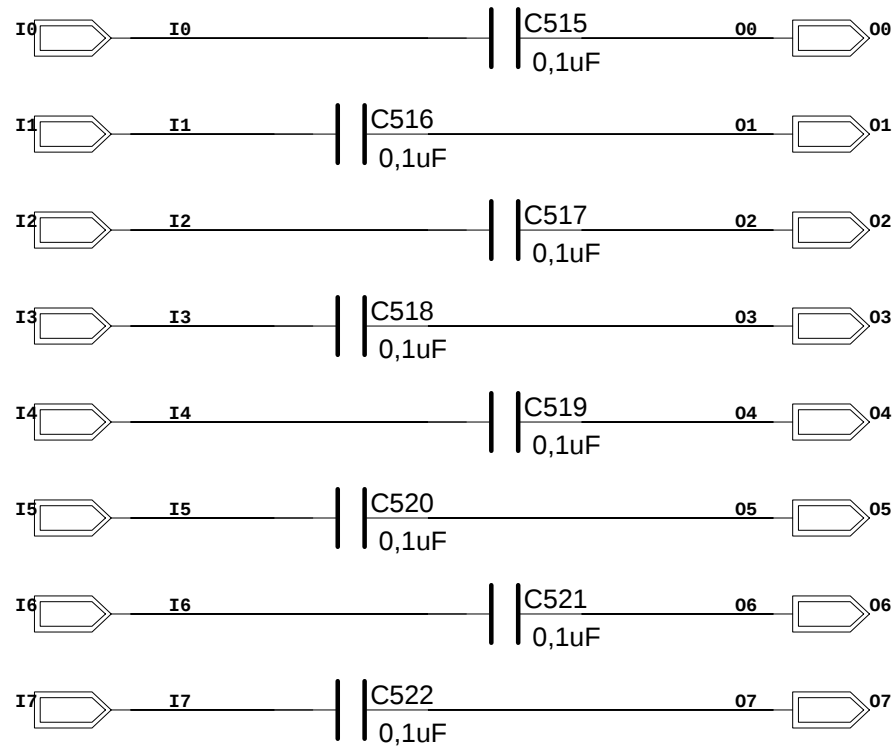
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED			A			
DATE	22/02/2018:15:25					
TIME:			SHEET 10 of 65			



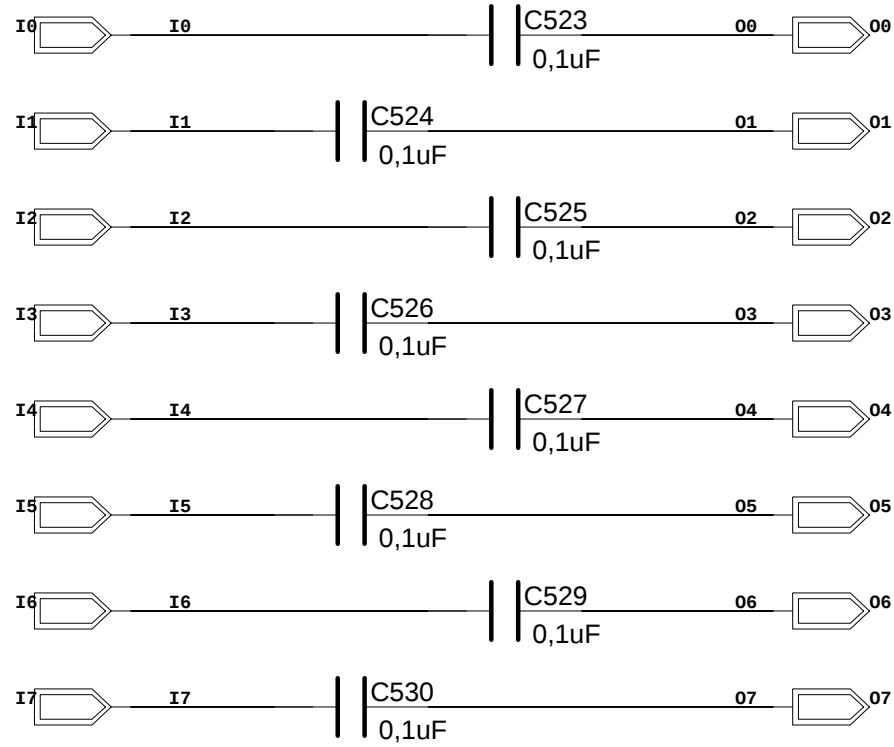
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APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED			A			
DATE	22/02/2018:15:25					
TIME:			SHEET 11 of 65			



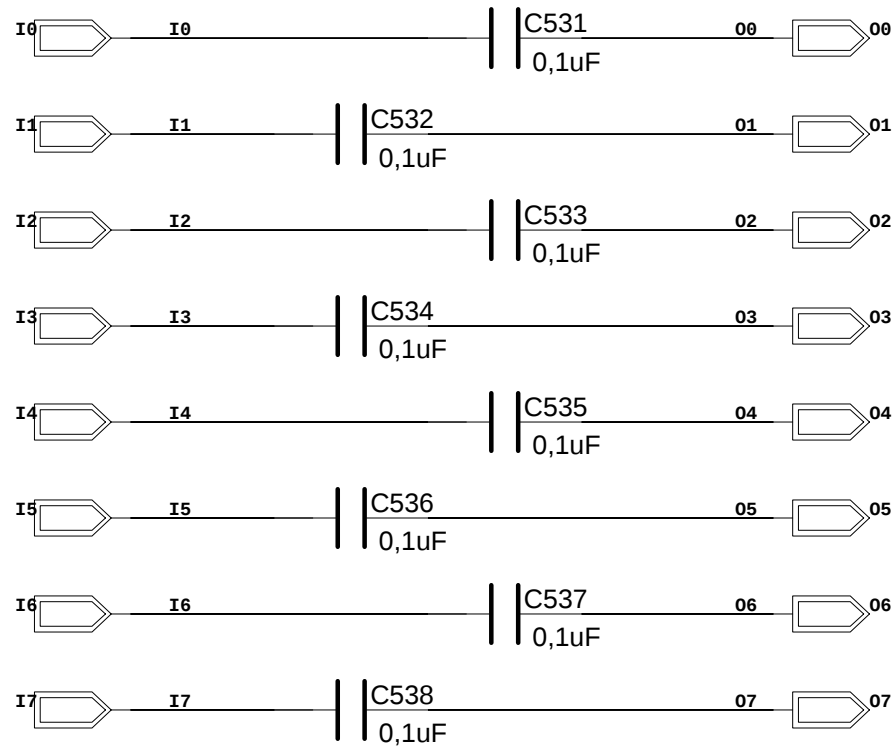
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED			A			
DATE	22/02/2018:15:25					
TIME:			SHEET 12 of 65			



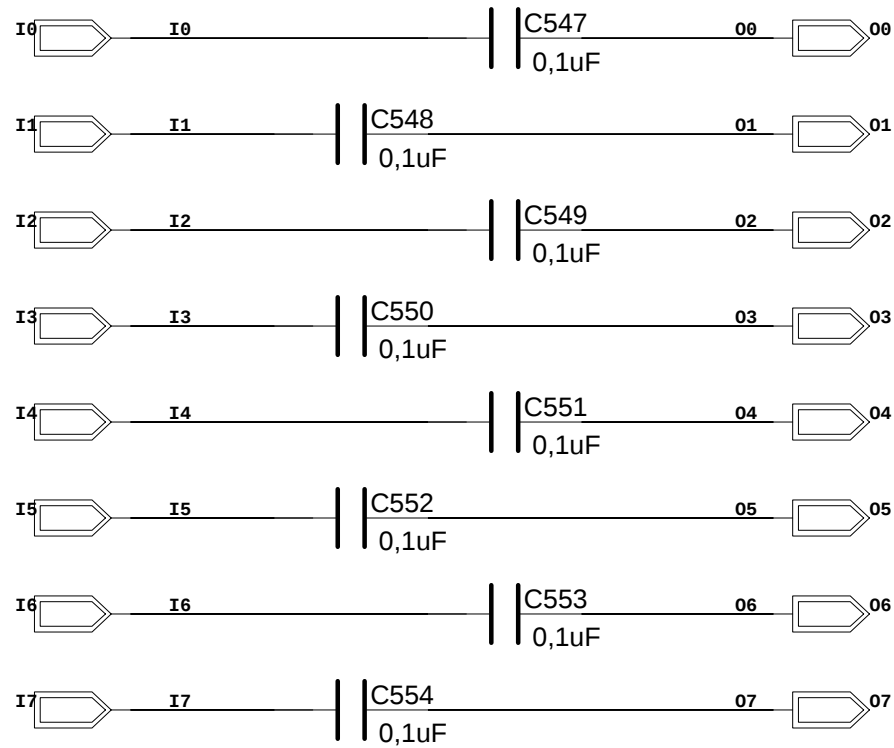
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	AC_DECOUPLING_8_2		VER	REV
DATE	22/02/2018:15:25	A				
TIME:		SHEET 13 of 65				



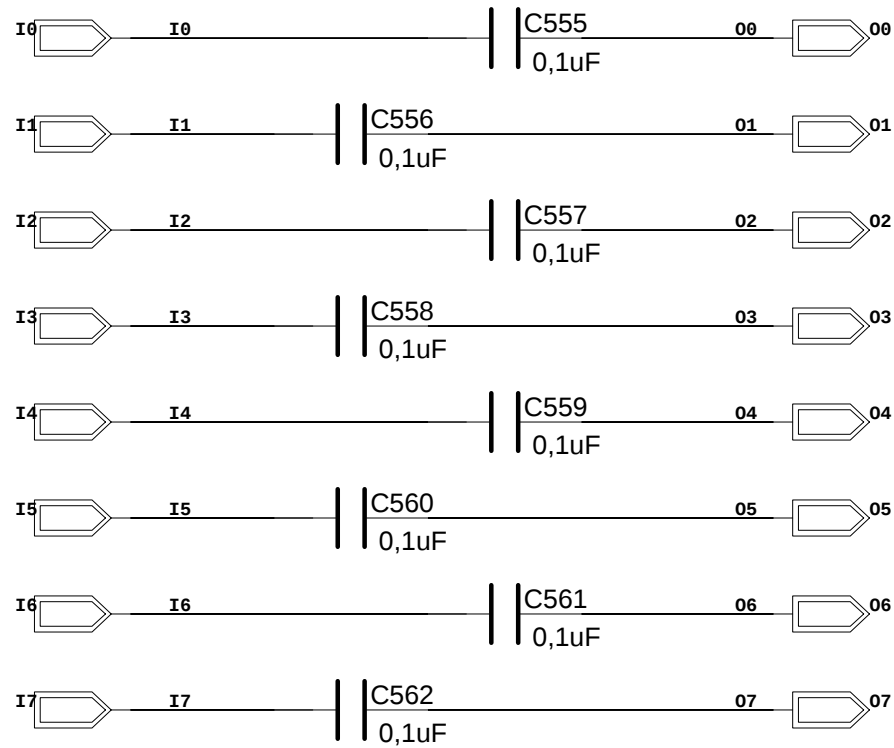
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED			A			
DATE	22/02/2018:15:25					
TIME:			SHEET 14 of 65			



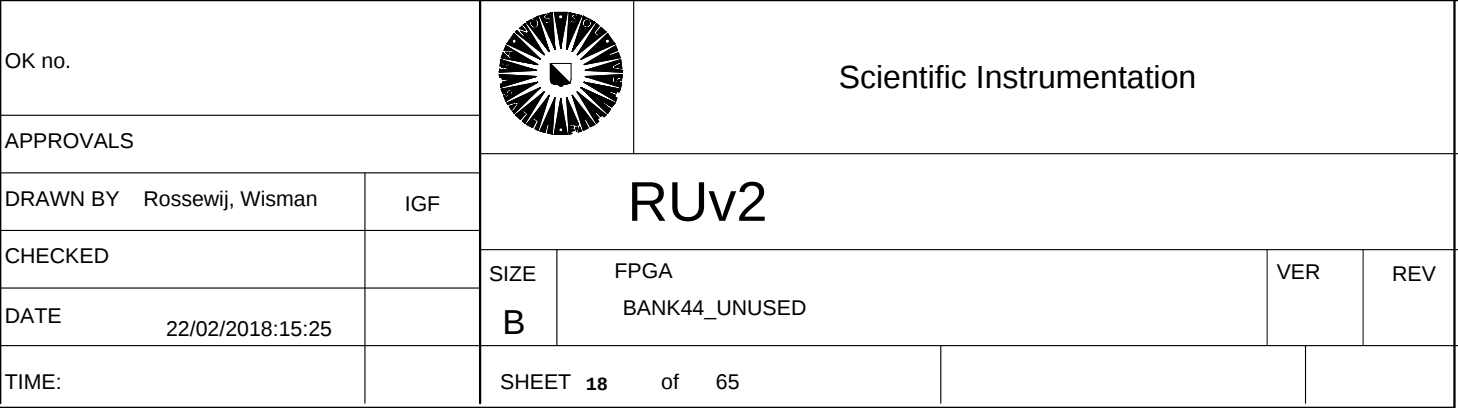
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED			A			
DATE	22/02/2018:15:25					
TIME:			SHEET 15 of 65			

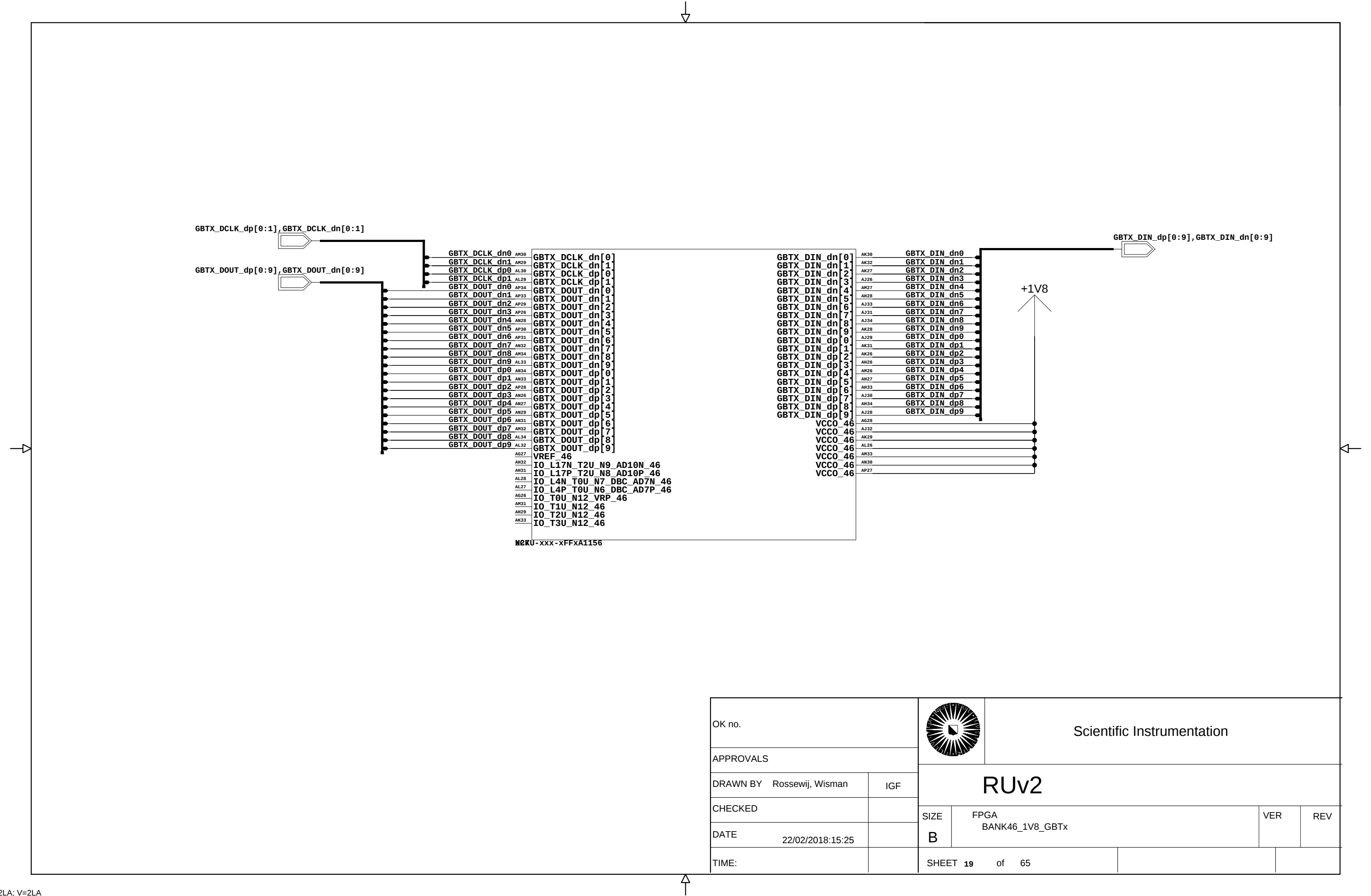


OK no.			Scientific Instrumentation				
APPROVALS			RUv2				
DRAWN BY	Rossewij, Wisman	IGF					
CHECKED			SIZE	AC_DECOUPLING_8_5		VER	REV
DATE	22/02/2018:15:25						
TIME:			SHEET 16 of 65				

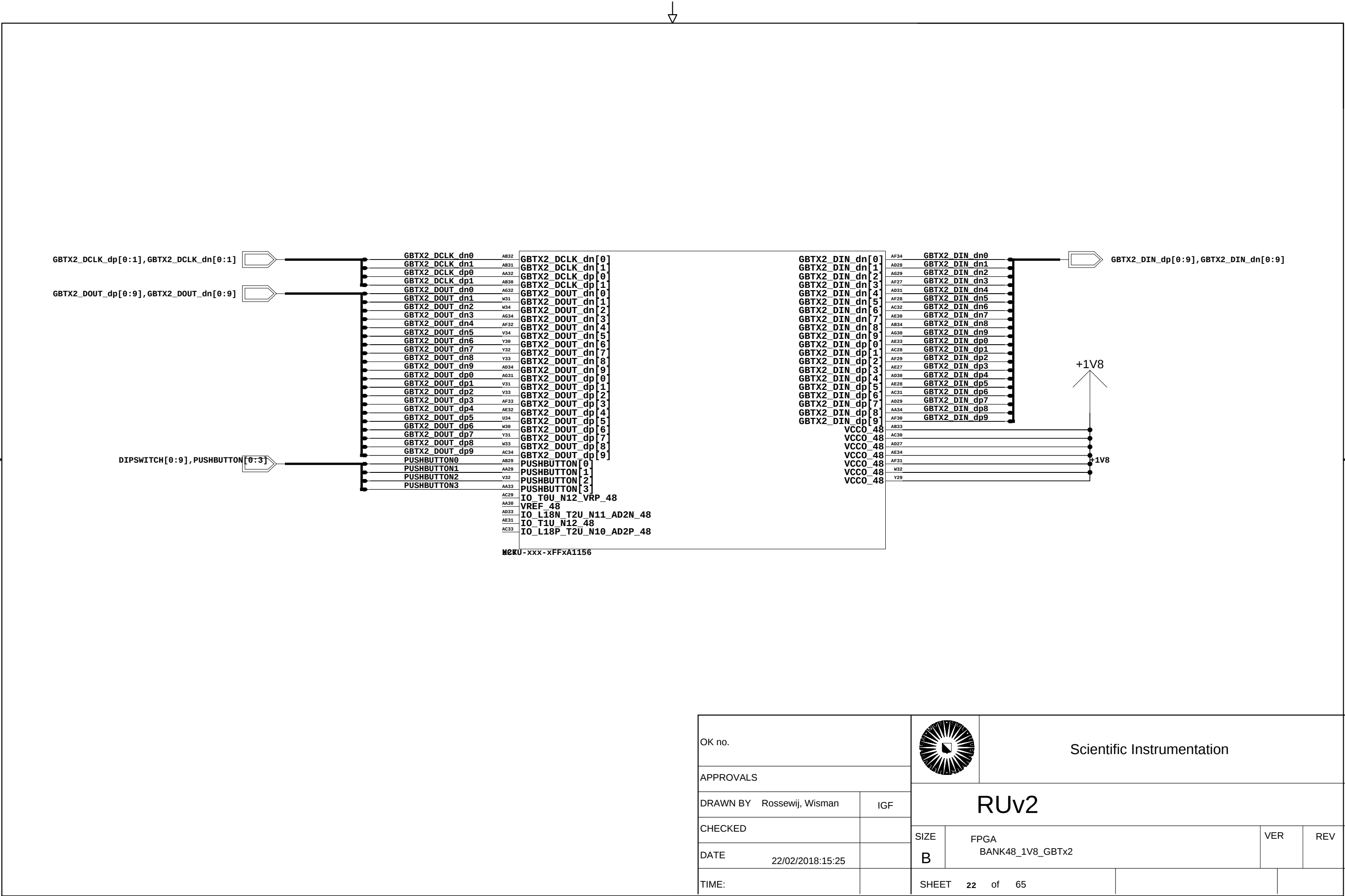


OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
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CHECKED		SIZE	AC_DECOUPLING_8_6		VER	REV
DATE	22/02/2018:15:25	A				
TIME:		SHEET 17 of 65				

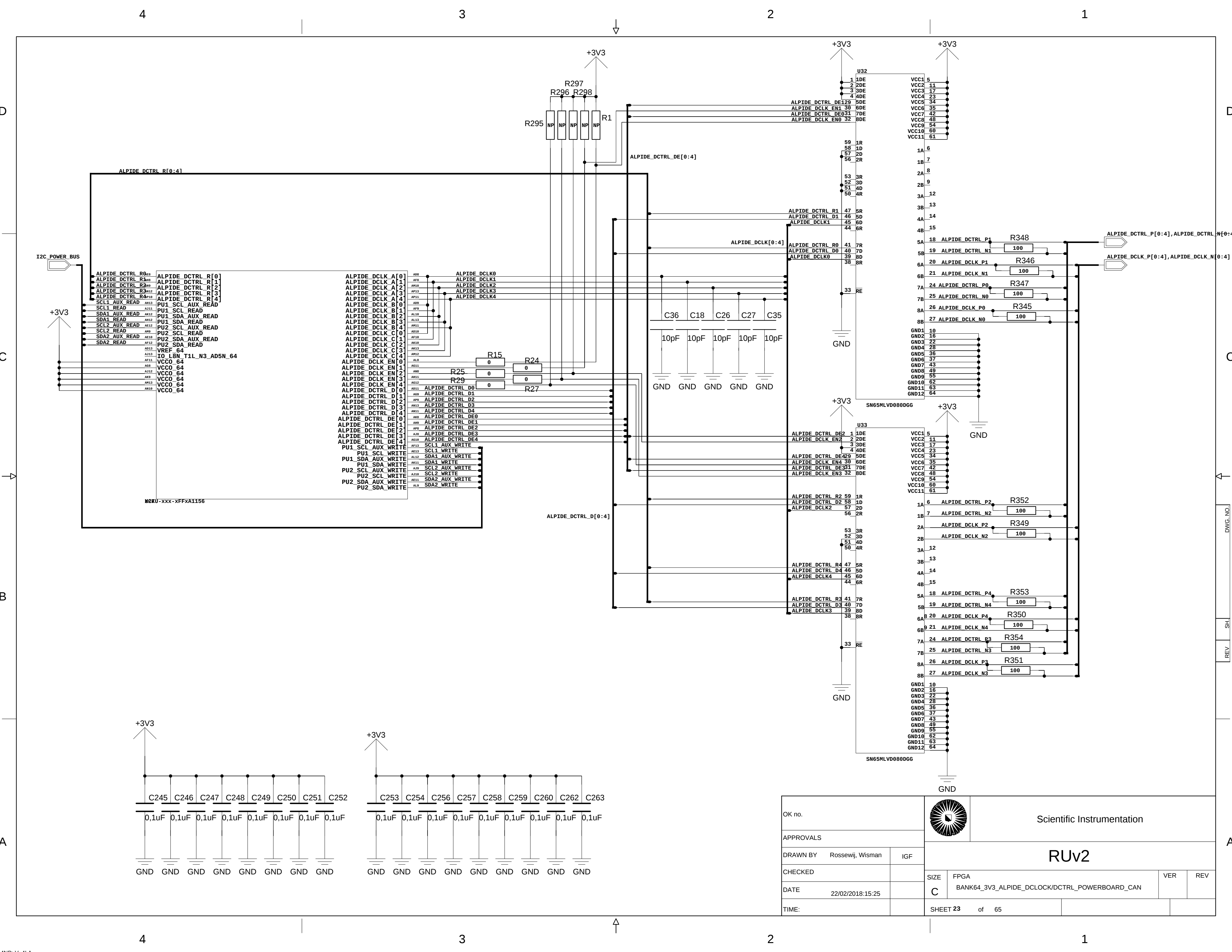




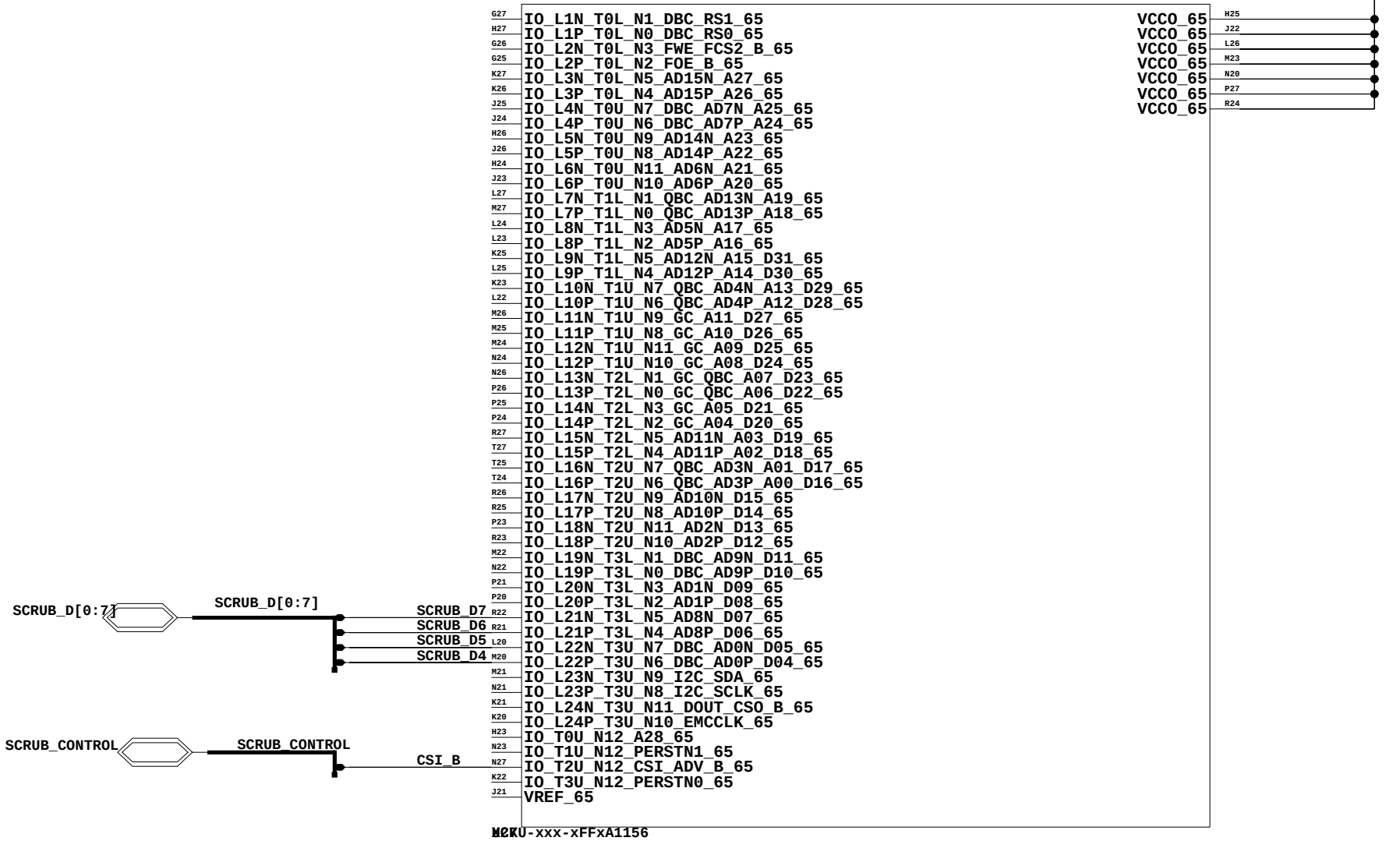
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APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	FPGA BANK46_1V8_GBtx		VER	REV
DATE	22/02/2018:15:25	B				
TIME:		SHEET 19 of 65				



OK no.			Scientific Instrumentation				
APPROVALS			RUv2				
DRAWN BY	Rossewij, Wisman	IGF					
CHECKED		SIZE FPGA BANK48_1V8_GBTx2				VER	REV
DATE	22/02/2018:15:25						
TIME:		SHEET 22 of 65					



OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	FPGA		VER	REV
DATE	22/02/2018:15:25	C	BANK64_3V3_ALPIDE_DCLOCK/DCTRL_POWERBOARD_CAN			
TIME:		SHEET 23		of 65		



OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	FPGA		VER	REV
DATE	22/02/2018:15:25	B	BANK65_1V5_SELECTMAP			
TIME:		SHEET 24 of 65				

ALPIDE_DATA_GPIO_dp[0:27],ALPIDE_DATA_GPIO_dn[0:27]

+1V8

ALPIDE_DATA_GPIO_dn1	C13	ALPIDE_DATA_GPIO_dn1	1	VCC0_66	B13
ALPIDE_DATA_GPIO_dn2	E13	ALPIDE_DATA_GPIO_dn2	2	VCC0_66	C10
ALPIDE_DATA_GPIO_dn3	B11	ALPIDE_DATA_GPIO_dn3	3	VCC0_66	F11
ALPIDE_DATA_GPIO_dn4	B12	ALPIDE_DATA_GPIO_dn4	4	VCC0_66	G8
ALPIDE_DATA_GPIO_dn5	G12	ALPIDE_DATA_GPIO_dn5	5	VCC0_66	J12
ALPIDE_DATA_GPIO_dn6	D11	ALPIDE_DATA_GPIO_dn6	6	VCC0_66	K9
ALPIDE_DATA_GPIO_dn7	D10	ALPIDE_DATA_GPIO_dn7	7		
ALPIDE_DATA_GPIO_dn8	C8	ALPIDE_DATA_GPIO_dn8	8		
ALPIDE_DATA_GPIO_dn9	C9	ALPIDE_DATA_GPIO_dn9	9		
ALPIDE_DATA_GPIO_dn10	F10	ALPIDE_DATA_GPIO_dn10	10		
ALPIDE_DATA_GPIO_dn11	E8	ALPIDE_DATA_GPIO_dn11	11		
ALPIDE_DATA_GPIO_dn12	H9	ALPIDE_DATA_GPIO_dn12	12		
ALPIDE_DATA_GPIO_dn13	F9	ALPIDE_DATA_GPIO_dn13	13		
ALPIDE_DATA_GPIO_dn14	H8	ALPIDE_DATA_GPIO_dn14	14		
ALPIDE_DATA_GPIO_dn15	J10	ALPIDE_DATA_GPIO_dn15	15		
ALPIDE_DATA_GPIO_dn16	H13	ALPIDE_DATA_GPIO_dn16	16		
ALPIDE_DATA_GPIO_dn17	J11	ALPIDE_DATA_GPIO_dn17	17		
ALPIDE_DATA_GPIO_dn18	K8	ALPIDE_DATA_GPIO_dn18	18		
ALPIDE_DATA_GPIO_dn19	K12	ALPIDE_DATA_GPIO_dn19	19		
ALPIDE_DATA_GPIO_dn20	K13	ALPIDE_DATA_GPIO_dn20	20		
ALPIDE_DATA_GPIO_dn21	A9	ALPIDE_DATA_GPIO_dn21	21		
ALPIDE_DATA_GPIO_dn22	A10	ALPIDE_DATA_GPIO_dn22	22		
ALPIDE_DATA_GPIO_dn23	A12	ALPIDE_DATA_GPIO_dn23	23		
ALPIDE_DATA_GPIO_dn24	G11	ALPIDE_DATA_GPIO_dn24	24		
ALPIDE_DATA_GPIO_dp1	D13	ALPIDE_DATA_GPIO_dp1	1		
ALPIDE_DATA_GPIO_dp2	F13	ALPIDE_DATA_GPIO_dp2	2		
ALPIDE_DATA_GPIO_dp3	C11	ALPIDE_DATA_GPIO_dp3	3		
ALPIDE_DATA_GPIO_dp4	C12	ALPIDE_DATA_GPIO_dp4	4		
ALPIDE_DATA_GPIO_dp5	H12	ALPIDE_DATA_GPIO_dp5	5		
ALPIDE_DATA_GPIO_dp6	E11	ALPIDE_DATA_GPIO_dp6	6		
ALPIDE_DATA_GPIO_dp7	E10	ALPIDE_DATA_GPIO_dp7	7		
ALPIDE_DATA_GPIO_dp8	D8	ALPIDE_DATA_GPIO_dp8	8		
ALPIDE_DATA_GPIO_dp9	D9	ALPIDE_DATA_GPIO_dp9	9		
ALPIDE_DATA_GPIO_dp10	G10	ALPIDE_DATA_GPIO_dp10	10		
ALPIDE_DATA_GPIO_dp11	F8	ALPIDE_DATA_GPIO_dp11	11		
ALPIDE_DATA_GPIO_dp12	J9	ALPIDE_DATA_GPIO_dp12	12		
ALPIDE_DATA_GPIO_dp13	G9	ALPIDE_DATA_GPIO_dp13	13		
ALPIDE_DATA_GPIO_dp14	J8	ALPIDE_DATA_GPIO_dp14	14		
ALPIDE_DATA_GPIO_dp15	K10	ALPIDE_DATA_GPIO_dp15	15		
ALPIDE_DATA_GPIO_dp16	J13	ALPIDE_DATA_GPIO_dp16	16		
ALPIDE_DATA_GPIO_dp17	K11	ALPIDE_DATA_GPIO_dp17	17		
ALPIDE_DATA_GPIO_dp18	L8	ALPIDE_DATA_GPIO_dp18	18		
ALPIDE_DATA_GPIO_dp19	L12	ALPIDE_DATA_GPIO_dp19	19		
ALPIDE_DATA_GPIO_dp20	L13	ALPIDE_DATA_GPIO_dp20	20		
ALPIDE_DATA_GPIO_dp21	B9	ALPIDE_DATA_GPIO_dp21	21		
ALPIDE_DATA_GPIO_dp22	B10	ALPIDE_DATA_GPIO_dp22	22		
ALPIDE_DATA_GPIO_dp23	A13	ALPIDE_DATA_GPIO_dp23	23		
ALPIDE_DATA_GPIO_dp24	H11	ALPIDE_DATA_GPIO_dp24	24		

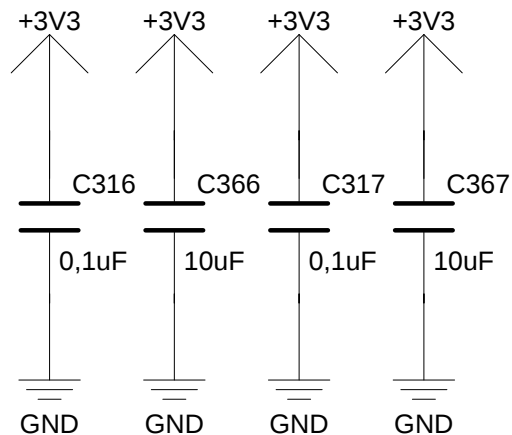
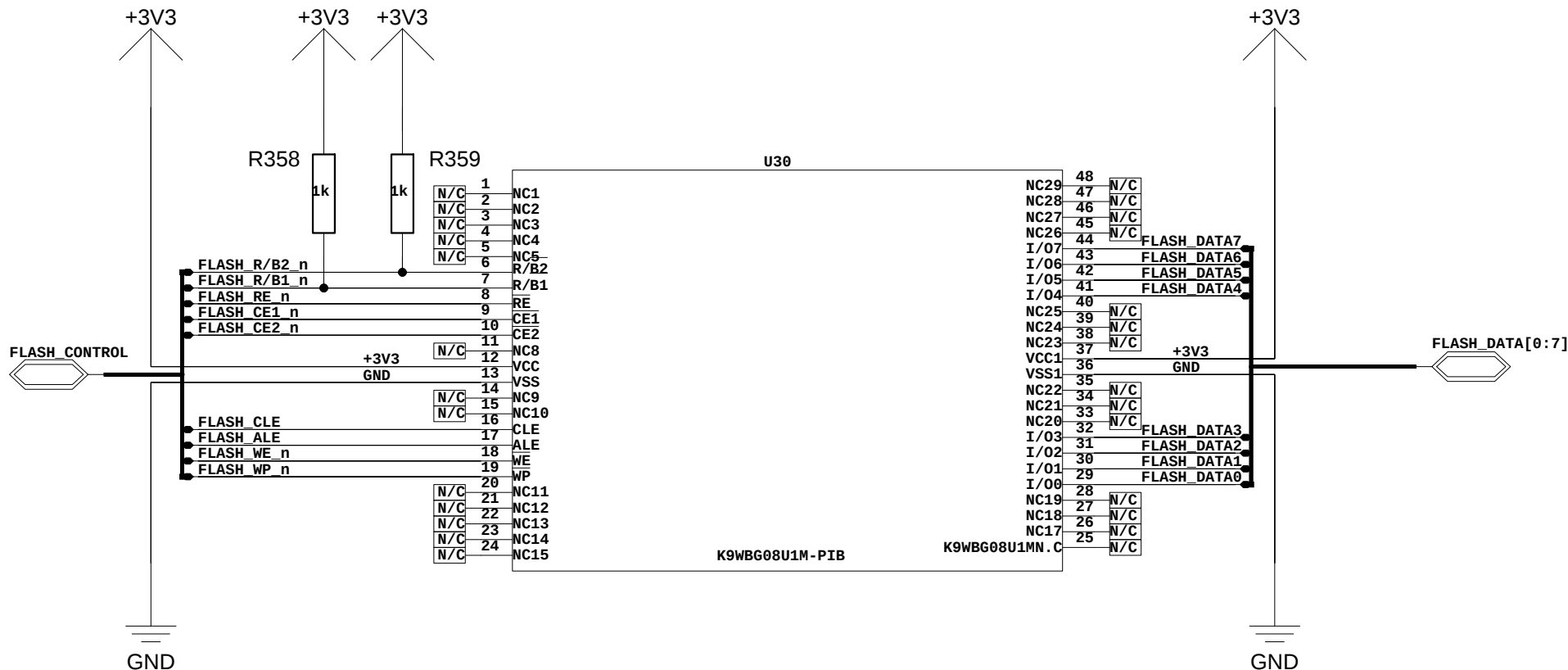
IO_T0U_N12_VRP_66
VREF_66
IO_T1U_N12_66
IO_T2U_N12_66
IO_T3U_N12_66

M2KU-xxx-xFFxA1156

OK no.			Scientific Instrumentation				
APPROVALS			RUv2				
DRAWN BY	Rossewij, Wisman	IGF					
CHECKED		SIZE			FPGA	VER	REV
DATE	22/02/2018:15:25	B	BANK66_1V8_ALPIDE_HSDATA				
TIME:		SHEET 25 of 65					



OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	FPGA	VER	REV	
DATE	22/02/2018:15:25	B	BANK68_1V8_ALPIDE_HSDATA_PA3			
TIME:		SHEET 26 of 65				



OK no.				Scientific Instrumentation	
APPROVALS				RUv2	
DRAWN BY Rossewij, Wisman		IGF			
CHECKED				VER	
DATE 22/02/2018:15:25				REV	
TIME:				SHEET 29 of 65	

4

3

2

1

D

D

C

C

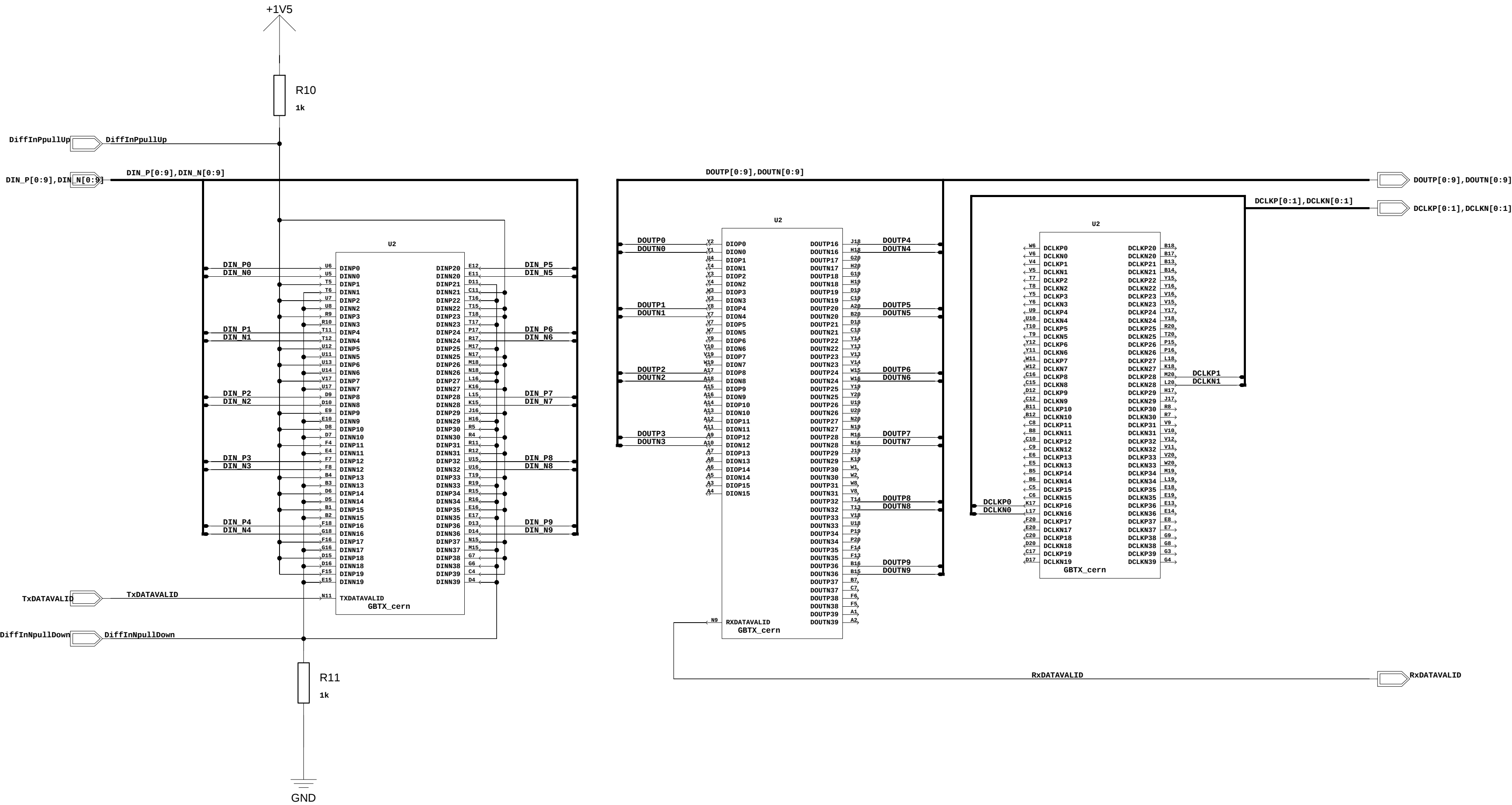
B

B

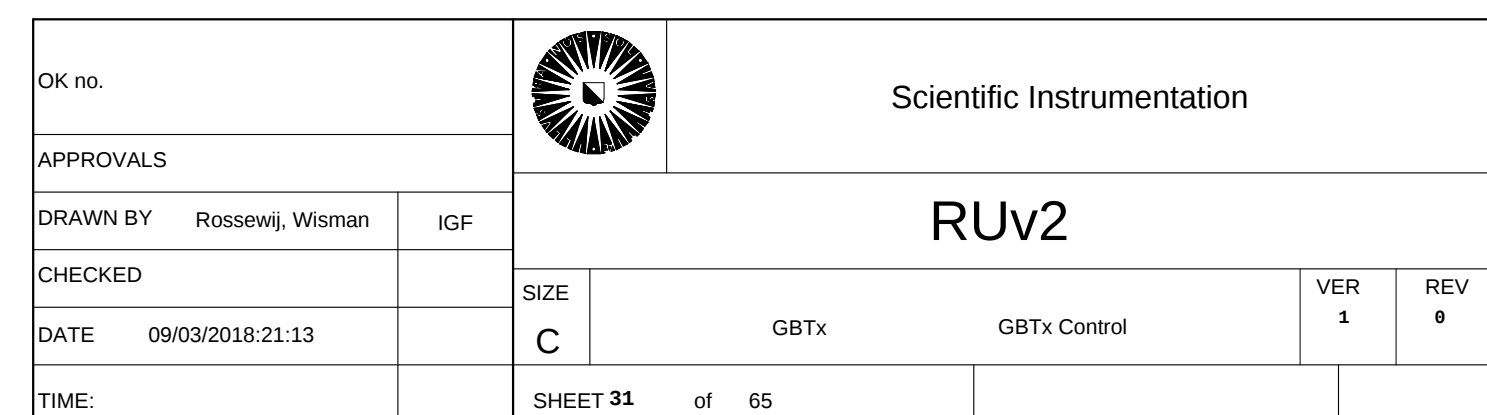
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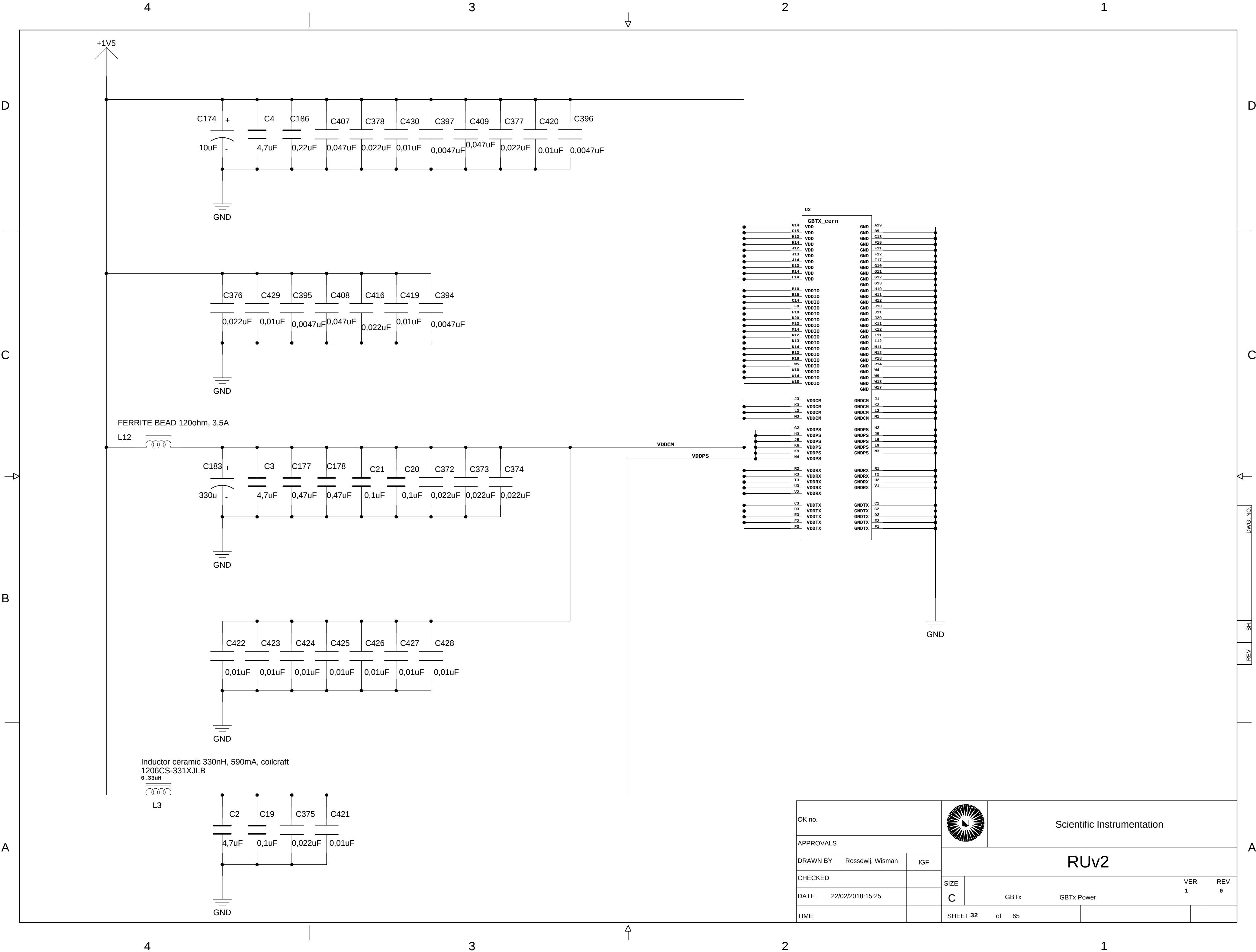
A

H=4ND; V=4LA



OK no.			Scientific Instrumentation			
APPROVALS			RUV2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE			VER 1	REV 0
DATE	09/03/2018:21:12	C	GBTx GBTx IO			
TIME:		SHEET 30 of 65				





OK no.			Scientific Instrumentation			
APPROVALS			RUV2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED						
DATE	22/02/2018:15:25	SIZE	GBTx		VER	REV
		C	GBTx Power		1	0
TIME:		SHEET 32 of 65				

D

C

B

A

D

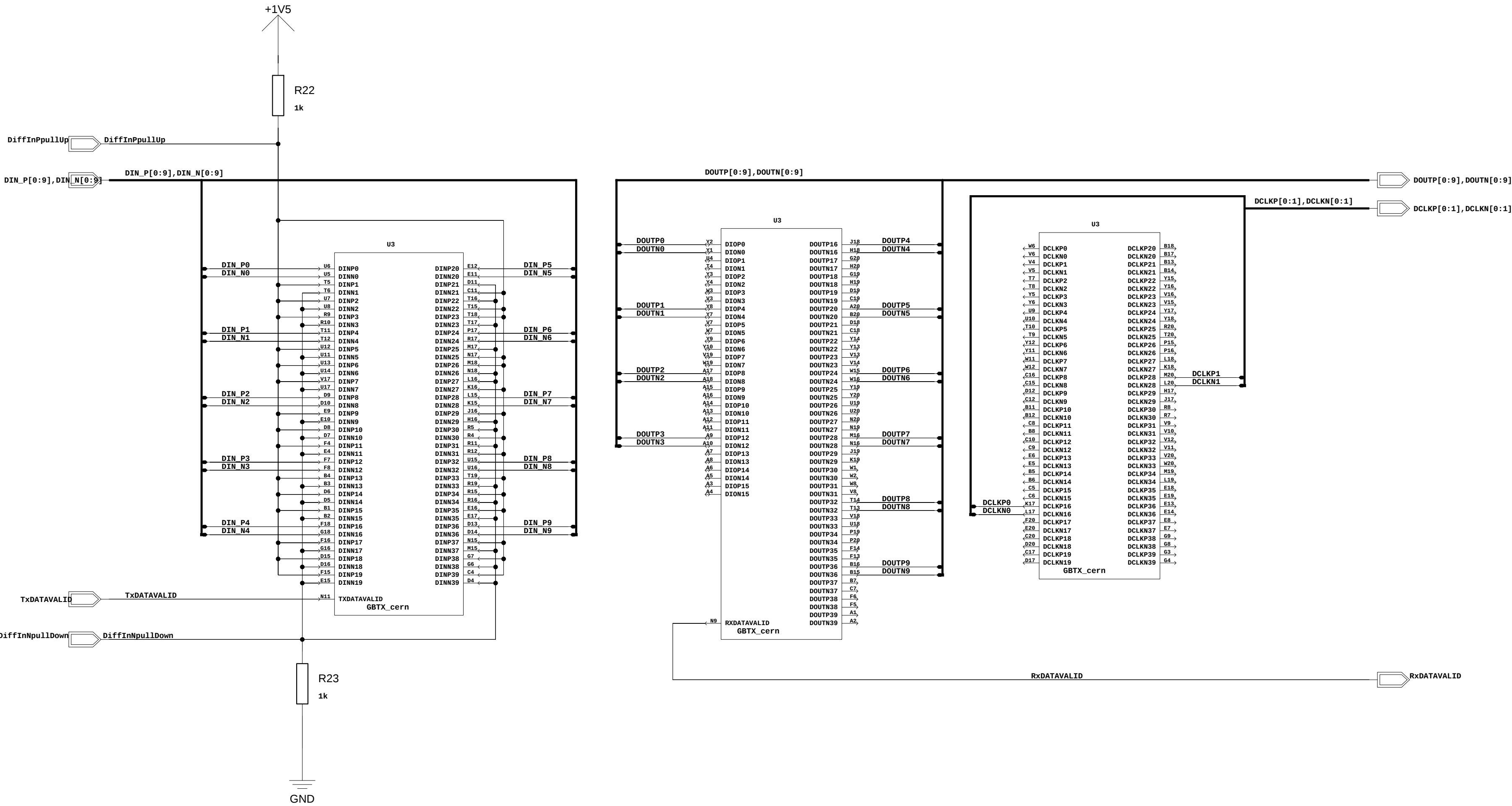
C

SH

REV

A

H=4ND; V=4LA



OK no.			Scientific Instrumentation				
APPROVALS			RUV2				
DRAWN BY	Rossewij, Wisman						IGF
CHECKED			SIZE			VER	REV
DATE	09/03/2018:21:12		C	GBTx1 GBTx IO		1	0
TIME:		SHEET 33 of 65					

4

3

2

1

D

D

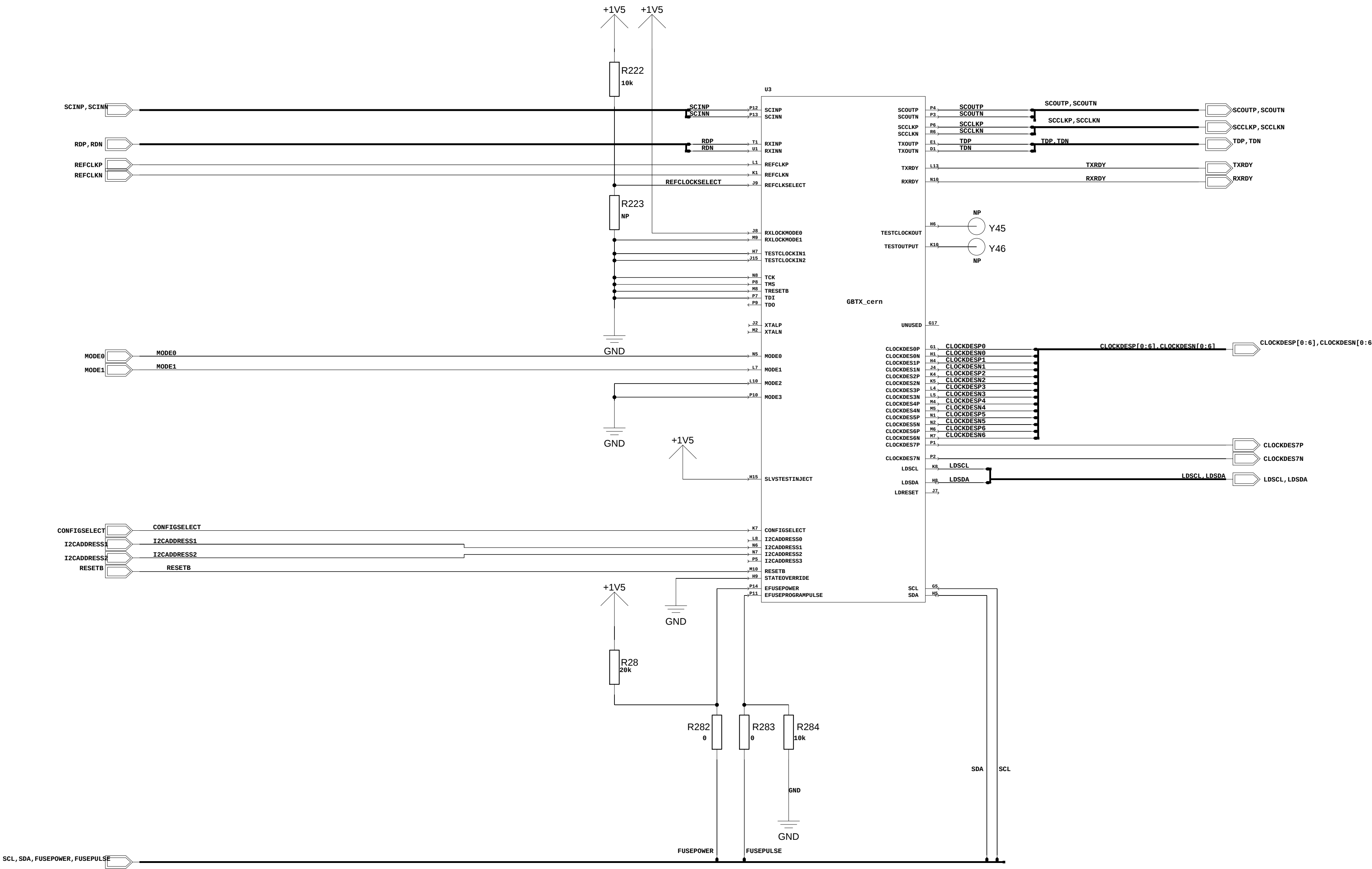
C

C

B

A

A



OK no.			Scientific Instrumentation				
APPROVALS			RUv2				
DRAWN BY	Rossewij, Wisman						IGF
CHECKED			SIZE	GBTx1		GBTx Control	VER 1
DATE	09/03/2018:21:13	C					
TIME:		SHEET 34 of 65					



D

C

B

A

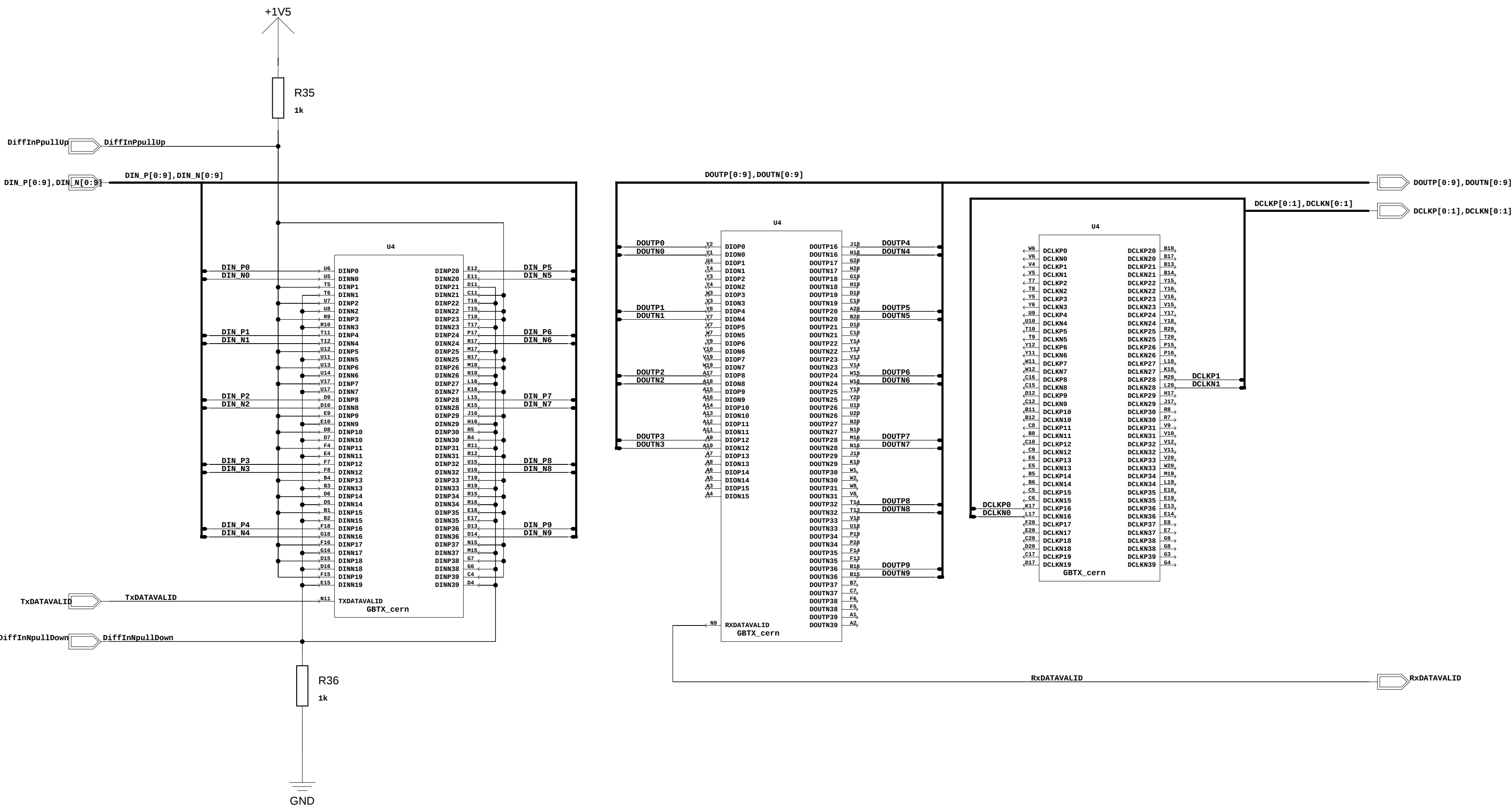
D

C

SH

REV

A



OK no.			Scientific Instrumentation					
APPROVALS			RUV2					
DRAWN BY	Rossewij, Wisman						IGF	
CHECKED								
DATE	09/03/2018:21:12		SIZE	GBTx2		GBTx IO	VER 1	REV 0
TIME:			C	SHEET 36 of 65				

4

3

2

1

D

D

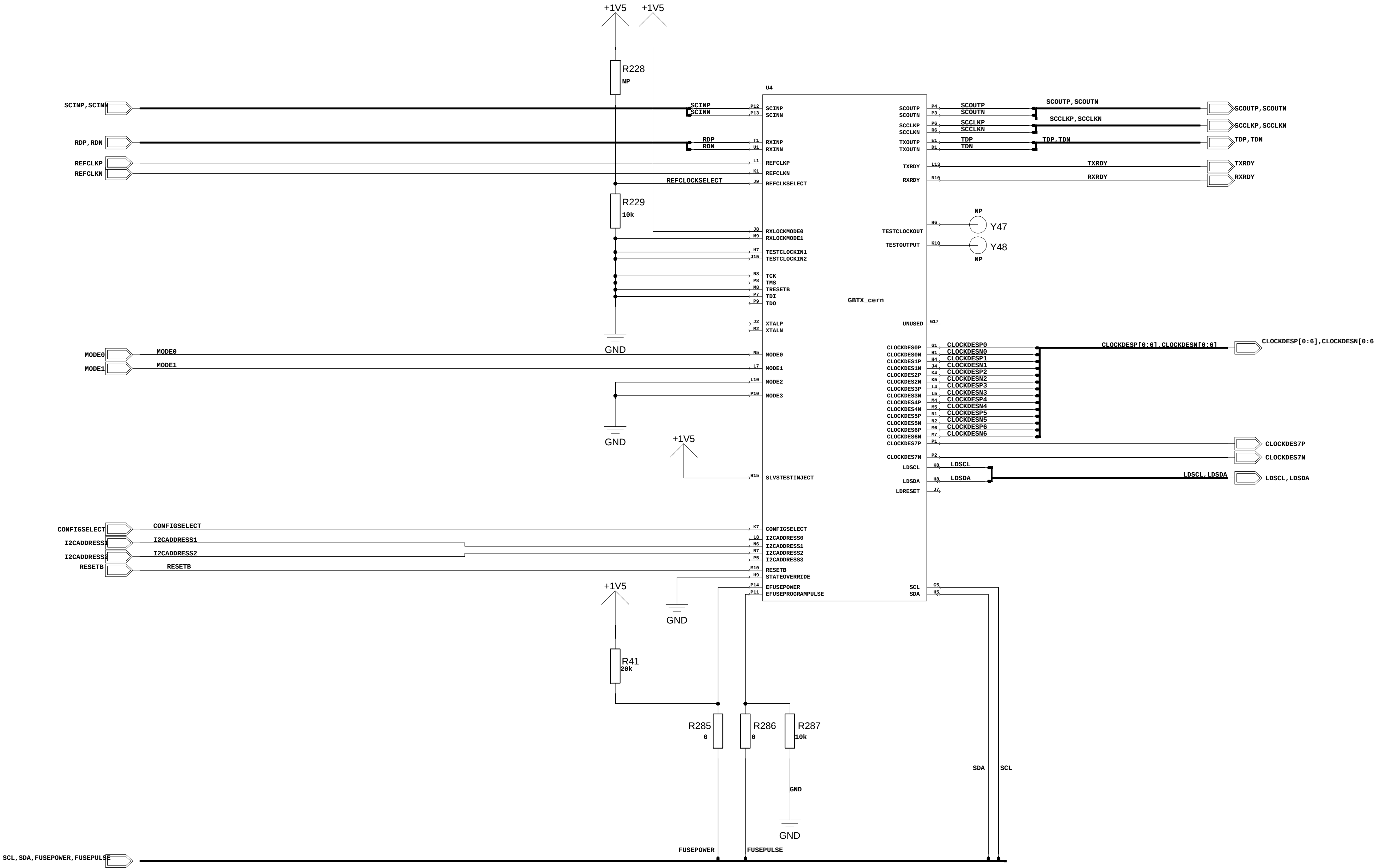
C

C

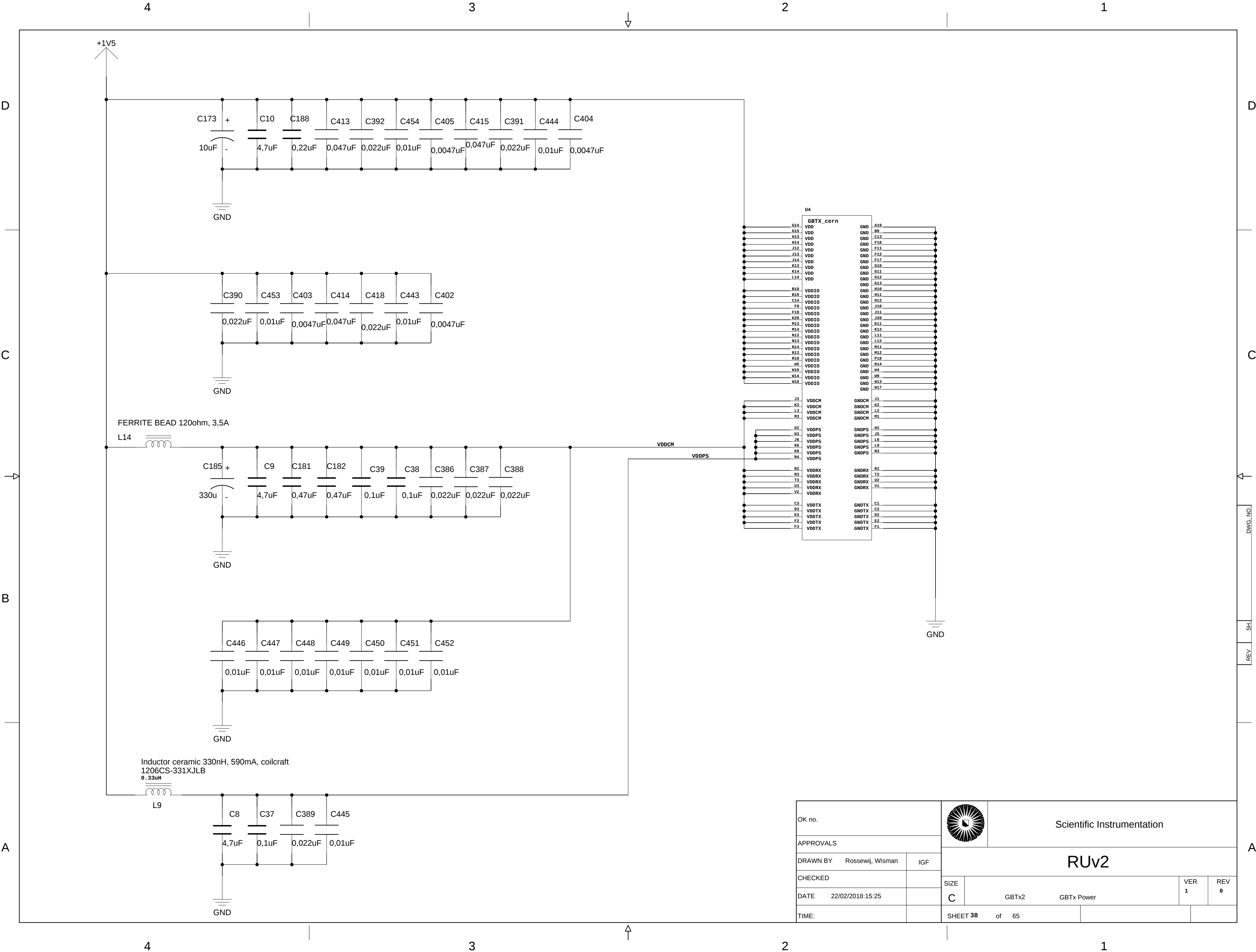
B

A

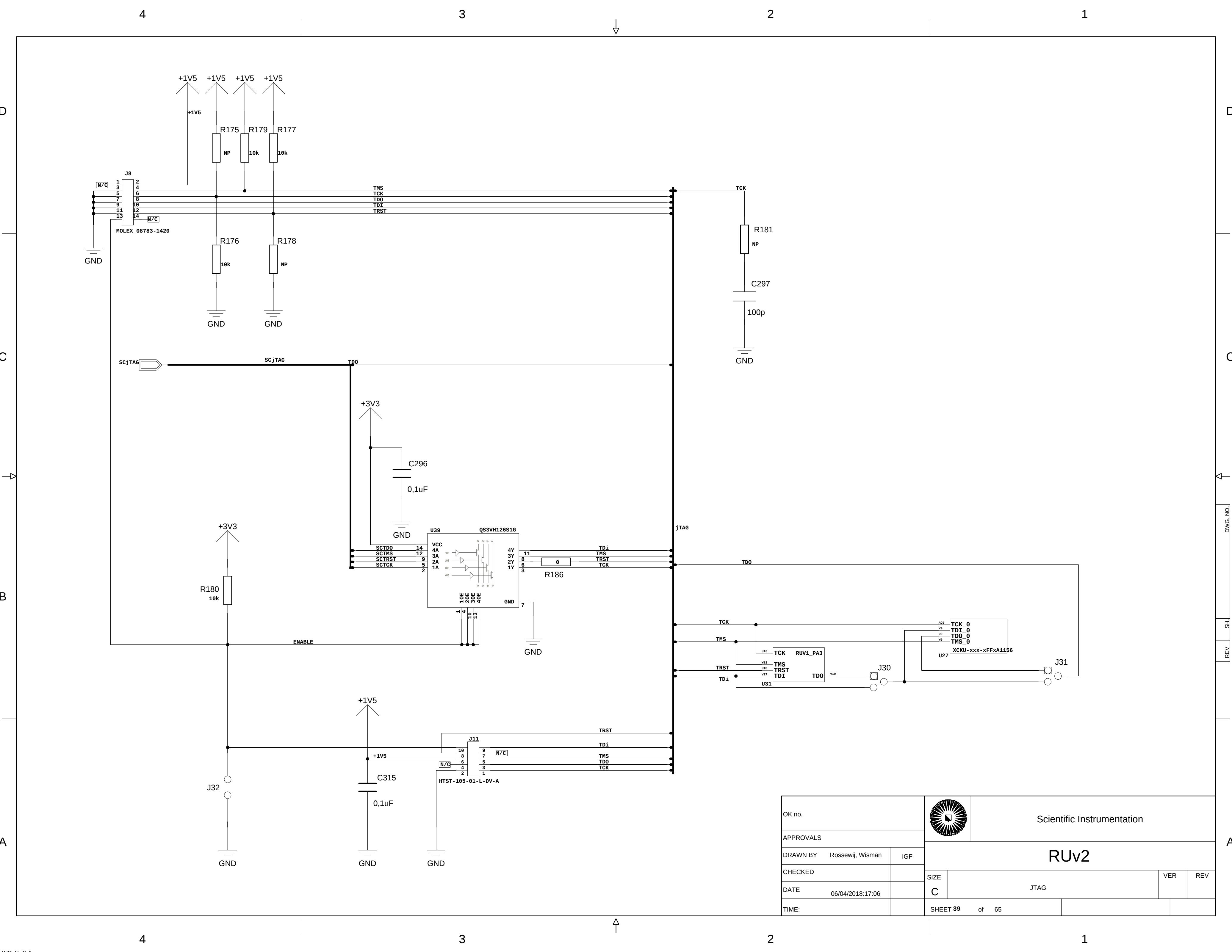
A



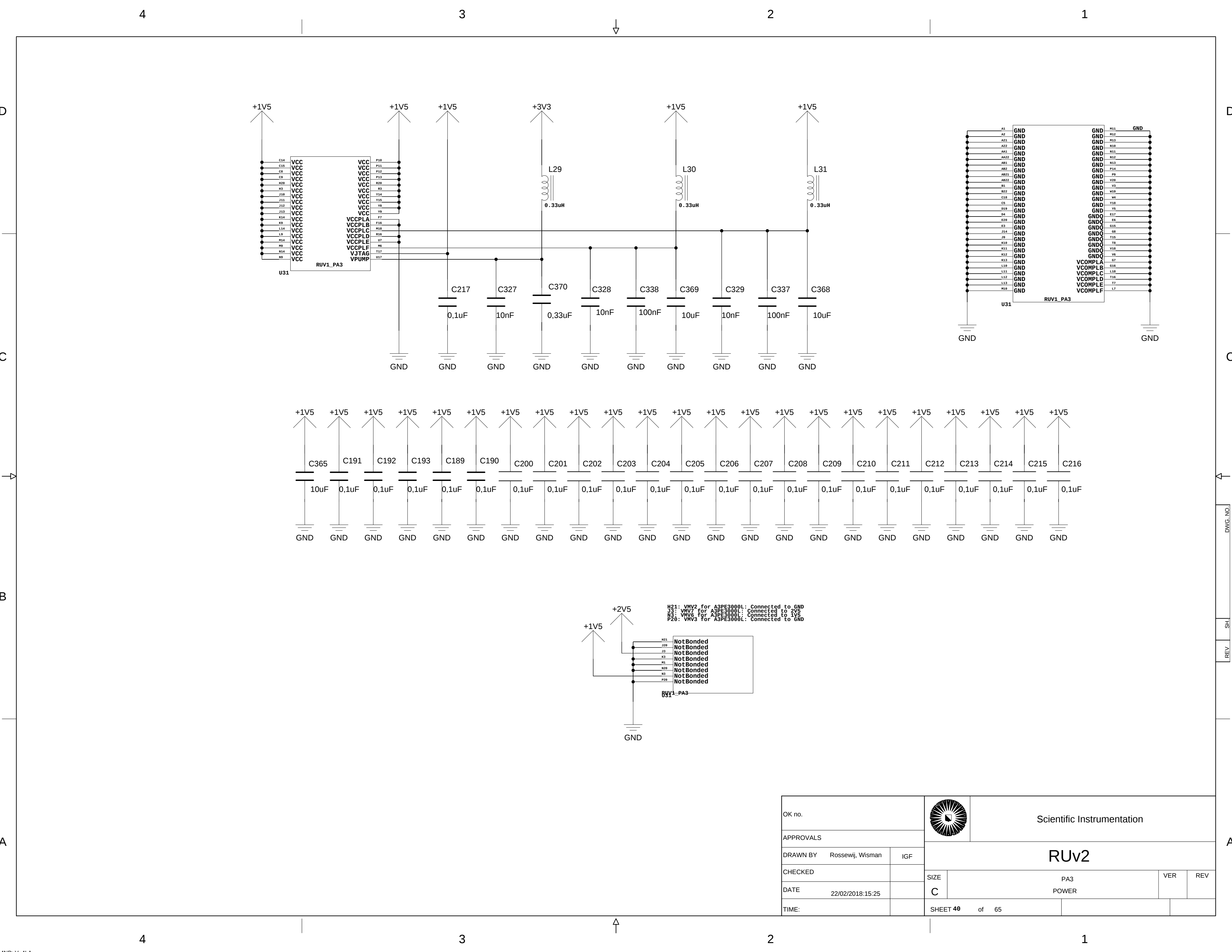
OK no.			 Scientific Instrumentation			
APPROVALS			RUV2			
DRAWN BY Rossewij, Wisman		IGF	SIZE C		GBTx2	GBTx Control
CHECKED			VER 1		REV 0	
DATE 09/03/2018:21:13			SHEET 37 of 65			
TIME:						



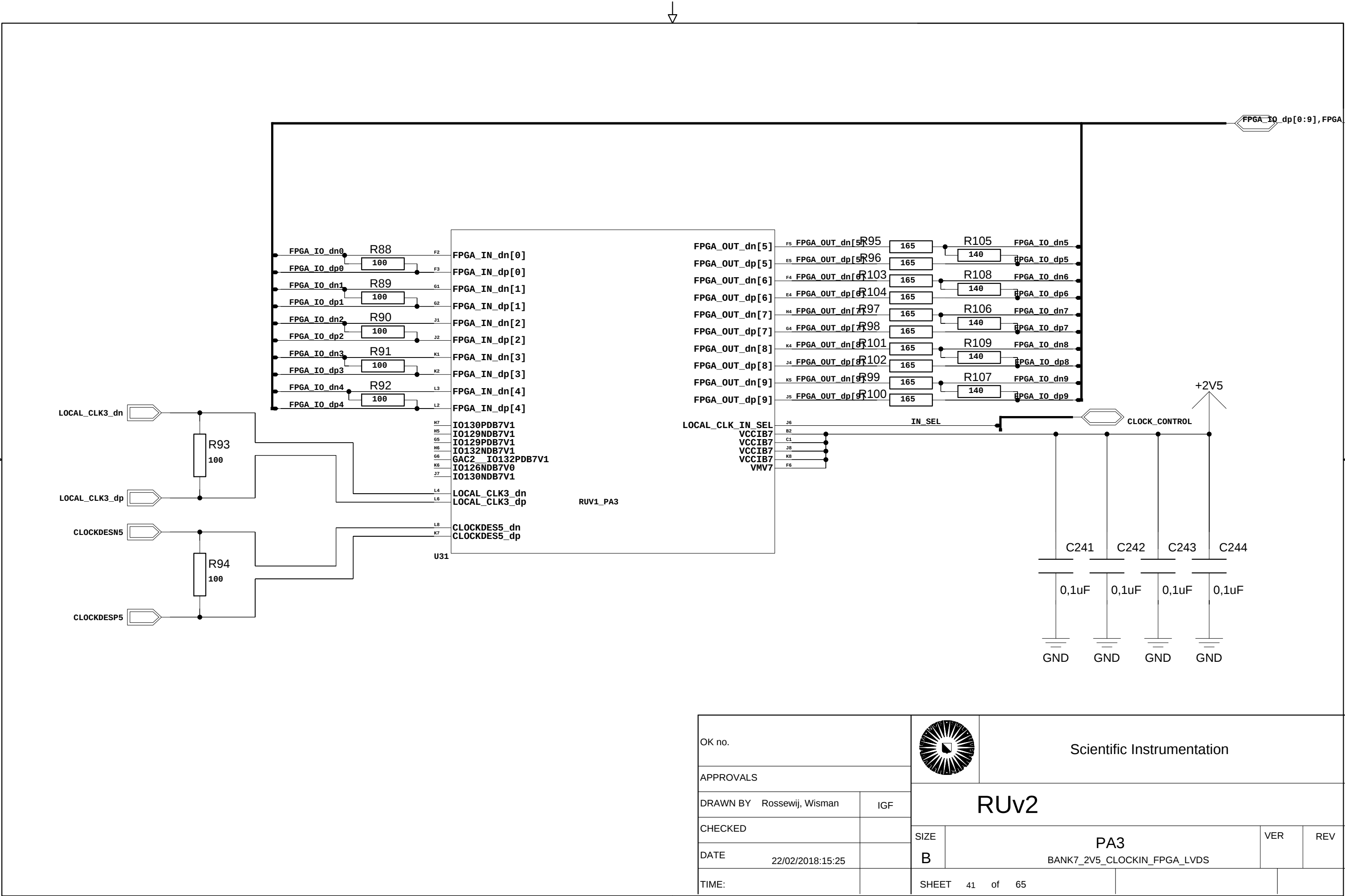
OK no.			Scientific Instrumentation			
APPROVALS			RUV2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE			VER	REV
DATE	22/02/2018:15:25	C	GBTx2 GBTx Power		1	0
TIME:		SHEET 38 of 65				



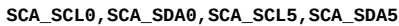
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	JTAG		VER	REV
DATE	06/04/2018:17:06	C				
TIME:		SHEET 39 of 65				



OK no.			 Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY Rossewij, Wisman		IGF	SIZE C	PA3 POWER	VER	REV
CHECKED						
DATE 22/02/2018:15:25						
TIME:			SHEET 40 of 65			



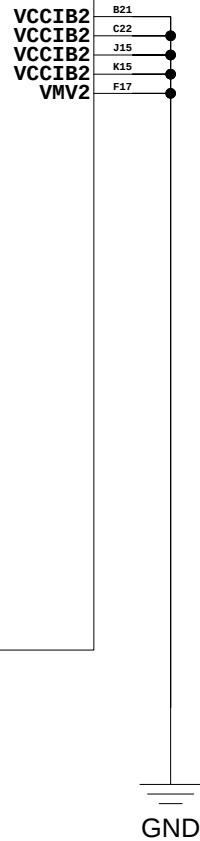
OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY Rossewrij, Wisman	IGF					
CHECKED						
DATE	22/02/2018:15:25	SIZE	PA3		VER	REV
		B	BANK7_2V5_CLOCKIN_FPGA_LVDS			
TIME:		SHEET 41 of 65				



OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	PA3		VER	REV
DATE	22/02/2018:15:25	B	BANK6_1V5_SCA			
TIME:		SHEET 42 of 65				

A19	Not Bonded
AA14	Not Bonded
AA15	Not Bonded
AA18	Not Bonded
AA19	Not Bonded
AA20	Not Bonded
AA3	Not Bonded
AB10	Not Bonded
AB11	Not Bonded
AB18	Not Bonded
AB19	Not Bonded
B14	Not Bonded
B15	Not Bonded
B19	Not Bonded
B20	Not Bonded
B3	Not Bonded
C12	Not Bonded
C13	Not Bonded
C16	Not Bonded
C17	Not Bonded
C19	Not Bonded
C2	Not Bonded
C20	Not Bonded
C21	Not Bonded
C3	Not Bonded
C4	Not Bonded
D1	Not Bonded
D2	Not Bonded
D20	Not Bonded
D21	Not Bonded
D22	Not Bonded
D3	Not Bonded
E1	Not Bonded
E2	Not Bonded
E21	Not Bonded
E22	Not Bonded
F1	Not Bonded
F20	Not Bonded
F21	Not Bonded
F22	Not Bonded
G22	Not Bonded
G3	Not Bonded
H1	Not Bonded
H2	Not Bonded
H22	Not Bonded
L1	Not Bonded
M22	Not Bonded
P1	Not Bonded
R1	Not Bonded
T1	Not Bonded
T20	Not Bonded
T3	Not Bonded
U1	Not Bonded
U20	Not Bonded
V1	Not Bonded
V2	Not Bonded
V21	Not Bonded
W1	Not Bonded
W2	Not Bonded
W20	Not Bonded
W21	Not Bonded
W22	Not Bonded
W3	Not Bonded
Y13	Not Bonded
Y16	Not Bonded
Y17	Not Bonded
Y19	Not Bonded
Y2	Not Bonded
Y20	Not Bonded
Y21	Not Bonded
Y3	Not Bonded
U31	RUV1_PA3

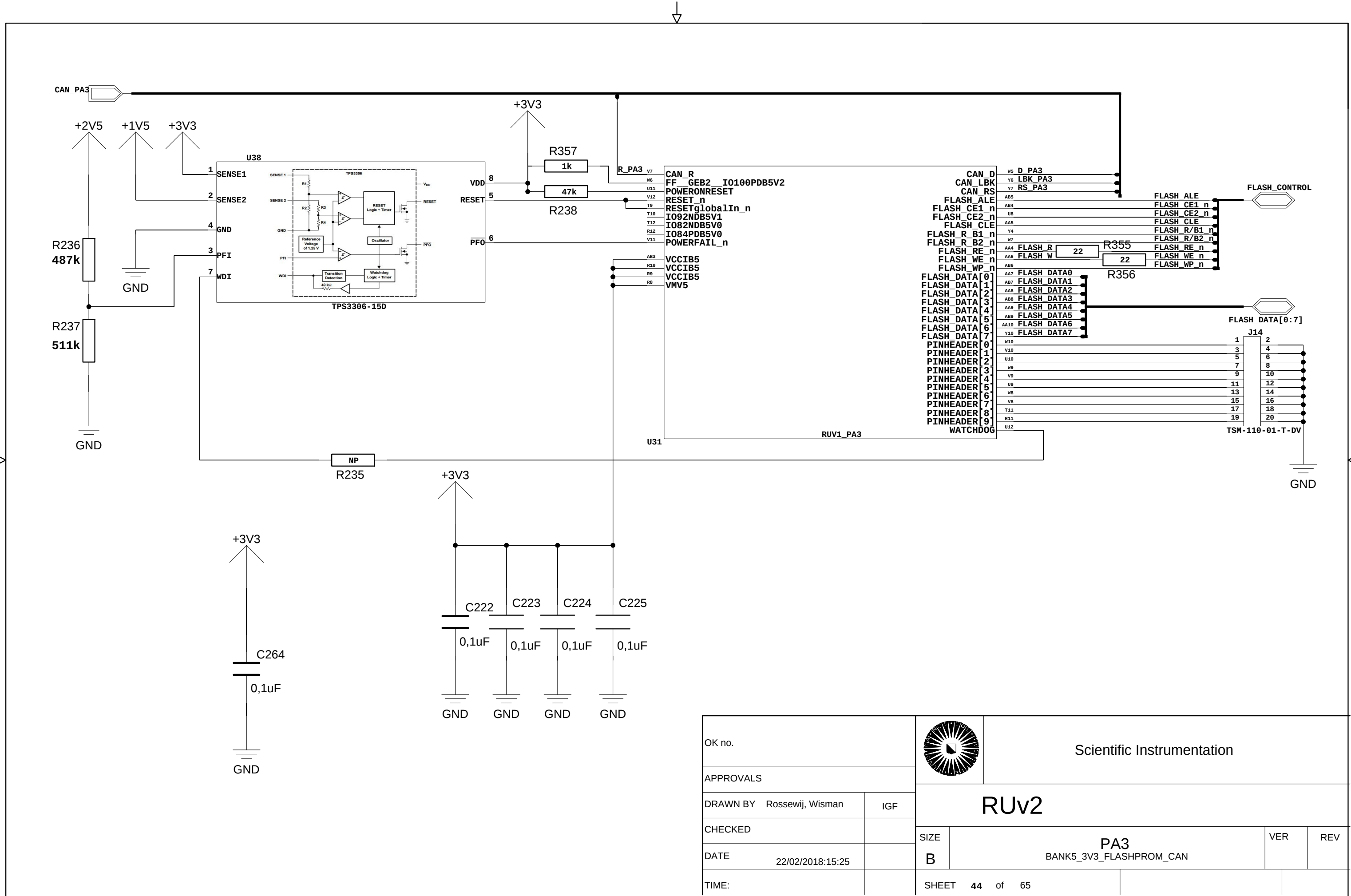
E18	GBA2	I036PDB2V0
G17	GBB2	I037PDB2V0
H16	GBC2	I038PDB2V0
L19	GCB0	I051NPB2V1
L16	GCB1	I051PPB2V1
L15	GCC0	I050NPB2V1
K16	GCC1	I050PPB2V1
F18		I036NDB2V0
H17		I037NDB2V0
J16		I038NDB2V0
G19		I039NDB2V0
G18		I039PDB2V0
J17		I040NDB2V0
J18		I040PDB2V0
H10		I041NDB2V0
H19		I041PDB2V0
E19		I042NDB2V0
F19		I042PDB2V0
G21		I043NDB2V0
G20		I043PDB2V0
K17		I044NDB2V1
K10		I044PDB2V1
K20		I045NPB2V1
J19		I045PPB2V1
K22		I046NDB2V1
J22		I046PDB2V1
L21		I047NDB2V1
L22		I047PDB2V1
K21		I048NDB2V1
J21		I048PDB2V1
K19		I049NPB2V1
L20		I049PPB2V1
BUY1_PA3		



M20	I053NDB3V0	VCCIB3	AA21
N16	I054NPB3V0	VCCIB3	N15
N18	I055NPB3V0	VCCIB3	P15
M21	I056NDB3V0	VCCIB3	Y22
M21	I056PDB3V0	VMV3	R15
N17	I057NPB3V0		L17
N19	I057PPB3V0	GCA0	M16
P22	I058NDB3V0	GCA1	M10
N22	I058PDB3V0	GCA2	M10
R21	I059NDB3V0	GCB2	M15
P21	I059PDB3V0	GCC2	M17
P17	I060NDB3V1	GDA0	U19
P18	I060PDB3V1	GDA1	T19
R19	I061NDB3V1	GDB0	P16
P19	I061PDB3V1	GDB1	R17
T22	I062NDB3V1	GDC0	T18
R22	I062PDB3V1	GDC1	R18
U22	I063PDB3V1		
U21	I064NDB3V1		
T21	I064PDB3V1		
BUY1_PA3			



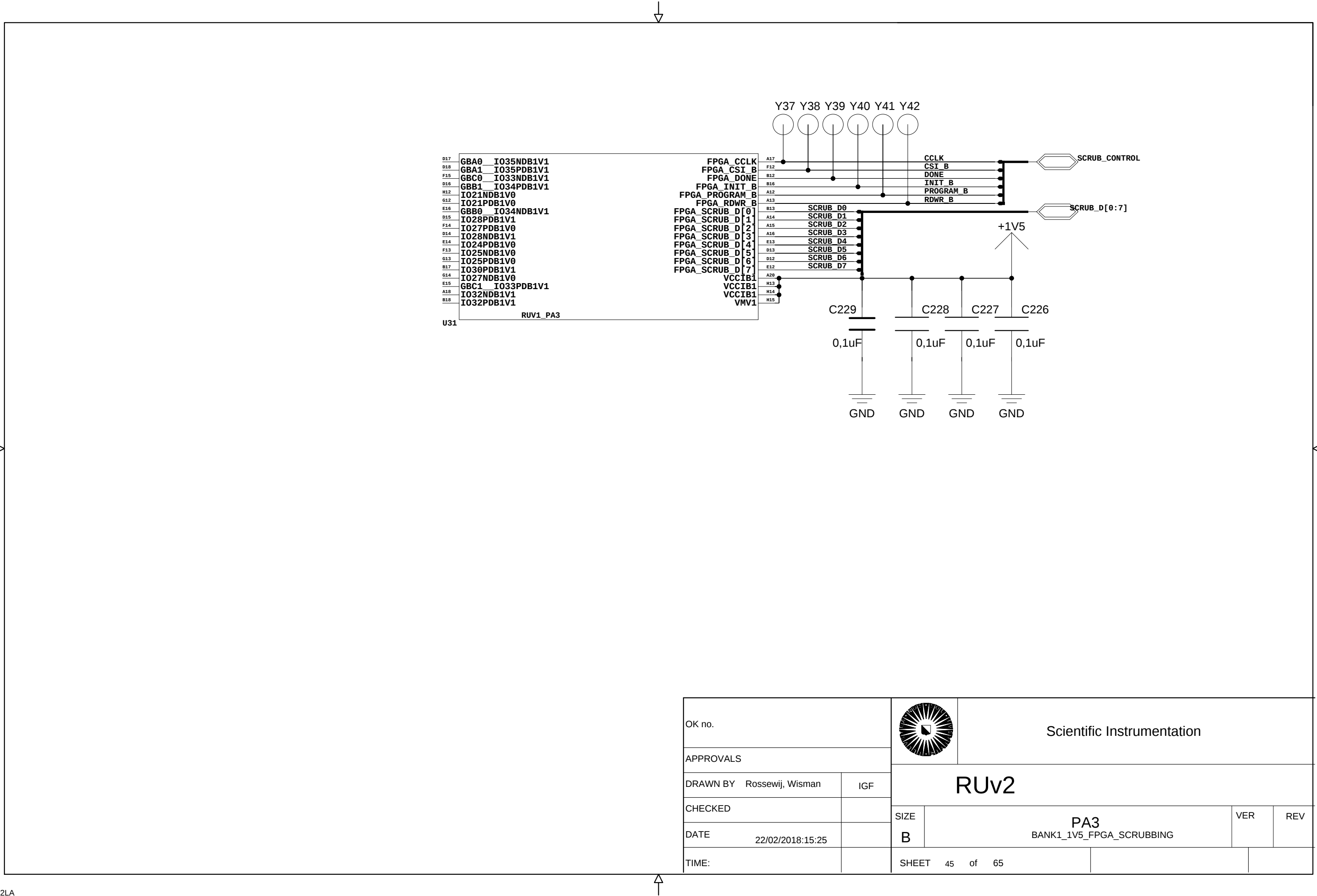
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DATE 22/02/2018:15:25				B	BANK_UNUSED			
TIME:				SHEET 43 of 65				



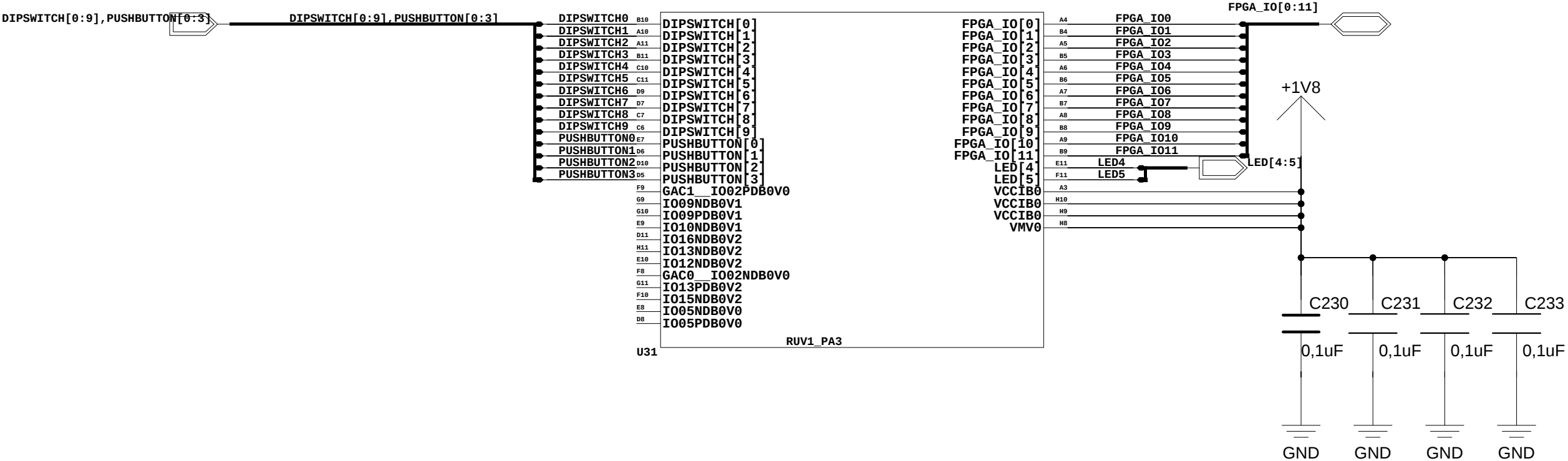
Scientific Instrumentation

RUv2

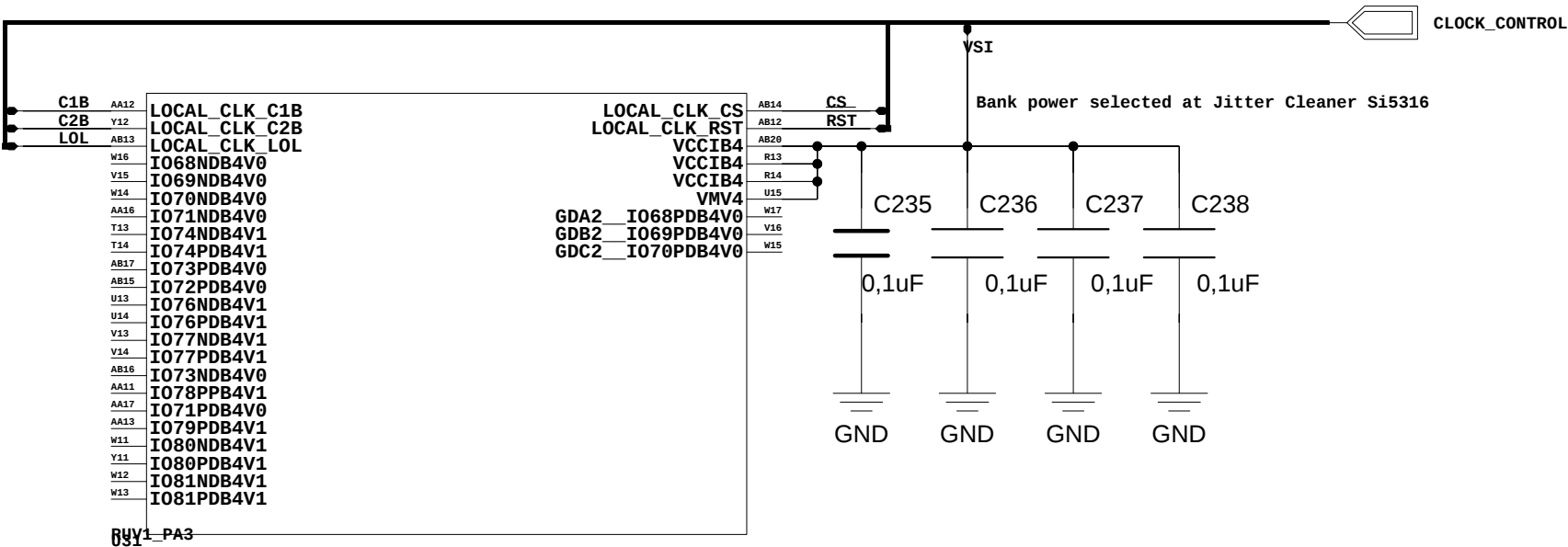
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DRAWN BY Rossewij, Wisman		IGF		B		BANK5_3V3_FLASHPROM_CAN			
CHECKED		DATE 22/02/2018:15:25							
TIME:				SHEET 44 of 65					



OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Rossewij, Wisman		IGF	RUv2				
CHECKED							
DATE			SIZE	PA3 BANK1_1V5_FPGA_SCRUBBING		VER	REV
22/02/2018:15:25			B				
TIME:			SHEET 45 of 65				



OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	PA3		VER	REV
DATE	22/02/2018:15:25	B	BANK0_1V8_DIPSWITCH_LED-FPGA_IO			
TIME:		SHEET 46 of 65				



OK no.			Scientific Instrumentation				
APPROVALS			RUv2				
DRAWN BY	Rossewij, Wisman	IGF					
CHECKED		SIZE		PA3		VER	REV
DATE	22/02/2018:15:25	B	BANK4_VSI_SI5316_CONFIG				
TIME:		SHEET 47 of 65					

D

C

B

A

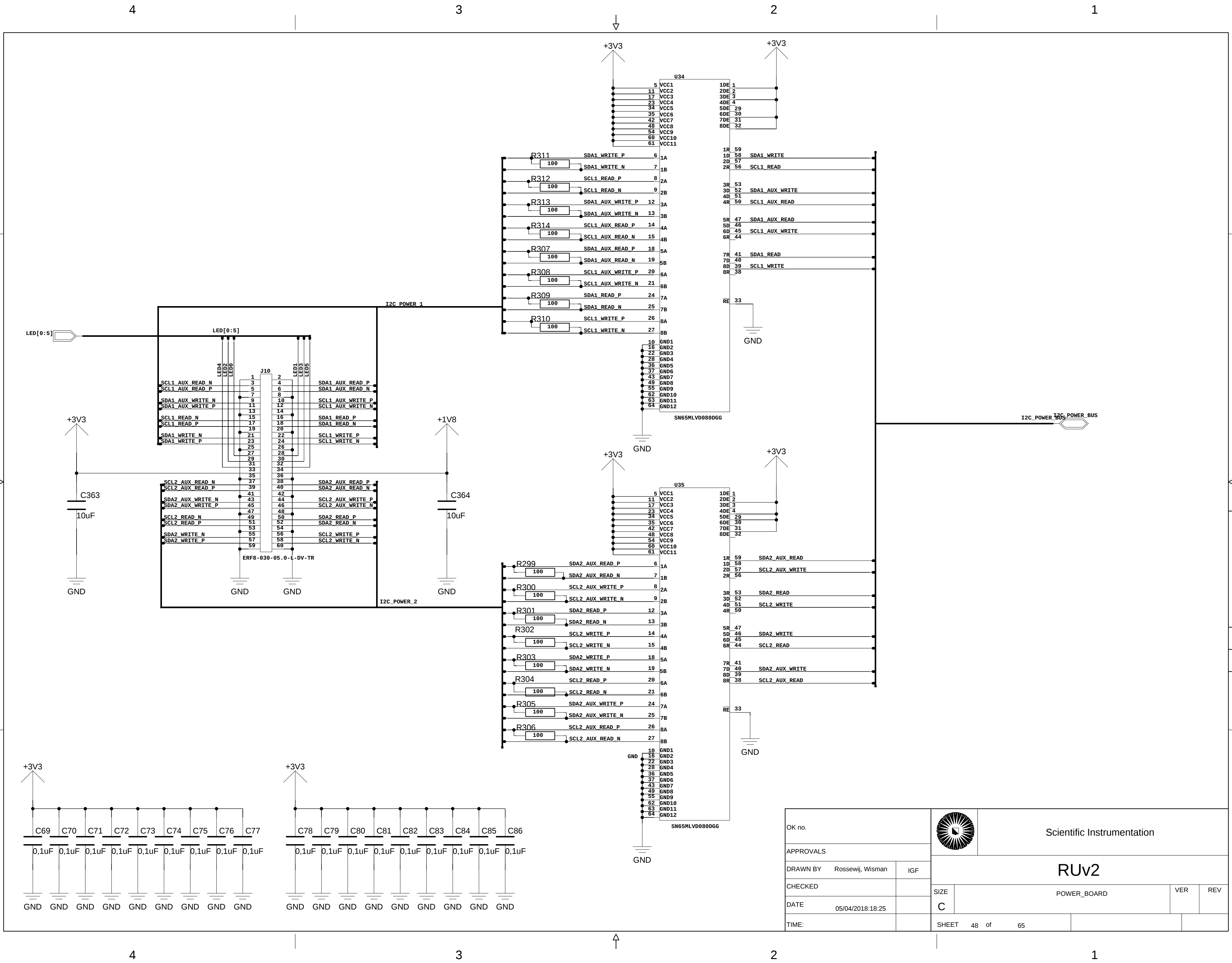
D

C

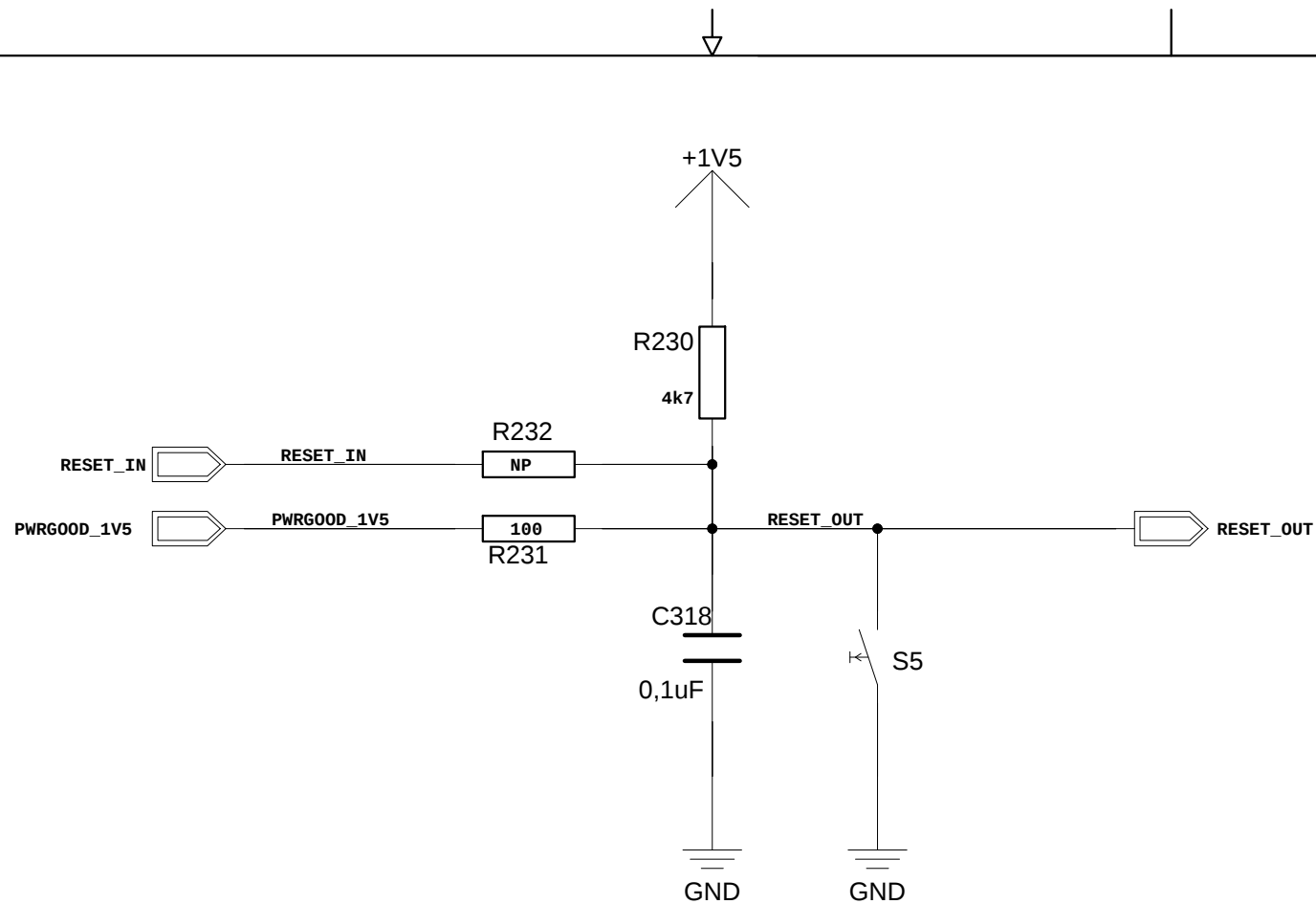
SH

REV

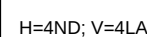
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CHECKED							
DATE	05/04/2018:18:25		SIZE	POWER_BOARD		VER	REV
TIME:			C				
			SHEET 48 of 65				



OK no.			Scientific Instrumentation			
APPROVALS			RUv2			
DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	RESET		VER	REV
DATE	22/02/2018:15:25	A				
TIME:		SHEET 49 of 65				



4

3

2

1

D

D

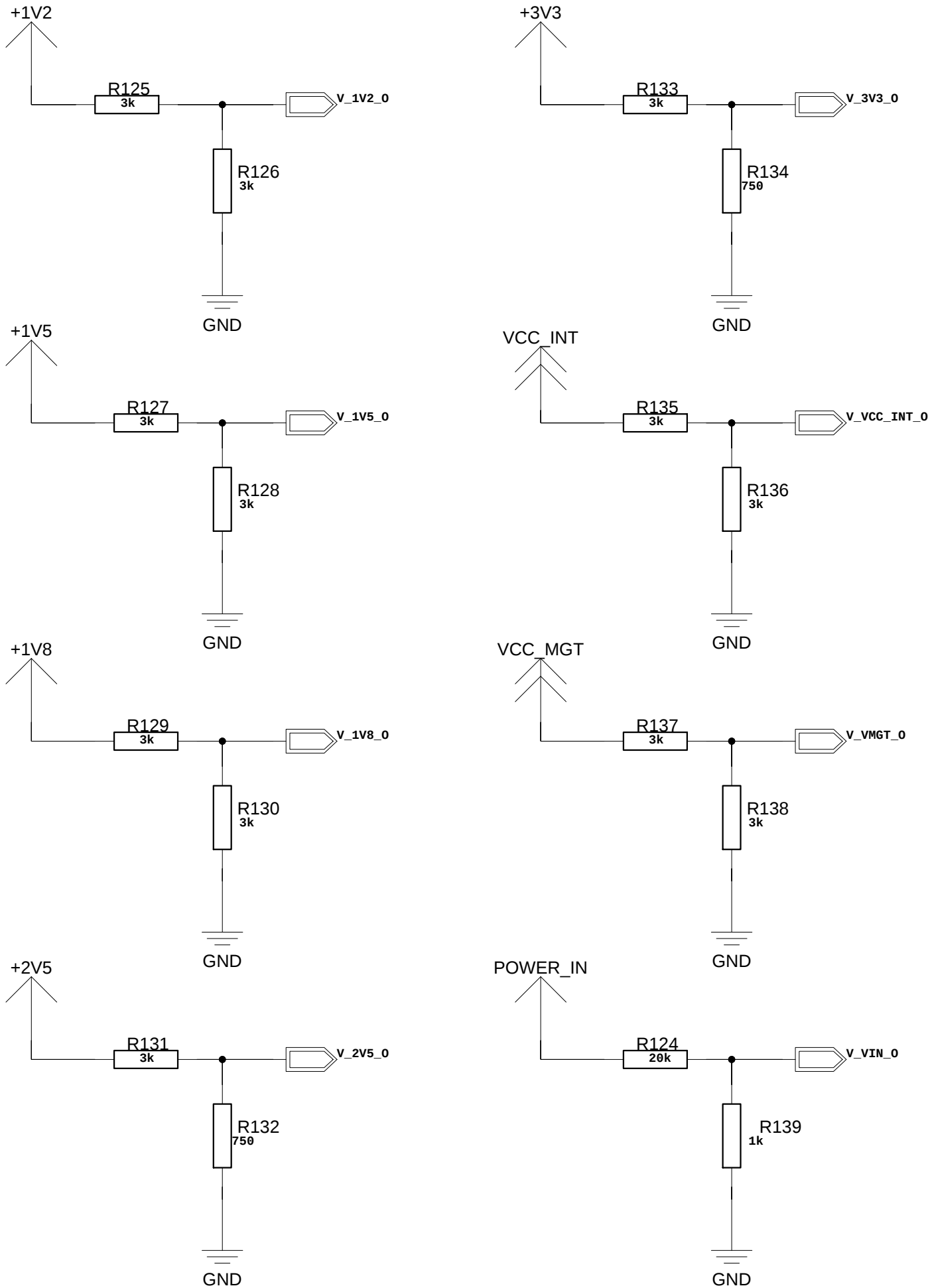
C

C

B

A

A



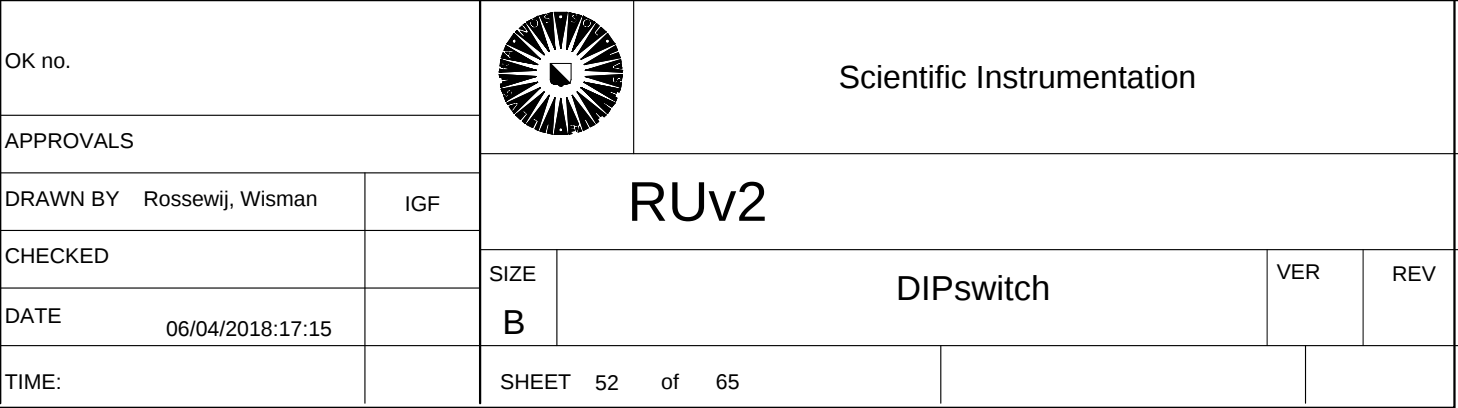
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DRAWN BY	Rossewij, Wisman	IGF				
CHECKED		SIZE	Voltage_Divider		VER	REV
DATE	22/02/2018:15:25	C				
TIME:		SHEET 51 of 65				

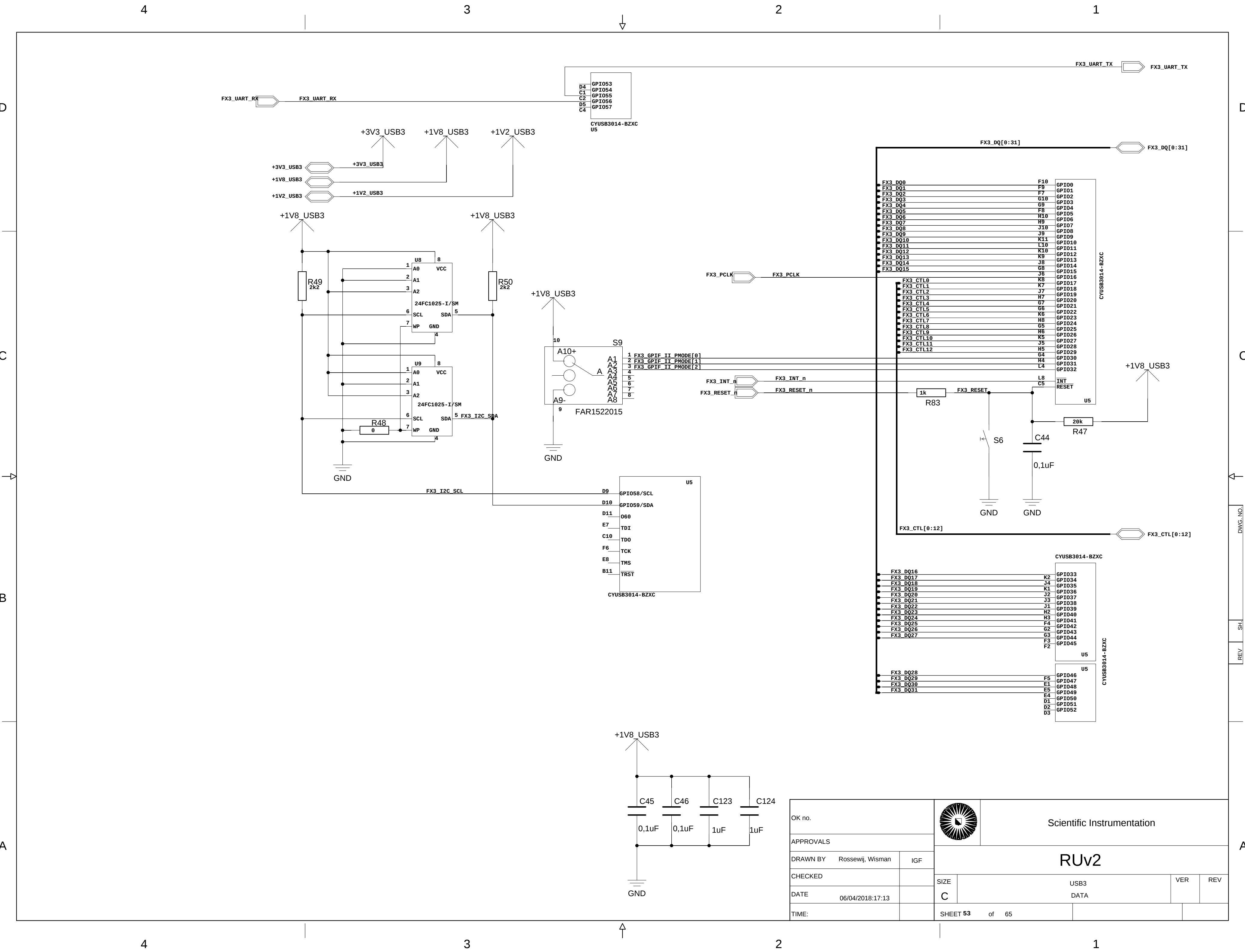
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3

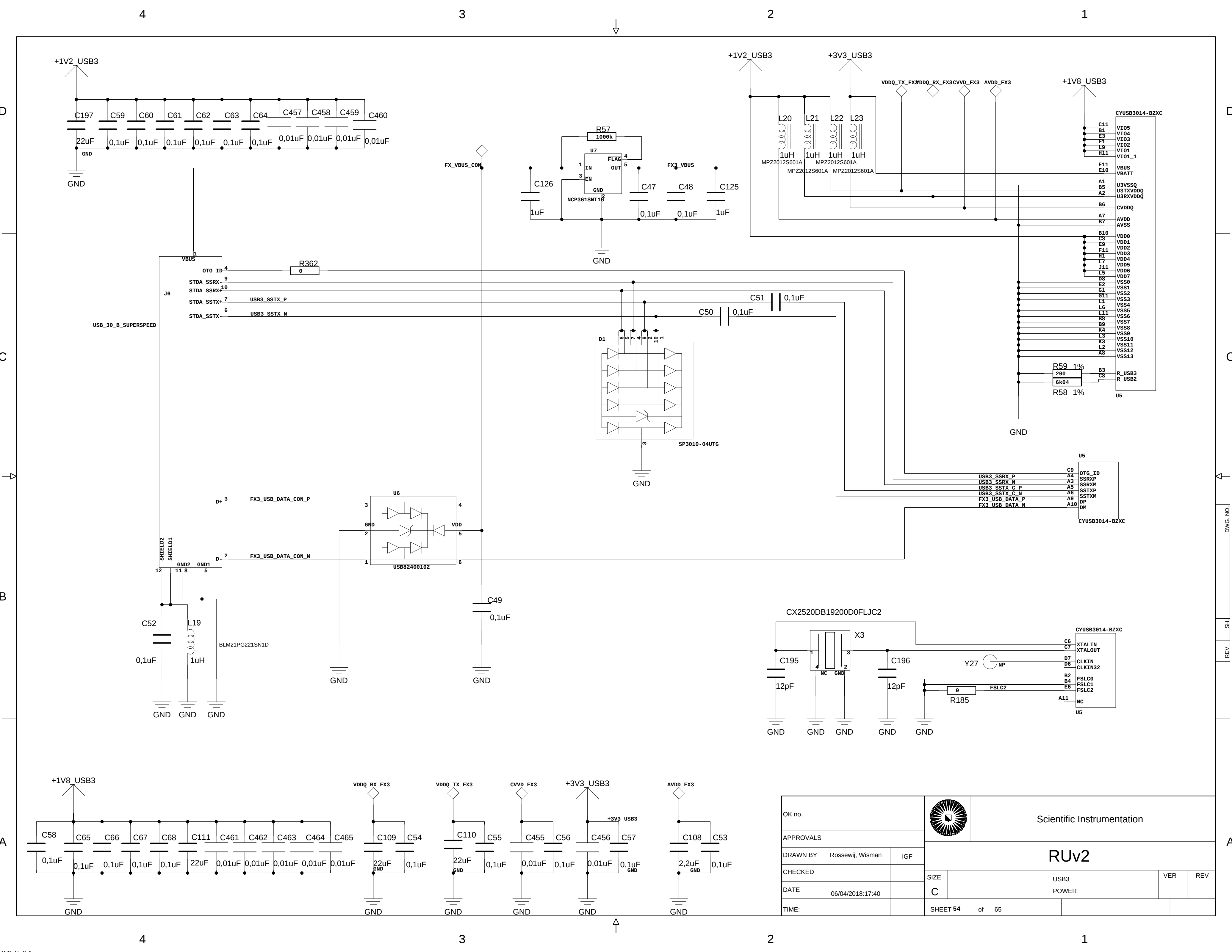
2

1

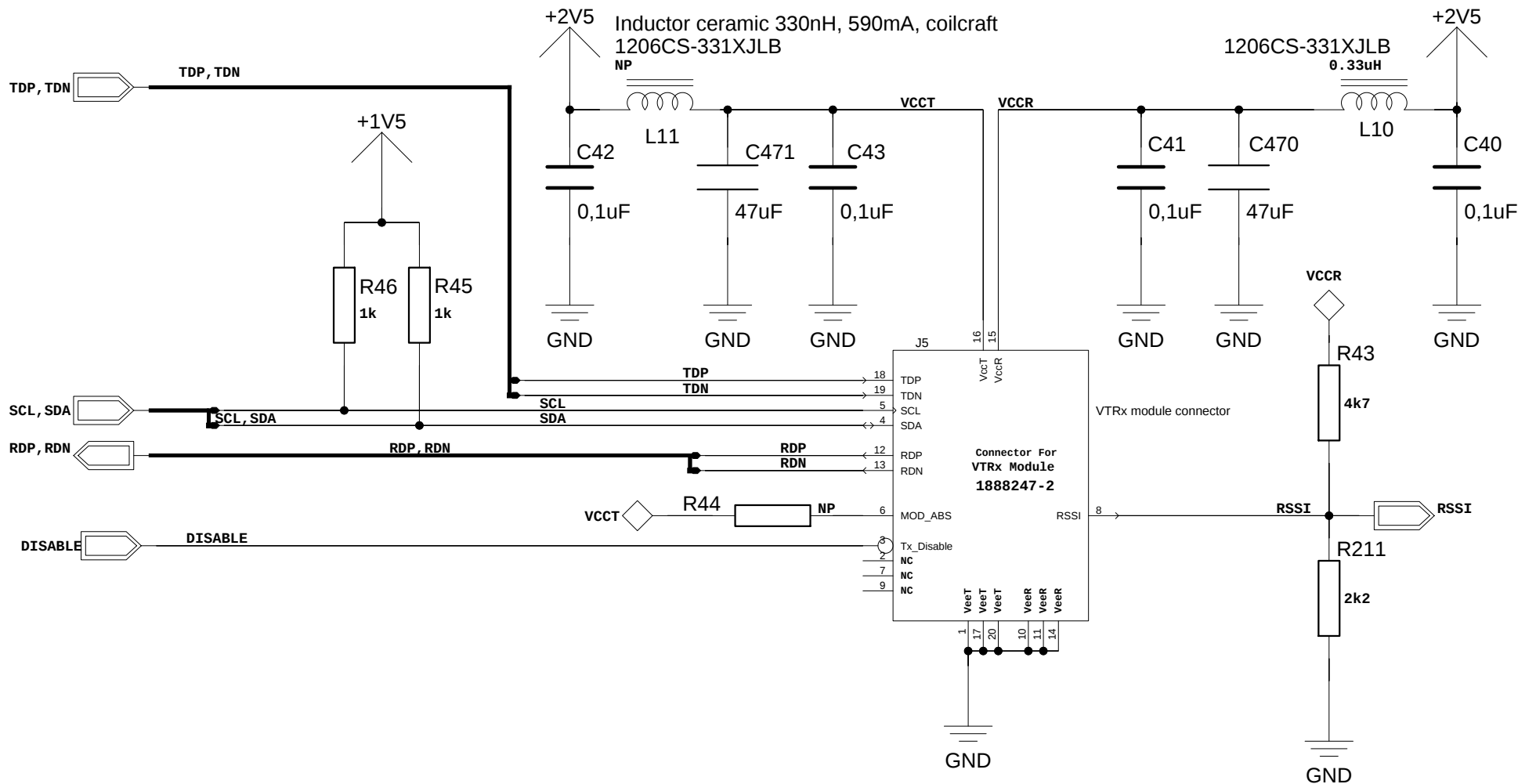




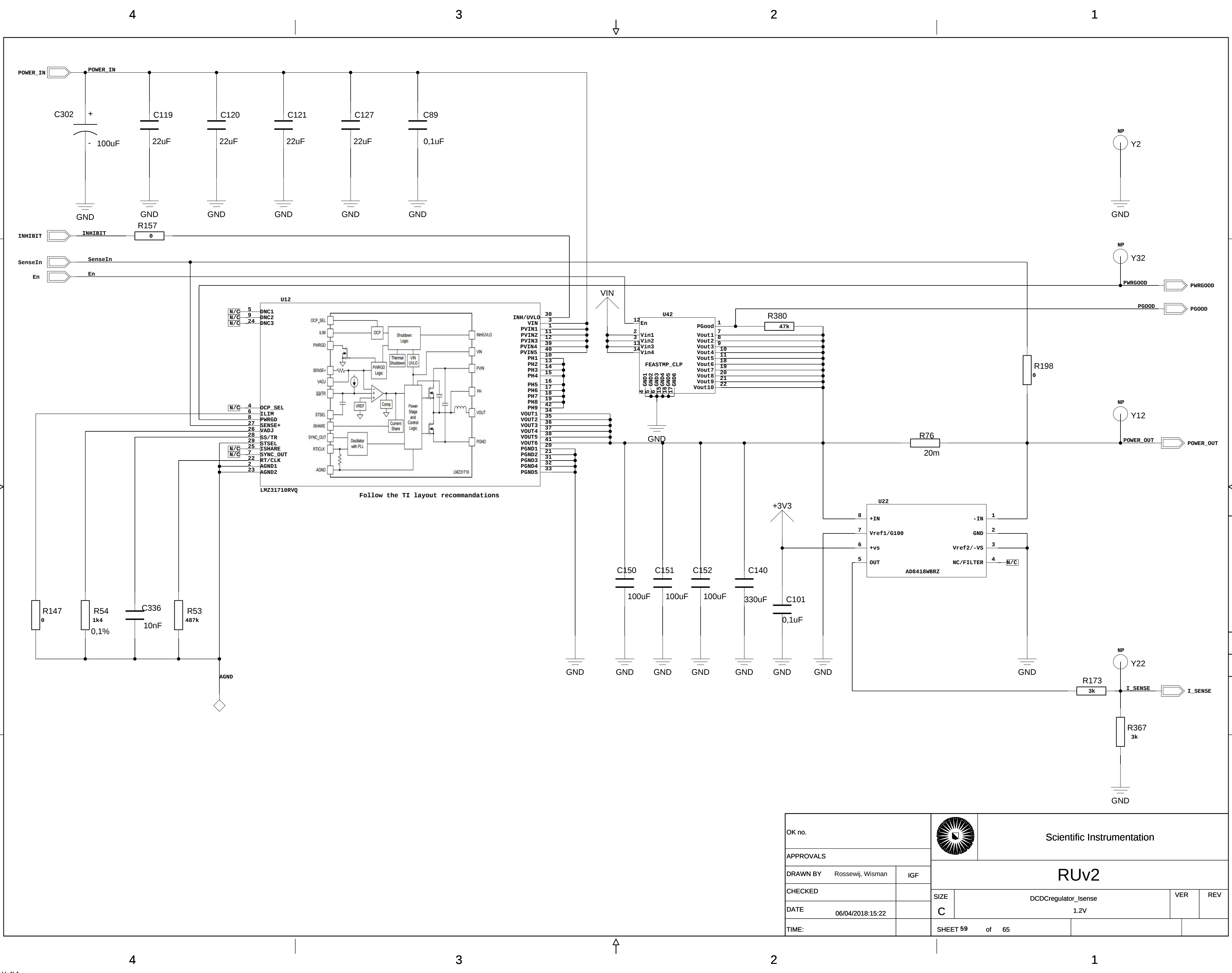
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APPROVALS			RUV2		
DRAWN BY	Rossewij, Wisman	IGF			
CHECKED		SIZE	USB3	VER	REV
DATE	06/04/2018:17:13	C	DATA		
TIME:		SHEET 53 of 65			



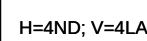
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APPROVALS				RUV2			
DRAWN BY Rossewrij, Wisman		IGF		SIZE C	USB3 POWER	VER	REV
CHECKED							
DATE 06/04/2018:17:40							
TIME:				SHEET 54 of 65			

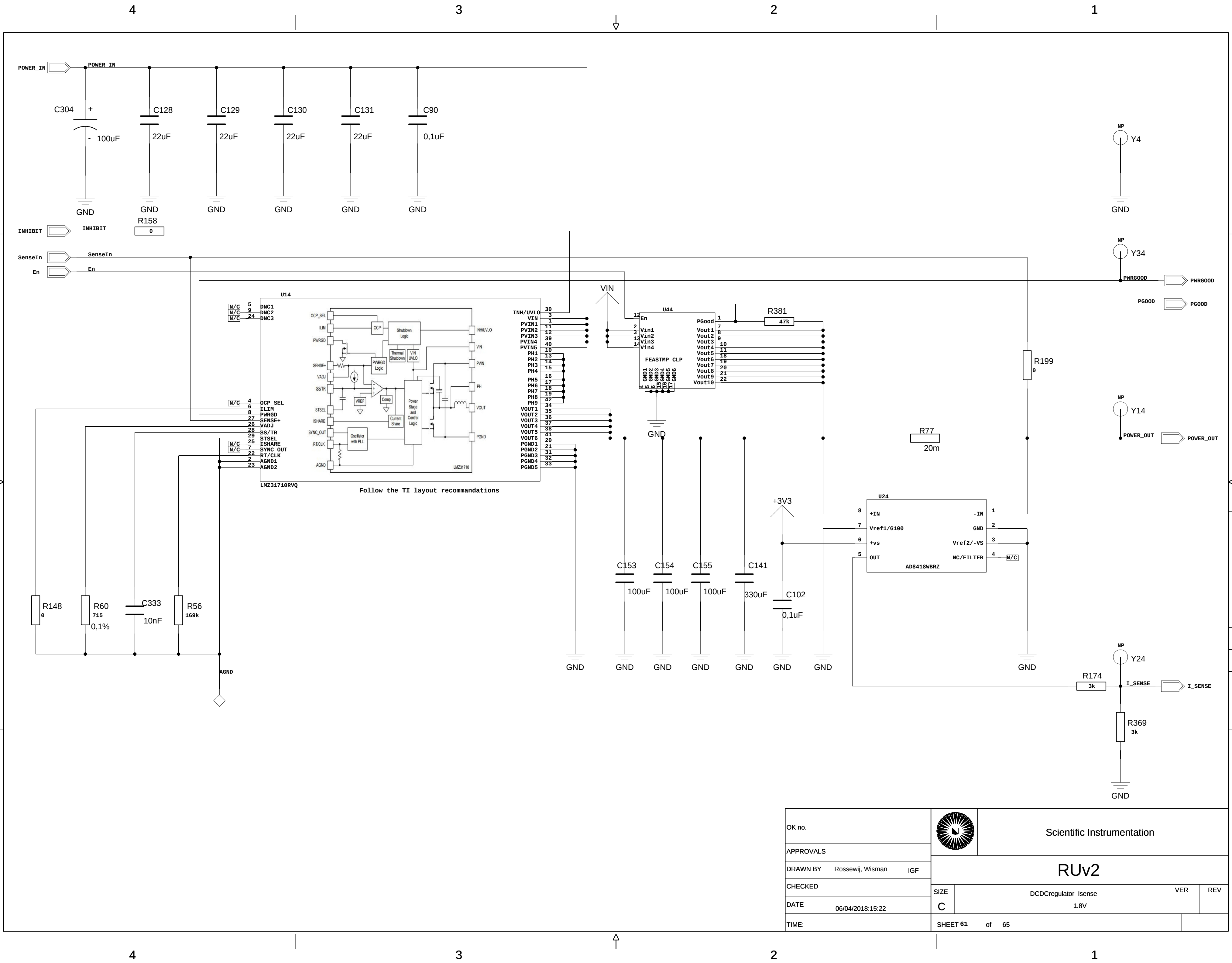


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APPROVALS			RUv2				
DRAWN BY	Rossewij, Wisman	IGF					
CHECKED			SIZE	VTRx2		VER	REV
DATE							
TIME:			SHEET 56 of 65				

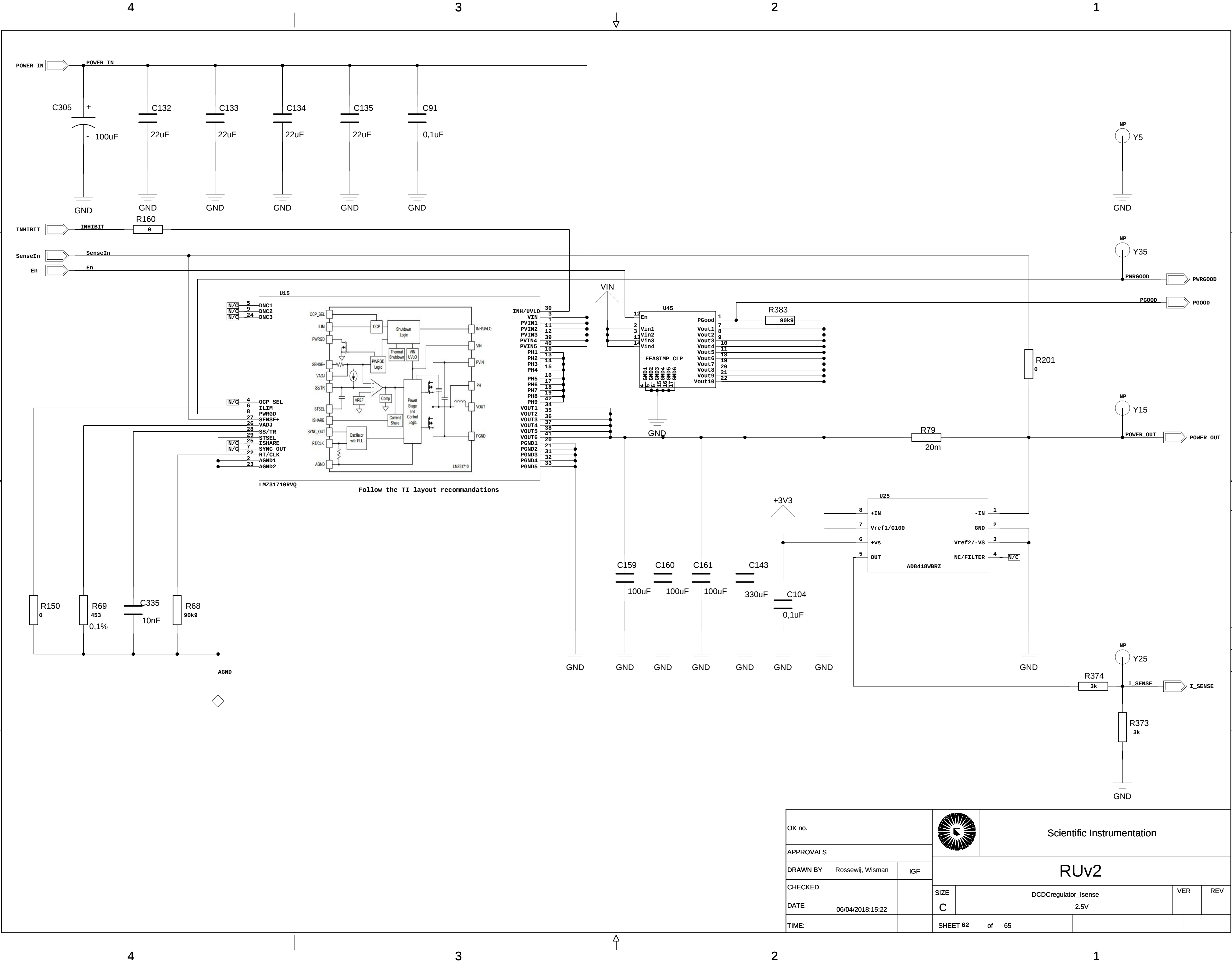


OK no.			Scientific Instrumentation				
APPROVALS							
DRAWN BY Rossewij, Wisman		IGF	RUv2				
CHECKED							
DATE 06/04/2018:15:22			SIZE C	DCDCregulator_1sense 1.2V		VER	REV
TIME:			SHEET 59 of 65				

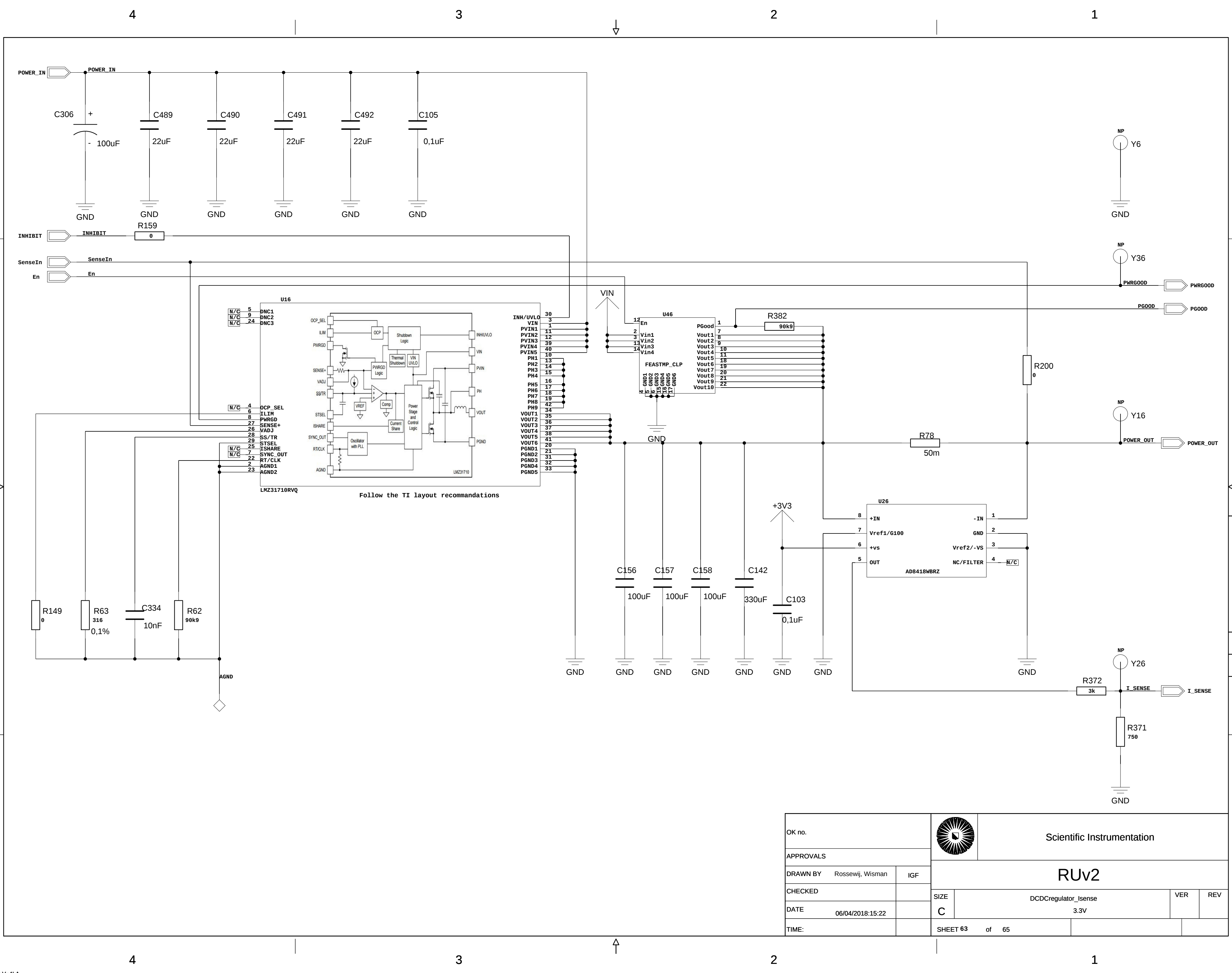




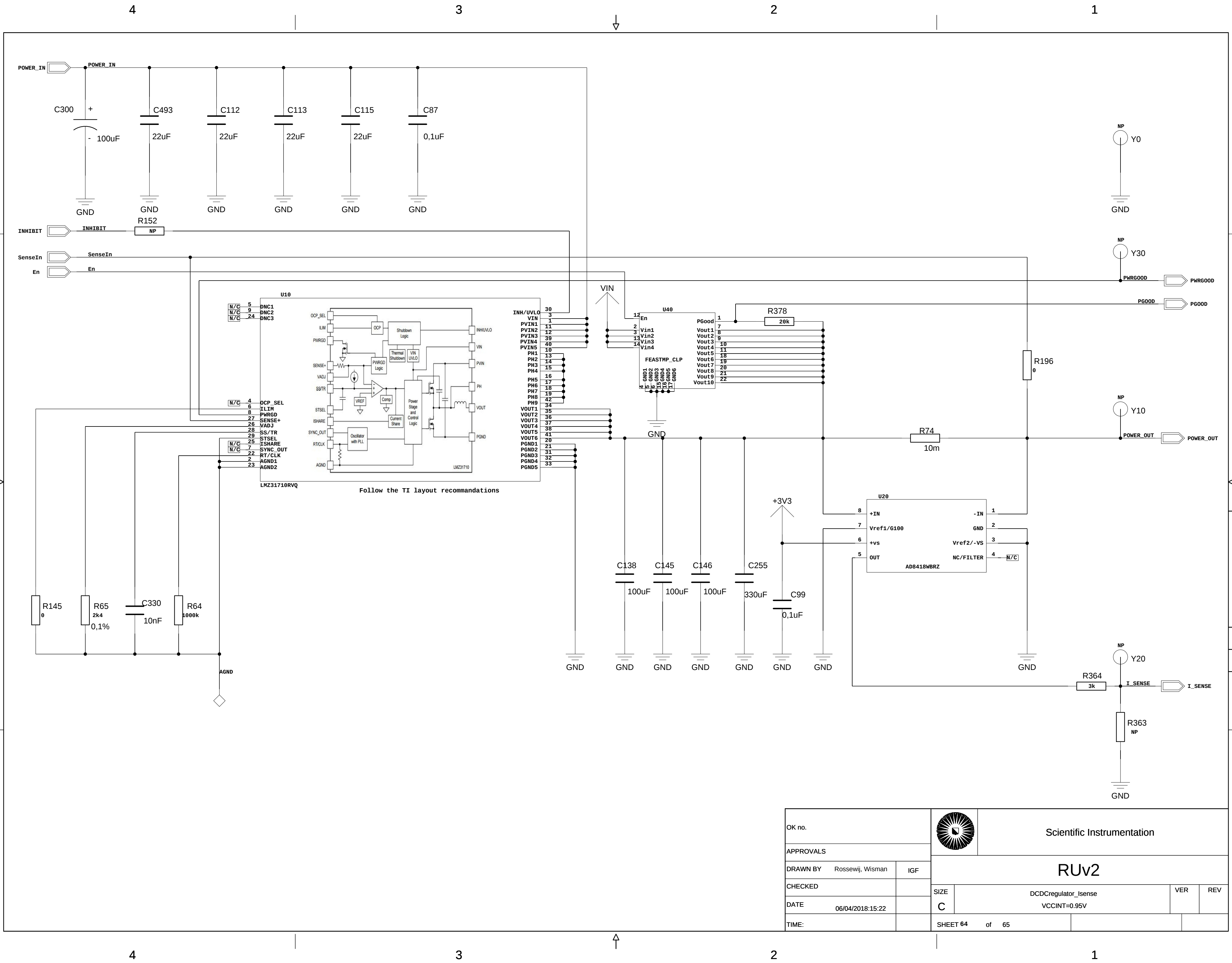
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APPROVALS						
DRAWN BY	Rossewij, Wisman	IGF	RUv2			
CHECKED						
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			C	1.8V		
TIME:			SHEET 61 of 65			



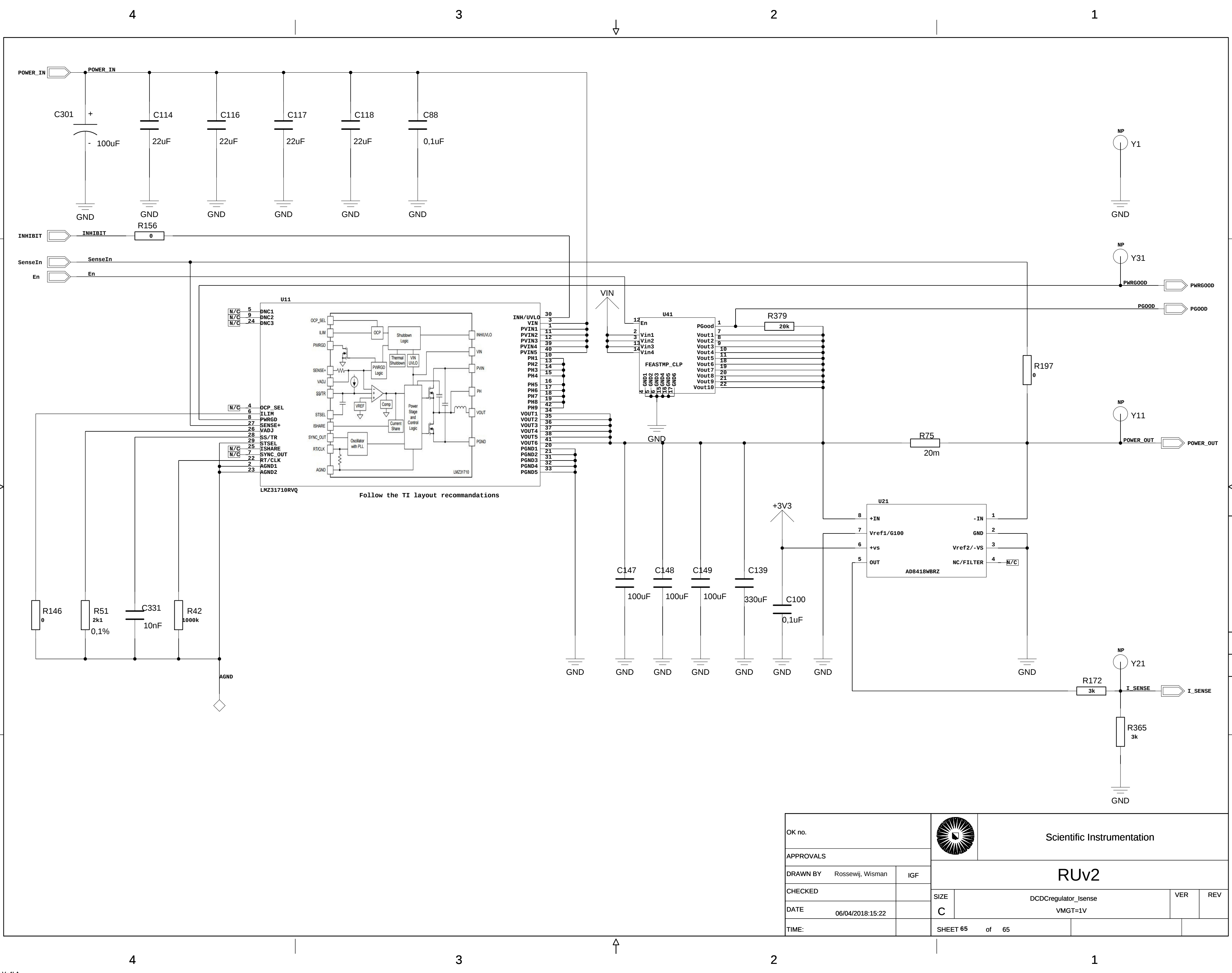
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CHECKED					SIZE		DCDCregulator_Isense		VER	REV	
DATE			06/04/2018:15:22		C		2.5V				
TIME:					SHEET 62			of 65			



OK no.			 Scientific Instrumentation		
APPROVALS			RUV2		
DRAWN BY	Rossewij, Wisman	IGF			
CHECKED					
DATE	06/04/2018:15:22				
TIME:					
			SIZE	DCDCregulator_Isense	VER
			C	3.3V	REV
			SHEET 63 of 65		



OK no.			Scientific Instrumentation		
APPROVALS			RUv2		
DRAWN BY	Rossewij, Wisman	IGF			
CHECKED		SIZE	DCDCregulator_Isense	VER	REV
DATE	06/04/2018:15:22	C	VCCINT=0.95V		
TIME:		SHEET 64 of 65			



OK no.			Scientific Instrumentation		
APPROVALS			RUV2		
DRAWN BY	Rossewij, Wisman	IGF			
CHECKED		SIZE	DCDCregulator_Isense	VER	REV
DATE	06/04/2018:15:22	C	VMGT=1V		
TIME:		SHEET 65 of 65			