PHLOM module signal assignment
6/16/09

	Daughtercard LVDS connector			
1	ET0	Out	MZ2	TrigOut
2	ET1	Out	MZ4	
3	ET2	Out	MZ5	ClkOut
4	ET3	Out	MZ6	CalStrobe

	DaughterCard NIM connector			
1	NIM0	In	MZ7	TrigIn
2	NIM1	In	MZ9	

	DaughterCard Internal LVDS Connector			
1	IN1	In	MZ10	Busy
2	IN2	In	MZ12	
3	IN3	In	MZ14	
4	IN4	In	MZ15	

	Daughtercard Mezzanine connector	
1	MZ0	-12V
3	GND	MZ1
5	MZ2	MZ3
7	MZ4	
9	MZ5	
11	GND	
13	MZ6	
15	MZ7	MZ8
17	MZ9	
19	MZ10	MZ11
21	MZ12	MZ13
23	MZ14	
25	MZ15	MZ16
27	MZ17	MZ18
29	MZ19	

	Mode Switch 0 Setting
0	Trigger Internal/External
1	CalStrobe Internal/External
2	
3	
4	
5	
6	
7	LED Blink Rate Fast/Slow

	Mode Switch 1 Setting
0	Internal Trigger Frequency Divider 0

1	Internal Trigger Frequency Divider 1
2	Internal Trigger Frequency Divider 2
3	Internal Trigger Frequency Divider 3
4	Internal Trigger Frequency Divider 4
5	Internal Trigger Frequency Divider 5
6	Internal Trigger Frequency Divider 6
7	Internal Trigger Frequency Divider 7

	ECL Output Port		
0	Xout0	Out	Trig
1	Xout1	Out	TrigIn
2	Xout2	Out	Busy
3	Xout30	Out	CalStrobe
4	Xout31	Out	TrigOut

	Differential Input Port		
0	Xin29	In	Busy
1	Xin30	In	CalStrobe
2	Xin31	In	TrigIn

	TTL VME P2 Port		
0	P2D00	Out	TrigOut-
1	P2D02		TrigOut+
2	P2D04		CalStrobe-
3	P2D06		CalStrobe+
4	P2D08		ClkOut-
5	P2D10		ClkOut+